Comprehensive study of crystalline AIN/sapphire templates after high-temperature annealing with various sputtering conditions

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Abstract: High-quality AlN/sapphire templates were fabricated by the combination of sputtering and high-temperature (HT) annealing. The influence of sputtering parameters including nitrogen flux, radio frequency power, and substrate temperature on the crystalline quality and surface morphology of annealed AlN films were investigated. With lower substrate temperature, lower power, and lower N₂ flux, the full width at half maximum of the X-ray rocking curve for AlN (0002) and ($10\bar{1}2$) were improved to 97.2 and 259.2 arcsec after high-temperature annealing. This happens because the increased vacancy concentration of sputtered AlN films can facilitate the annihilation of dislocations by increasing the recovery rate during HT annealing. Step and step-bunching morphologies were clearly observed with optimized sputtering conditions.

Key words: sputter; annealing; AIN; dislocation density

Citation: W Gu, Z B Liu, Y N Guo, X D Wang, X L Jia, X F Liu, Y P Zeng, J X Wang, J M Li, and J C Yan, Comprehensive study of crystalline AIN/sapphire templates after high-temperature annealing with various sputtering conditions[J]. *J. Semicond.*, 2020, 41(12), 122802. http://doi.org/10.1088/1674-4926/41/12/122802

1. Introduction

AIN is of great potential in the implantation of deep ultraviolet (DUV) emitters because of its wide bandgap (~6.2 eV), excellent UV transparency and high thermal conductivity^[1, 2]. Low threading dislocation density (TDD) is necessary to improve the quantum efficiency of UV light-emitting diodes (LEDs)^[3]. Recently, the bulk AIN substrate with a dislocation density (DD) of 10³ cm⁻² has been achieved^[4, 5]. However, the high impurity concentration and high cost has made it inferior to the AIN/sapphire template for commercial motivations. To date, the reported full width at half maximum (FWHM) values of (0002)- and (1012)-plane of AIN grown by MOCVD have been reduced to 79 and 206 arcsec^[6]. Meanwhile, due to the large lattice and thermal expansion coefficient mismatches between AIN and sapphire, AIN grown on sapphire by metal-organic chemical vapor deposition (MOCVD) system still suffers from the TDD higher than 10⁸ cm^{-2[7]}, although many methods have been proposed to reduce the TDD of AIN films grown on sapphire, such as periodical high/middle-temperature AIN growth method^[8], ELOG method^[9], the nano-patterned sapphire substrates (NPSS)^[10], and two-phase growth method^[11]. Moreover, the serious prereaction of trimethylaluminum (TMAI) and ammonia (NH₃)^[12],

Correspondence to: Z B Liu, zbliu@semi.ac.cn; J C Yan, yanjc@semi.ac.cn Received 29 MARCH 2020; Revised 8 MAY 2020. ©2020 Chinese Institute of Electronics the poor uniformity of temperature distribution in the chamber, and rapid consumption of heater at high temperature (HT) limit the yield of AIN films by MOCVD with small-size reactor and lead to the high cost of UVC LEDs. To improve the productivity and quality of AIN/sapphire template, and reduce the cost of UVC LEDs, a method that combines sputtering and HT annealing technologies outside MOCVD has been reported^[13, 14]. For example, the annealing of low temperature AIN buffer layer has shown remarkable improvement of crystalline guality and the FWHM values were 59 and 284 arcsec for the (0002)- and (1012)-plane^[15]. Besides, the FWHM values of (0002)- and (1012)-plane of annealed sputtered AIN were also reduced to 36 and 260 arcsec, respectively^[16]. The effect of annealing temperature and the thickness of AIN films sputtered with AIN targets on the sapphire substrate has been investigated^[17]. However, few reports concentrate on the sputtering parameters with AI target during depositing AIN, which has complicated formation mechanisms. In this work, the effects of sputtering parameters, including power, temperature, and N₂ flux, on the crystallinity evolution of annealed AIN films are systematically investigated. The correlations between the sputtering process and HT annealing are comprehensively discussed.

2. Experiment

In this experiment, AIN layers with a target thickness of 200 nm were deposited on 2-inch *c*-plane sapphire by radio frequency (RF) reactive magnetron sputtering. High-purity AI (> 99.9999 at%) was used as the target. The RF power was set

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Table 1. Sputtering parameters for the deposition of AIN films.

Sputtering parameter	Value
Target	AI (> 99.9999 at%)
Substrate	<i>c</i> -sapphire
Target to substrate distance	64.8 mm
Substrate temperature	550–700 °C
N ₂ flux	100–190 sccm
RF power	2000 and 3000 W
Process pressure	0.31 to 0.55 Pa
Deposition rate	0.20–0.32 nm/s
Targeted AIN thickness	200 nm

to 2000 and 3000 W. N₂ was used as the sputtering gas in the range of 100-180 sccm, and the substrate temperature was changed from 550 to 700 °C. The chamber background pressure was lower than 3.2×10^{-5} Pa, and the process pressure was varied from 0.31 to 0.55 Pa as the increase of N₂ flux. The deposition rate of the sputtered particles was in the range of 0.20-0.35 nm/s, which is mainly determined by RF power^[18]. These parameters and characterizations of sputtered AIN are presented in Tables 1 and 2, respectively. Subsequently, the sputtered AIN was annealed in N₂ at 1700 °C for 3 h. During the annealing process, the pressure was kept at the atmosphere and the "face to face" method was applied to suppress AIN decomposition^[19]. The crystallinity of sputtered and annealed AIN films was investigated by Bede D1 X-ray diffraction (XRD). The FWHM of (0002)- and (1012)-plane were adopted to evaluate the tilt and twist components. The surface morphology evolution was determined by Veeco Dimension 3100 atomic force microscopy (AFM). Raman spectroscopy measurement was performed to analyze the stress state.

3. Results and discussion

3.1. The effect of substrate temperature

Fig. 1 shows the (0002)- and (1012)-plane XRCs of the 200-nm AIN films sputtered at 2000 W and 600 °C with a N₂ flux of 100 sccm before and after annealing. Before annealing, the (0002)-plane XRC of sputtered AIN films shows a distinct peak in Fig. 1(a) and the FWHM value is as low as 268 arcsec. A side peak is also observed, which implies the incoherent crystallographic orientation of the sputtered AIN. Due to the low deposition temperature during sputtering, the $(10\overline{1}2)$ -plane XRC in Fig. 1(b) shows a board peak feature and the FWHM value is 2915 arcsec. These FWHM values indicate that the orientations of sputtered AIN films are composed of low tilt component and high twist component. After HT annealing, the side peak of (0002)-plane disappear. Moreover, the FWHM values of (0002)- and (1012)-plane are dramatically reduced to 97.2 and 259.2 arcsec. The remarkable crystallinity improvement of AIN films after HT annealing is due to the higher recovery rate or further reduction in the dislocation density, which happens because the high vacancy concentration in sputtered AIN films or a faster diffusion process improves dislocation climb during annealing^[20, 21].

To better comprehend the impact of substrate temperature on the crystallinity of annealed AlN films, AlN films with various substrate temperatures are applied. Fig. 2 shows the effect of substrate temperature on the FWHM values of $(10\overline{1}2)$ -plane of the annealed AlN films sputtered at 550, 600,



Fig. 1. (Color online) The (a) (0002)- and (b) ($10\overline{1}2$)-plane XRCs of the AIN films sputtered at 2000 W, 600 °C and 100 sccm N₂ before and after HT annealing.

650, 700 °C with a N₂ flux of 100 sccm. For the annealed AlN films which are sputtered at 3000 W, the FWHM values of (1012)-plane are gradually reduced from 367.2 to 313.2, 284.4, 280.8 arcsec as the reduction of substrate temperature. Meanwhile, the FWHM values of (1012)-plane of the annealed AlN films sputtered at 2000 W are also reduced from 356.4 to 273.6, 259.2 arcsec as the substrate temperature decreases from 700 to 600 °C. However, the FWHM value of (1012)-plane increases to 280.8 arcsec for the annealed AlN films sputtered at 550 °C and 2000 W. Besides, the FWHM values of (0002)-plane of all the annealed AlN films are reduced to lower than 100 arcsec and are similar (not shown here), indicating the great elimination of the tilt component.

The relationship between the FWHM values of (1012)plane of the annealed AIN and the substrate temperature are studied by the tilt components of sputtered AIN films obtained from the (0002)-plane XRC and Raman measurement. As shown in Fig. 3(a), for the AIN films sputtered at 2000 W, the (0002)-plane XRC of AIN film deposited at 550 °C has two side peaks. It is because that this lower substrate temperature provides insufficient energy for AIN deposition and causes more tilt component^[22]. This lower sputter temperature also leads to higher vacancy concentration during sputtering. It is possible that the more tilt component or the too much vacancy prevents the vacancies from diffusion and stop dislocation climb. Thus, the crystallinity of the annealed AIN film sputtered at 2000 W and 550 °C deteriorates. As the substrate temperature increases from 600 to 700 °C, only one side peak exists and tends to disappear and the FWHM value

Sputtering condition Dep	Deposition rate (nm/s)	Actual thickness (nm)	FWHM values of XRC (arcsec)		DMS (pm)
	Deposition rate (mm/s)		(0002)-plane	(10ī2)-plane	— כועוא (וווו)
2000 W, 100 sccm, 550 °C	0.210	213.77	360	3229	0.852
2000 W, 100 sccm, 600 °C	0.216	219.72	270	2941	1.510
2000 W, 100 sccm, 650 °C	0.233	237.42	191	2714	2.200
2000 W, 100 sccm, 700 °C	0.197	200.70	130	2491	0.988
3000 W, 100 sccm, 550 °C	0.330	207.93	184	3301	0.850
3000 W, 100 sccm, 600 °C	0.340	214.19	241	2923	0.995
3000 W, 100 sccm, 650 °C	0.346	218.17	205	2664	1.060
3000 W, 100 sccm, 700 °C	0.316	199.31	119	2584	0.955
2000 W, 150 sccm, 600 °C	0.206	209.28	371	2776	1.860
2000 W, 180 sccm, 600 °C	0.218	220.40	349	2743	1.930
3000 W, 150 sccm, 600 °C	0.315	198.23	292	2966	1.970
3000 W, 180 sccm, 600 °C	0.322	202.65	410	3049	2.010

Table 2. Characterizations for the sputtered AIN films.



Fig. 2. (Color online) The FWHM values of $(10\overline{1}2)$ -plane of the annealed AIN films with different substrate temperatures at 2000 and 3000 W.

is reduced, which is attributed to the enhanced surface migration of Al atoms as the increase of substrate temperature^[23]. As a result, the tilt component of the sputtered AlN shrinks, and the vacancy concentration is reduced. In this case, the reduced vacancy concentration is the key point to prevent dislocation climb. Therefore, the FWHM value of (1012)-plane of the annealed AlN increases with increasing sputter temperature and has the minimum value of 259.2 arcsec at 600 °C. In the 3000 W case, the FWHM values of (1012)-plane of the annealed AlN increases with increasing sputter temperature because of less vacancy density. However, with the substrate temperature increases in Fig. 3(b), which means the more tilt component. Further studies are expected to explain this phenomenon.

Figs. 4(a) and 4(b) show the Raman spectrum of AlN films sputtered at various temperatures before annealing. The $E_2^{(high)}$ peak frequency is presented in Fig. 4(c). The peak intensity of strain-free AlN films was reported at the phonon frequency of 657.4 cm^{-1[24]}. The smaller (larger) peak frequency indicates a larger tensile (compressive) stress. Before annealing, the $E_2^{(high)}$ peak frequency of AlN films sputtered at 2000 W is gradually increased from 654.686 to 660.488 cm⁻¹ as the substrate temperature increases. This means that the stress state of sputtered AlN films can be modulated from tensile to compressive stress by increasing the substrate temperature^[25]. It is possibly because of the less vacancy density and disloca-

tion density with higher sputter temperature. The 3000 W case has the similar tendency. However, the stress becomes from tensile to compressive at 600 °C because of less vacancy density and less dislocation density than the 2000 W case. After HT annealing, all the $E_2^{(high)}$ peak frequencies of AIN films are higher than 657.4 cm⁻¹ and similar, implying all AIN films have compressive stress after annealing due to the lattice and thermal expansion mismatch between sapphire and AIN in the recrystallization process^[26].

Fig. 5 shows AFM images (5 \times 5 μ m²) of the AIN films sputtered at 2000 W and a N₂ flux of 100 sccm with different substrate temperatures before and after HT annealing. The surface morphologies of the sputtered AIN films show high-density and uniform column structures in Fig. 5(a)^[17]. The diameter and altitude of the columns are increased as the substrate temperature increases from 550 to 650 °C (increased size and number of white dots on the surfaces in these images), which is also demonstrated by the enlarged root mean square (RMS) from 0.852 to 2.20 nm. This may happen because the high substrate temperature will promote the deposition rate and increase the grain size of the AIN column^[23]. However, as the substrate temperature further increases to 700 °C, the surface morphology is improved with small-size columns and RMS is reduced to 0.988 nm possibly because the reduced deposition rate and the increased nucleation density at the very start of sputtering. The 3000 W case has a similar trend (not shown here). After HT annealing, the column structures coalesce and a few pits (defects marked in the images) are observed, which were also mentioned in previous work^[17]. For the annealed AIN films with low substrate temperature in Figs. 5(e) and 5(f), step-bunching morphology is formed after annealing, and sparse high-altitude columns still exist on the surface and decreases with increasing substrate temperature. When the substrate temperature increases to 700 °C, step morphologies with the RMS of 0.275 nm appear free of high-altitude columns in Fig. 6(h). All of the surfaces of samples with 3000 W after HT annealing have step morphologies (not shown here). The significant improvement of surface morphology after HT annealing is attributed to the coalescence of columnar domains and the annihilation of domain boundaries^[17].

3.2. The effect of N₂ flux

 N_2 has two functions during the sputtering process, in-



Fig. 3. (Color online) The (0002)-plane XRCs of the AIN films sputtered at (a) 2000 and (b) 3000 W with various substrate temperatures before HT annealing.



Fig. 4. (Color online) Raman spectrum of the AIN films sputtered at (a) 2000 and (b) 3000 W, a N₂ flux of 100 sccm, and various substrate temperatures before annealing. (c) The *E*₂^(high) peak frequency of sputtered and annealed AIN films with different substrate temperatures.

volving that (1) N₂ is ionized as N source to bombard the Al target and form AlN and (2) N₂ acts as environment gas and collides with sputtered particles. To reveal the effect of N₂ flux during the sputtering process on the crystallinity of annealed AlN films, three different N₂ fluxes of 100, 150, 180 sccm are used to deposit AlN at 600 °C. Fig. 6 shows the FWHM values of (1012)-plane for these sputtered AlN films after HT annealing. For the annealed AlN films sputtered at 2000 W, the FWHM values of (1012)-plane are increased from 259.2 to 342 arcsec as the increase of N₂ flux. Meanwhile, in 3000 W case, the FWHM values of (1012)-plane of the annealed AIN films also increases from 284.4 to 360 arcsec along with the increase of the N₂ flux. All the FWHM values of (0002)-plane are reduced to around 97.2 arcsec after annealing.

To further analyze the crystallinity evolution of annealed AIN films with the increase of N_2 flux during sputtering, the tilt component of sputtered AIN films is investigated. Fig. 7



Fig. 5. (Color online) $5 \times 5 \ \mu \text{m}^2$ AFM images of the AIN films sputtered at 2000 W, a N₂ flux of 100 sccm and substrate temperatures of (a) 550, (b) 600, (c) 650 and (d) 700 °C before annealing and (e) 550, (f) 600, (g) 650 and (h) 700 °C after annealing.



Fig. 6. The effect of N₂ flux on the FWHM values of ($10\overline{1}2$)-plane of the annealed AIN films.

shows the (0002)-plane XRCs of the AIN films sputtered with various N₂ fluxes at 600 °C, 2000 W and 3000 W before annealing. For the AIN films sputtered at 2000 W, all samples have side peaks. Furthermore, the normalized intensity of side peaks decrease with decreasing N₂ flux, which means the less tilt component. The 3000 W case has a similar tendency. During sputtering, the process pressure is decreased as the decrease of N₂ flux. This will result in a longer mean free path of particles, which means that sputtered AI particles from AI target have less possibility to collide with N₂ molecules from target to substrate. Thus, less energy of the sputtered Al will be lost in this process and AIN will deposit on the surface with higher energy, which will lead to less tilt component. This lower tilt component will possibly improve the vacancy diffusion. Hence, the FWHM values of (1012)-plane are reduced as the decrease of N₂ flux mainly due to the less tilt component.

Fig. 8 presents AFM images (5 × 5 μ m²) of the sputtered AlN films sputtered at 2000 W, 600 °C with various N₂ fluxes before and after HT annealing. Before annealing, the size and altitude of column structures are increased as the increase of N₂



Fig. 7. (Color online) The (0002)-plane XRCs of the AIN films sputtered at (a) 2000 and (b) 3000 W before annealing with various N₂ fluxes.

flux and the RMS values are increased from 1.51 to 1.93 nm. After HT annealing, step-bunching structures are formed due to the coalescence of column structures. Moreover, high-altitude columns tend to disappear with the increase of N₂ flux. However, lots of voids are observed for the annealed AIN films with a N₂ flux of 180 sccm, which are ascribed to the re-

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Fig. 8. (Color online) AFM images of the AIN films sputtered at 2000 W and 600 °C with N₂ fluxes of (a) 100, (b) 150 and (c) 180 sccm before annealing and (d) 100, (e) 150 and (f) 180 sccm after annealing.

mained residual spaces after the coalescence of domains^[17].

3.3. The effect of RF power

The effect of RF power can be analyzed by comparing the 2000 and 3000 W cases in the previous parts. Fig. 3 and Fig. 7 show that the 3000 W case has narrower (0002)-plane XRC of sputtered AlN films than 2000 W case with the same other sputtering conditions. It is because the higher RF power provides high kinetic energy for AlN during sputtering which lead to less tilt component. However, the FWHM values of (1012)-plane of annealed 3000 W samples are higher than that of 2000 W samples, as shown in Figs. 3 and 7, possibly because of less vacancy density. In addition, the surface morphology could maintain the step and step-bunching structures.

By further reducing the RF power to 1000 W, the color of sputtered AlN films will change from transparent to light yellow due to the lower deposition energy (not shown here). The FWHM values of $(10\overline{1}2)$ -plane of the annealed AlN sputtered at 1000 W become higher than that sputtered at 2000 W because of the more title component (not shown here).

4. Conclusion

The effect of sputtering parameters on the annealed AIN films is comprehensively studied. The high-quality annealed AIN films are obtained at lower substrate temperature, lower N₂ flux, and lower RF power. The lower substrate temperature and lower RF power provide less energy for AIN deposition during sputtering, which leads to the increased tilt component and high vacancy concentration. A high vacancy will increase dislocation annihilation by improving dislocation climb during HT annealing. However, a vacancy concentration that is too high or too much of a tilt component will prevent the vacancy from diffusion or stop the dislocation climb. The transformation from compressive stress to tensile stress of sputtered AIN films may also be useful for AIN columns to

twist. Lower N₂ flux reduces the energy loss by decreasing the collision possibility of sputtered AI particles and N₂ molecules, and thus improves the tilt component, which will improve vacancy diffusion. Finally, the FWHM of the (0002) and (1012)-plane of AIN films after annealing are improved to 97.2 and 259.2 arcsec at the substrate temperature of 600 °C, a N₂ flux of 100 sccm and a RF power of 2000 W, corresponding to the screw and edge-type TDD of 2.06 × 10⁷ and 7.52 × 10⁸ cm⁻², respectively. By modulating these sputtering parameters, step and step-bunching morphologies with a very low RMS are achieved. We expect to further reveal the crystallinity evolution of annealed AIN with various sputtering conditions in the future.

Acknowledgements

This work was supported by the National Key R&D Program of China (Nos. 2016YFB0400800 and 2017YFB0404202), the National Natural Sciences Foundation of China (Grant Nos. 61527814, 61674147, 61904176, U1505253), Beijing Nova Program Z181100006218007 and Youth Innovation Promotion Association CAS 2017157.

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