# Analytical model for the effects of the variation of ferrolectric material parameters on the minimum subthreshold swing in negative capacitance capacitor

### Raheela Rasool<sup>†</sup>, Najeeb-ud-Din, and G. M. Rather

Department of Electronics and Communication, National Institute of Technology, Srinagar, Jammu & Kashmir, India

**Abstract:** In this paper, we analytically study the relationship between the coercive field, remnant polarization and the thickness of a ferroelectric material, required for the minimum subthreshold swing in a negative capacitance capacitor. The interdependence of the ferroelectric material properties shown in this study is defined by the capacitance matching conditions in the subthreshold region in an NC capacitor. In this paper, we propose an analytical model to find the optimal ferroelectric thickness and channel doping to achieve a minimum subthreshold swing, due to a particular ferroelectric material. Our results have been validated against the numerical and experimental results already available in the literature. Furthermore, we obtain the minimum possible subthreshold swing for different ferroelectric materials used in the gate stack of an NC-FET in the context of a manufacturable semiconductor technology. Our results are presented in the form of a table, which shows the calculated channel doping, ferroelectric thickness and minimum subthreshold for five different ferroelectric materials.

Key words: NC-capacitor; ferroelectrics; subthreshold swing; negative capacitance

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### 1. Introduction

Negative capacitance field effect transistor (NC-FET) was proposed in 2008 by Salahuddin et al.<sup>[1]</sup> to improve the subthreshold slope of the MOSFET. It has been experimentally proven by Asif *et al.* in Ref. [2]. Although many technologies<sup>[3, 4]</sup> before it were suggested to improve the subthreshold slope of the MOSFET, they had inherent disadvantages that first had to be overcome. The NC-FET reduces subthreshold swing (SS) below Boltzmann's limit, due to the use of ferroelectric materials. The negative capacitance property of these materials, which actually reduces the SS, is entirely material and process dependent as described in phenomenological Landau-Devonshire theory<sup>[5]</sup>. In an NC-FET, any ferroelectric material needs to be integrated directly within the gate stack of a conventional CMOS device and its properties determine the characteristics of the device. These materials are highly process and temperature dependent, and they can switch their properties from one state to another under certain conditions. The coercive field ( $E_c$ ) and remnant polarization ( $P_0$ ) of the material and process temperature determine the Landau parameters in Landau-Khalatnikov (LK)<sup>[6, 7]</sup> equation described in the theory part of the paper.

In this paper, we have obtained an analytical equation to determine the thickness of the ferroelectric material required to get minimum subthreshold swing, under capacitance matching condition. The limit on minimum possible subthreshold swing ( $S_{min}$ ) reflects the choice of underlying MOS capacit-

ance ( $C_{MOS}$ ) and ferroelectric capacitance ( $C_{FE}$ ), which is fundamentally dictated by the need to stabilise the total NC capacitor capacitance. In section 2, we describe the fundamental capacitance constraints that define the minimum subthreshold swing in an NC capacitor. In section 3, we determine the optimal thickness of the ferroelectric material and substrate doping required to observe the NC effect for a minimum subthreshold swing. In section 4, we discussed the dependence of ferroelectric thickness on the coercive field and remnant polarization. Finally, in section 5 we obtain the equation for  $S_{min}$ and corresponding  $S_{min}$  for five different ferroelectric materials.

### 2. Fundamental considerations

For the analytical analysis of our model, we have considered a basic MOS capacitor with an extra ferroelectric layer in the gate stack. A schematic of a Metal-Ferroelectric-Metal-Insulator-Substrate (MFMIS) negative capacitance capacitor<sup>[8]</sup> and its corresponding capacitance equivalent formed between the ferroelectric and the dielectric layer is shown in Figs. 1(a) and 1(b), respectively. The  $V_{GS}$  is the applied gate voltage and V<sub>GMOS</sub> is the internal node voltage formed between the ferroelectric and the dielectric layer. Because our study examines the properties of a ferroelectric layer, we are not going to examine the electrical characteristics of the NC-FET and will instead limit our study to the capacitor only. The subthreshold swing (SS) of a MOSFET device is defined as the change in gate voltage ( $V_{GS}$ ) required to change the drain current  $(I_D)$  by one order of magnitude<sup>[9]</sup>. For the negative capacitance MOS capacitor equivalent shown in Fig. 1(b), SS is given as<sup>[9]</sup>

Correspondence to: R Rasool, raheela\_03phd14@nitsri.net Received 13 FEBRUARY 2019; Revised 14 JUNE 2019. ©2019 Chinese Institute of Electronics



Fig. 1. (a) Schematic of MFMIS based NC-FET. (b) Equivalent capacitance divider.

$$SS \equiv \frac{\partial V_{GS}}{\partial (\log_{10} I_{D})} = \frac{\partial V_{GS}}{\partial V_{GMOS}} \times \frac{\partial V_{GMOS}}{\partial (\log_{10} I_{D})} = m \times n.$$
(1)

The SS can be reduced by reducing any of the two *m* or *n* factors. In the case of the NC capacitor, we are only concerned with the reduction of *m* to decrease the SS. The factor,  $n = \frac{\partial V_{\text{GMOS}}}{\partial (\log_{10} I)} = \frac{2.3k_{\text{B}}T}{q}$  ( $k_{\text{B}}$  is Boltzmann's constant, *T* is temperature and *q* is electron charge) is known as transport factor and is  $\approx 60 \text{ mV/decade at room temperature.}$ 

The unique property of NC effect in an MFMIS structure is to provide an internal voltage ( $V_{GMOS}$ ) that is greater than the applied gate voltage ( $V_{GS}$ ). From the capacitor divider formed in Fig. 1(b), the partial derivative of the  $V_{GS}$  with respect to  $V_{GMOS}$ , also known as body factor *m*, can be written as

$$m = \frac{\partial V_{\rm GS}}{\partial V_{\rm GMOS}} = 1 + \frac{C_{\rm MOS}}{C_{\rm FE}},$$
 (2)

where  $\frac{1}{C_{MOS}} = \frac{1}{C_S} + \frac{1}{C_{OX}}$  is the series sum of substrate capa-

citance ( $C_S$ ) and oxide capacitance ( $C_{OX}$ ) capacitances.

To include the model for the dynamics of ferroelectric capacitor, the Landau Khalatnikov (LK) equation<sup>[6, 7]</sup> that describes the correlation between the polarization and the electric field in a single domain ferroelectric material is employed. The voltage drop across the ferroelectric layer is given as

$$E = \frac{V_{FE}(Q)}{t_{FE}} = 2\alpha Q + 4\beta Q^3 + 6\gamma Q^5, \qquad (3)$$

where  $\alpha$ ,  $\beta$ ,  $\gamma$  are known as Landau parameters of the material. Furthermore,  $\alpha < 0$ ,  $\beta > 0$ ,  $\gamma \ge 0$  defines the negative capacitance property,  $V_{\text{FE}}$  is the voltage drop across the ferroelectric film and  $t_{\text{FE}}$  is the thickness of the ferroelectric layer. The total charge Q is the same as the total charge in the channel, which is calculated using the depletion width approximation and has already explained in Ref. [10]. The potential balance in the equivalent capacitor network results in applied gate voltage  $V_{\text{GS}}$  as the sum of the voltage drop across the ferroelectric capacitor ( $V_{\text{FE}}$ ) and the internal node voltage  $V_{\text{GMOS}}$  is giv-

en as

$$V_{\rm GS} = V_{\rm FE} + V_{\rm GMOS},\tag{4}$$

where  $V_{GMOS} = V_{OX} + \psi_S + V_{FB}$  and where  $V_{FB}$  is the flatband voltage,  $V_{OX}$  is the voltage across the oxide layer between ferroelectric and silicon substrate and  $\psi_S$  is the surface potential or band bending in the semiconductor. The capacitance  $C_{FE}$  is the differential capacitance of ferroelectric layer and is defined  $dQ/dV_{FE}$ . From the LK equation described in Eq. (3), the inverse of  $C_{FE}$  is obtained as

$$C_{\mathsf{FE}}^{-1}(Q) = \left(2\alpha + 12\beta Q^2 + 30\gamma Q^4\right) t_{\mathsf{FE}}.$$
 (5)

The total gate capacitance  $C_{\rm T}$  of the NC capacitor is given by

$$C_{\rm T}^{-1}(Q) = C_{\rm MOS}^{-1}(Q) + C_{\rm FE}^{-1}(Q).$$
(6)

The increased gate capacitance due to  $C_{FE}$  of the ferroelectric layer in the gate stack, amplifies the internal node voltage. However,  $C_T$  must be positive for all charges for a hysteresis free stable operation of the device<sup>[11-13]</sup>. This stability requirement puts a fundamental constraint on  $C_{MOS}$ ; i.e.,

$$C_{MOS}(Q)^{-1} \ge |C_{FE}^{-1}(Q)|,$$
 (7)

which should hold throughout the NC regime. Eq. (7) puts a minimum limit on the subthreshold swing that can be obtained in an NC-FET. For a given dependence of  $C_{MOS}$  and  $C_{FE}$  on Q, minimum SS can be obtained where the two capacitances match as closely as possible<sup>[12, 14]</sup>. However, the two cannot be made equal because they have different dependence on the value of Q and it is not possible to obtain the same values for all of the values of Q.

For a fixed  $C_{\text{OX}}$ ,  $C_{\text{MOS}}$  changes with  $C_{\text{S}}$  which depend on the channel doping  $N_{\text{A}}$ ; however,  $C_{\text{S}}$  depends on whether the channel is in subthreshold, where  $C_{\text{S}}(Q) \propto \frac{N_{\text{A}}}{Q}$ , or in inversion, where  $C_{\text{S}}(Q) \propto Q$ .

Unlike conventional MOSFETs, where the minimum value of SS is fixed at 60 mV/dec, there is no such single value in NC-FET. The minimum subthreshold swing ( $S_{min}$ ) that character-

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Fig. 2. Voltage-charge plot for a ferroelectric material.

izes the NC-FET is different for different devices. However, the two necessary conditions for minimum subthreshold swing are a)  $C_{\text{MOS}}(Q) \leq -C_{\text{FE}}(Q)$  and b) channel should be in sub-threshold throughout the NC regime<sup>[15]</sup> remain the same.

Mathematically, the condition for being in subthreshold is given by  $\psi_{\rm s} < 2\psi_{\rm B} = \frac{2k_{\rm B}T}{q} \ln\left(\frac{N_{\rm A}}{n_{\rm i}}\right)$  (where  $\psi_{\rm s}$  is the surface potential,  $\psi_{\rm B}$  is the built-in potential on p-substrate,  $N_{\rm A}$  is the

substrate doping and  $n_i$  is the intrinsic carrier concentration), throughout the NC-regime. For uniform substrate doping and the partially depleted case,  $\psi_S$  is related to the depletion charge Q by<sup>[9]</sup>

$$\psi_{\rm S} = \frac{Q^2}{2q\varepsilon_0\varepsilon_{\rm s}N_{\rm A}},\tag{8}$$

where  $\varepsilon_0$  is the permittivity of free space and  $\varepsilon_s$  is the permittivity of silicon substrate. The condition for subthreshold re-

gion translates Eq. (8) into  $l\psi_{\rm B} = \frac{Q^2}{2q\varepsilon_0\varepsilon_8 N_{\rm A}}$ , where l < 2.

Condition (7) is general and will work irrespective of values of  $C_{MOS}$  and  $|C_{FE}|$ . However, Eq. (8) changes as we change the FET structure, such as single gate versus multigate<sup>[16]</sup> or bulk FET versus depleted FET.

### 3. Optimal ferroelectric thickness

Fig. 2 shows the  $V_{FE}-Q$  characteristics of the ferroelectric material.  $V_{FE}$  decreases in the range of  $-Q_1 < Q < +Q_1$  and hence  $C_{FE}$  is also negative between  $-Q_1$  and  $+Q_1$ . However, an n-channel NC-FET must be operated in the Q > 0 branch of the NC regime, so that it can be balanced by the negative charges of the channel. Therefore,  $Q_2 = 0$  is defined as the other boundary of the negative capacitance regime. At  $Q_2 = 0$ , Eq. (4) reduces to  $C_{FE}(Q) = \frac{1}{2at_{FE}}$  and at flatband capacitance of the underlying MOS capacitance is given by<sup>[17]</sup>

$$\frac{1}{C_{\rm MOS}} = \sqrt{\frac{v_{\rm t}}{q\varepsilon_0 \varepsilon_{\rm s} N_{\rm A}}} + \frac{1}{C_{\rm OX}}, \qquad (9)$$

where  $v_t = K_B T/q$  is the thermal voltage. Therefore, the condition in Eq. (7) for minimum subthreshold swing and hysteresis free operation of the device can be translated into

$$t_{\rm FE} = -\frac{1}{2\alpha} \left[ \frac{1}{C_{\rm OX}} + \sqrt{\frac{v_{\rm t}}{q\varepsilon_0 \varepsilon_{\rm s} N_{\rm A}}} \right], \tag{10}$$

which relates  $t_{FE}$ , a and  $N_A$  for minimum subthreshold slope.

In addition,  $Q_1$  is the maximum charge value and Eq. (8) becomes

$$N_{\rm A} = \frac{Q_1^2}{2q\varepsilon_0\varepsilon_{\rm s}/\psi_{\rm B}}.$$
 (11)

The final equation for the optimized ferroelectric thickness is

$$t_{\mathsf{FE}} = -\frac{1}{2\alpha} \left[ \frac{1}{C_{\mathsf{OX}}} + \sqrt{\frac{2\nu_{\mathsf{t}} 2\psi_{\mathsf{B}}}{Q_1^2}} \right]. \tag{12}$$

Thus, for a given ferroelectric material characterized by a,  $\beta$ , and  $\gamma$ , SS is minimized for  $N_A$  and  $t_{FE}$  given by Eqs. (11) and (12). It is clear from this equation that the thickness of ferroelectric material required depends on the landau parameter a of the material and the capacitance of the oxide between the ferroelectric and the silicon substrate. It is evident that high value of parameter a reduces the optimal ferroelectric thickness for a minimum subthreshold swing.

## 4. Dependence on coercive field ( $E_c$ ) and remnant polarization ( $P_0$ )

The anisotropy constants  $\alpha$  and  $\beta$  in Landau equation are calculated by fitting the Q- $V_{FE}$  characteristics to yield the given value of coercive field  $E_C$  and the remnant polarization  $P_0$ . Hence,  $V_{FE} (Q = P_0) = 0$  and  $dV_{FE}/dQ(V_{FE} = E_C t_{FE}) = 0$ . Consequently, the equation for  $\alpha$  and  $\beta$  becomes

$$\alpha = \frac{3\sqrt{3}}{4} \frac{E_{\rm c}}{P_0}, \quad \beta = \frac{3\sqrt{3}}{8} \frac{E_{\rm c}}{P_0^3}.$$
 (13)

Therefore, the thickness of the ferroelectric film in terms of coercive field and remnant polarization can be obtained as

$$t_{\rm FE} = \frac{2}{3\sqrt{3}} \frac{P_0}{E_c} \left( \frac{1}{C_{\rm OX}} + \sqrt{\frac{2v_t 2\psi_{\rm B}}{Q_1^2}} \right), \tag{14}$$

the high coercive field and small remnant polarization results in small  $t_{\rm FE}$ .

Ignoring the higher terms of *Q* in the Eq. (5), the ferroelectric negative capacitance equation reduces to

$$C_{\rm FE} = \frac{1}{2\alpha t_{\rm FE}} = -\frac{2}{3\sqrt{3}} \frac{P_0}{E_{\rm C} t_{\rm FE}}.$$
 (15)

In an NCFET device, the improved  $I_{ON}/I_{OFF}$  ratio and small subthreshold swing without hysteresis in the transfer characteristics depends on the capacitance matching ( $|C_{FE}| > C_{MOS}$ ) or  $|C_{FE}| \approx C_{MOS}$  as already discussed<sup>[12, 13]</sup>. For the best results,  $|C_{FE}|$  should be as close as possible to  $C_{MOS}$ , or should be higher than the  $C_{MOS}$ . Based on the values of  $N_A$  and  $t_{FE}$  obtained for the material PZT,  $C_{MOS}$  and  $|C_{FE}|$  shown in Fig. 3(a), suggesting  $|C_{FE}(Q)| \ge C_{MOS}(Q)$  at each Q, as in the numerical simulation for the same material in the MFIS capacitor<sup>[18]</sup>. The corresponding  $V_{FE}$  and  $V_{GMOS}$  obtained from numerical simulations of MFMIS capacitor are shown in Fig. 3(b). It is clear that with the increase in Q,  $V_{FE}$  decreases and this in turn amplifies  $V_{GMOS}$ , and reduces SS below Boltzmann's limit of 60 mV/dec.

The higher values of  $C_{FE}$  due to smaller alpha is due to small  $E_C$  and large  $P_0$  for fixed ferroelectric thickness, which res-

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Fig. 3. (a) Capacitance versus charge plots for MOS capacitor, ferroelectric capacitor and the total capacitance of MFMIS capacitor. (b) Voltage charge plot of the MFMIS capacitor.

Table 1.	Ferroelectric thickness	$(t_{FF})$ and minir	num subthresho	old swing (S <sub>min</sub>	) for differen	t ferroelectric materials.
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Parameter	PZT <sup>[20]</sup>	BaTiO <sub>3</sub> <sup>[15]</sup>	SBT <sup>[15]</sup>	HfSiO <sup>[22]</sup>	P(VDF-TrFE) <sup>[23]</sup>	
<i>a</i> (cm/F)	$-2.25 \times 10^{9}$	-5 × 10 <sup>8</sup>	$-3.25 \times 10^{9}$	-8.65 × 10 <sup>10</sup>	$-1.8 \times 10^{11}$	
$\beta$ (cm <sup>3</sup> /F/C <sup>2</sup> )	$1.3 \times 10^{18}$	$2.2 \times 10^{18}$	$9.37 \times 10^{18}$	$1.92 \times 10^{20}$	5.8 × 10 <sup>22</sup>	
γ(cm <sup>5</sup> /F/C <sup>4</sup> )	$9.83 \times 10^{25}$	$7.5 \times 10^{27}$	0	0	0	
<i>N</i> <sub>A</sub> (cm <sup>-3</sup> )	$2.3 \times 10^{20}$	$3.3 \times 10^{20}$	7.5 × 10 <sup>19</sup>	$5.7  imes 10^{19}$	$4 \times 10^{18}$	
<i>t</i> <sub>ox</sub> (nm)	2	2	2	2	2	
<i>t</i> <sub>FE</sub> (nm)	22.3	19.3	15.5	5.8	2.8	
S <sub>min</sub> (mV/dec)	52.8	46.5	41.6	31	27	

ults in lower voltage step up  $\left(\frac{\partial V_{MOS}}{\partial V_{GS}} = \frac{C_{FE}}{C_{FE} - C_{MOS}}\right)$ . Therefore, the gain reduces and consequently  $I_{ON}$  decreases. Although an increase in  $P_0$  reduces the hysteresis due to positive total gate capacitance, it comes at the cost of reduction in  $I_{ON}/I_{OFF}$  which renders ineffective the primary advantage of using ferroelectric in gate stack<sup>[19]</sup>.

Similarly, high values of  $\alpha$  implies a proportionally low value of  $|C_{FE}|$  for a fixed  $t_{FE}$ . This can also be understood in terms of very high coercive field and low remnant polarization. The higher values of  $\alpha$  results in the smaller  $|C_{FE}|$  than  $C_{MOS}$ , results in total negative capacitance which appears as the hysteresis in the transfer characteristics of the devices<sup>[20]</sup>. Therefore, a high coercive field improves the  $I_{ON}/I_{OFF}$  ratio; however, hysteresis occurs in the characteristics due to instability.

Consequently, we come to the conclusion that in an NC capacitor using ferroelectric material, the material parameters and the thickness of the material are interdependent to achieving a good result.

Considering Eq. (14), a high  $P_0$  and low  $E_C$  results in higher  $t_{FE}$ ; whereas, a low  $P_0$  and high  $E_C$  results in smaller  $t_{FE}$ . The calculated values of  $t_{FE}$  can be used to stabilise the capacitance and enhance the capacitance matching to the obtain the best possible results with high  $I_{ON}$  and no hysteresis. Therefore, we can opt for either higher polarization and smaller  $E_C$  or higher  $E_C$  with smaller  $P_0$ . However, from the design perspective of the NC-FET as experimentally shown in Ref. [20], the gate stack with ferroelectric material giving high  $E_C$  and low  $P_0$  resulted in 45% higher  $I_{ON}$  with the same OFF current. Therefore, we can say that higher alpha and consequently lower  $t_{FE}$  results in better characteristics and are also viable from the point of view of scaling these devices.

### 5. Equation for minimum subthreshold swing

Unlike  $S_{min} = 60 \text{ mV/dec}$  for classical FETs, a single  $S_{min}$  does not define the performance of all NCFETs; instead the stability constrained  $S_{min}$  depends on the choice of material system and channel type. In this section, an explicit analytical equation has been obtained to calculate the minimum possible SS ( $S_{min}$ ) under capacitance matching conditions. From the calculated  $S_{min}$  for different materials, we can infer the viability of different materials for the designing of NC-FETs.

For a given ferroelectric material characterised by  $\alpha$ ,  $\beta$  and  $\gamma$  subthreshold swing is minimized for  $N_A$  and  $t_{FE}$  given in Eqs. (11) and (12). The equation for minimum subthreshold swing ( $S_{min}$ ) is obtained by substituting the value of  $C_{FE}$  in Eq. (2) and can be written as

$$S_{\min} \approx \frac{2.3k_{\rm B}T}{q} \left(1 + M \times t_{\rm FE}\right),$$
 (16)

where  $M = (2\alpha + 12\beta Q^2 + 30\gamma Q^4) C_{MOS}$  is a material dependent constant. The values of calculated optimal thicknesses and corresponding  $S_{min}$  for the five different ferroelectric materials viz: lead zirconium titanate (PZT), strontium barium titanate (SBT), barium titanate (BaTiO<sub>3</sub>), hafnium silicate (HfSiO) and organic ferroelectric material P(VDF-TrFE) are given in Table 1. The non-ideal effects (such as domain propagation, grain boundaries and domain nucleation of the ferroelectric material) degrade the subthreshold swing of the realistic device and, therefore, the minimum subthreshold swing ( $S_{min}$ ) in Eq. (16) can be obtained when we assume that the all these non-idealities are absent<sup>[21]</sup>. The value of  $S_{min}$  for PZT material is 52.8 mV/dec using the above equation, which is consistent with the simulated value of 55 mV/dec for the

same material by Ref. [23]. Furthermore, the value of 27 mV/dec for the organic ferroelectric material PVDF is in agreement with low SS values for such materials, as described in Ref. [24].

### 6. Conclusion

In this paper, we have developed an analytical equation for the ferroelectric thickness and the minimum subthreshold swing possible in an MFMIS capacitor. This captures the impact of ferroelectric material properties on the Landau coefficients, which in turn determines the thickness of the ferroelectric material required to have better capacitance matching and which reduces the subthreshold swing. Finally, this study calculated the minimum subthreshold swing of an NC capacitor with five different ferroelectric materials, which may help us in the selection of a particular ferroelectric material for an actual NC-FET design.

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