# Recent progress in epitaxial growth of III–V quantum-dot lasers on silicon substrate

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**Abstract:** In the past few decades, numerous high-performance silicon (Si) photonic devices have been demonstrated. Si, as a photonic platform, has received renewed interest in recent years. Efficient Si-based III–V quantum-dot (QDs) lasers have long been a goal for semiconductor scientists because of the incomparable optical properties of III–V compounds. Although the material dissimilarity between III–V material and Si hindered the development of monolithic integrations for over 30 years, considerable breakthroughs happened in the 2000s. In this paper, we review recent progress in the epitaxial growth of various III–V QD lasers on both offcut Si substrate and on-axis Si (001) substrate. In addition, the fundamental challenges in monolithic growth will be explained together with the superior characteristics of QDs.

Key words: quantum dots; silicon photonics; epitaxial growth; semiconductor laser

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# 1. Introduction

For decades, with increasing research in the silicon (Si) microelectronics industry, the exponentially increasing complexity of integrated circuits (ICs) has been witnessed. The capabilities of interconnects have become an essential role in evaluating chip performance, especially for the very large-scale integration (VLSI). This results in the urgent demand for achieving cost-effective and high-performance integrated photonics devices. The superior properties of Si, including low-defect density, high thermal conductivity and high refractive-index contrast between Si and its native oxide i.e. SiO<sub>2</sub> raise the potential for scalable Si photonic integrated circuits (PICs). Thus far, the Si community has well-developed many photonics building blocks, including ultra-fast modulators<sup>[1-3]</sup>, high-bandwidth detectors<sup>[4-6]</sup> and low-loss waveguides<sup>[7, 8]</sup>. Amidst this progress, the absence of an integrated laser source has impeded the full development of this field due to Si's inefficient light emission. In recent years, research groups from all over the world have succeeded in generating light on group IV platforms with the usage of competent light-emitters from group III-V<sup>[9-12]</sup>. Among these approaches, the monolithically growth of group III-V materials on Si substrate seems to be the optimum choice own to its low-cost and ease of scalability. However, the material dissimilarity between group III-V materials and Si substrates<sup>[13, 14]</sup> tends to introduce various defects that will significantly degrade devices' performance seriously. Fortunately, the well-established quantum dots (QDs) structure with its unique characteristics of defects tolerance and carrier confinement offers the possibility of practical III-V/Si integration.

Correspondence to: S M Chen, siming.chen@ucl.ac.uk Received 29 JUNE 2019; Revised 27 JULY 2019. ©2019 Chinese Institute of Electronics This review paper began by reviewing the challenges of monolithic growth of Si-based III–V compounds. Then, the advantages of QDs for monolithic III–V/Si integration technology were discussed. Followed by our recent progress being made in achieving high-performance O-band InAs QD lasers monolithically growth of III–V QD lasers on both off cut and on-axis Si platforms.

# 2. Challenges in monolithic growth

Ever since the first attempt to grow GaAs thin films on Sisubstrates in the 1980s<sup>[14, 15]</sup>, the monolithic growth of III–Vs on Si has been pursued with little progress due to the large material dissimilarity between group III–V and group IV materials, including polar vs non-polar surfaces, lattice mismatch, and different thermal expansion coefficients. These differences tend to produce various types of defects, such as antiphase boundaries (APBs), threading dislocations (TDs) and thermal cracks, as seen in Fig. 1, which all generate non-radiative recombination centres, and dramatically undermine the promise of III–Vs monolithically grown on Si.

The first issue shown in Fig. 1(a) is related to the formation of the APBs in III–V/Si epitaxy, which comes from the combination of polar and non-polar bonds. In practice, the Si (001) surface always contains single atomic steps, and the nucleation of the group V species on the Si surface always emerges preferentially over the group III species. Single atomic steps will induce planes of III–III and V–V bonds separately<sup>[16]</sup>. As a result, these planar defects act as nonradiative recombination centres for the optoelectronic device and affect devices' optical properties. Meanwhile, to a certain extent, they can be treated as leakage paths for electronic devices that will significantly reduce electron mobility<sup>[17, 18]</sup>.

The second issue is the formation of the TDs. Compared to Si, whose lattice constant is 5.431, GaAs (5.653) has approx-



Fig. 1. (Color online) Three major issues faced in monolithic III-V/Si integration: (a) APBs, (b) TDs and (c) thermal cracks.

imately 4% lattice mismatch. This leads to continuous stress accumulation in the epilayer, and this stacked energy will be released at the pseudomorphic critical point with increasing deposition thickness. This relaxation will cause two types of dislocations: misfit dislocations and TDs. Neither of the dislocations can terminate within a crystal due to the energetic reasons; they must either stop at the edge as misfit dislocations or propagate upwards to form the TDs<sup>[19]</sup>. Consequently, the TDs tend to enhance the non-radiative recombination and eventually reduce the devices' internal efficiency as well as their lifetime<sup>[20]</sup>.

The last issue is that of thermal cracks, which normally appears during the cooling-down process from high growth temperature (normally ~600 °C) to room temperature (RT). It originates from the different thermal expansion coefficient between III–V and Si material, which promotes the accumulative thermal strain in the epilayer. The strain relaxation results in cracks formed in the III–V film and this will seriously reduce the performance and the yield of the device. Moreover, it will limit the maximum achievable growth thickness of III–V compounds on Si. In addition to cracks, this thermal strain may create an extra driving force for dislocation growth and further degrade devices' performance<sup>[21]</sup>.

# 3. Advantages of quantum dots (QDs)

After the hypothetical heterojunction structures proposed by Kroemer in 1963<sup>[22]</sup>, it took seven years to realise the first double heterostructure lasers<sup>[23]</sup>. This successful demonstration reveals the necessity of carrier confinement in achieving lasing. Following this, with growing interests in quantum confined semiconductor lasers, massive progress has been made in the worldwide semiconductor laser community. By reducing the dimensionality of the active region, a quantum well (QW) structure with 1-D confinement and a quantum wire structure with 2-D confinement were achieved in 1974 and 1989 respectively<sup>[24, 25]</sup>. As those materials are well-studied, establishing a 3-D confined structure (QD) is considered as the ultimate goal due to its more significant modification of the density of states (DOSs). Even though, using the QD as the active region was proposed in 1982 by Arakawa and Sakaki<sup>[26]</sup>, it was obtained only after the Stranski-Krastanow (S-K) growth method came into use in 1994<sup>[27]</sup>. This growth mode takes advantage of the strain energy resulting from the mismatched lattice constant between III-V compounds and group IV materials. With increasing thickness, the growth-material could be self-assembled into 3D islands<sup>[28]</sup>. The properties of these islands could be tuned and optimised for target applications by controlling the growth conditions such as growth temperature, growth rates, growth

time and material ratios<sup>[29-31]</sup>.

However, in reality, early QDs devices suffered from QD size fluctuations resulting from the inevitable self-assembly techniques<sup>[28]</sup>. For example, the inhomogeneous broadening leads to a wider spectral range along with the reduction of maximum peak gain<sup>[20]</sup>. Over the decades, optimised QDs, as well as device structures, have been pursued. With persistent efforts to improve layer structures, dots uniformity, dots density and the number of layers of dots, many predicted superior properties of QDs have been gradually exhibited, including low threshold current<sup>[28]</sup>, narrow spectrum linewidth<sup>[32]</sup>, large modulation bandwidth<sup>[33, 34]</sup>, low relative intensity noise (RIN)<sup>[35]</sup>, high efficiency and incredibly long lifetime<sup>[36]</sup>. In addition to these well-established properties, QDs have recently been widely employed as the active region for monolithic III-V/Si integration technology due to its lower sensitivity to defects results from effective carrier localisation. For example, in the QW active region, any TDs through the active region will become non-radiative recombination centres (Fig. 2(a)), leading to a significantly increased threshold or even 'killing' the device. However, in contrast, for the QD active region, one TD can only 'kill' one or a very limited number of dots, leaving the rest unaffected, and continue to contribute to optical gain<sup>[11]</sup>. Furthermore, there is a case called 'dislocation bending' in QDs material, which is presented in Fig. 2(b). For a pyramidal-shaped QD, when a TD propagates towards its base, the dislocation bending will occur if the dislocation selfenergy is equal to or smaller than the strain energy released for generating a misfit dislocation<sup>[37, 38]</sup>. In this case, a piece of misfit dislocation will be generated and drift beneath the QD<sup>[39]</sup>. Moreover, the strong strain field existing in the QDs could drive away the TDs and this could further reduce the dislocation density<sup>[40]</sup>.

# 4. QD lasers on Si substrate with off cut

In the last few years, unbelievable developments have been witnessed in Si photonics: the increasing functionality in Si-based integrated circuits (IC) and the excellent performance of most key optical components. Based on these achievements, many efforts have been devoted to fabricating efficient Si-based lasers which are indispensable to the phonics integrated circuits (PICs), and much progress has been made. In spite of that, the very first QD laser monolithically grown on Si was reported in 1999<sup>[41]</sup>, and a QD laser grown on Si lase at 1.3 or 1.55  $\mu$ m was not obtained until the research group from UCL achieved the first electrically pumped 1.3  $\mu$ m InAs QD laser grown directly on a Si (001) substrate with 4° off-cut towards [011] plane in 2011<sup>[42]</sup>. Since then, numerous approaches have been investigated to optimise devices'



Fig. 2. (Color online) (a) Schematic illustration of the comparative interaction of threading dislocations with QWs and QDs. (b) Cross-sectional schematic description of the mechanism of dislocation bending by QDs. (c) Bright-field scanning transmission electron microscopy (TEM) images of the TDs in the QD active region<sup>[11]</sup>. Copyright © 2016, Springer Nature. With permission of Springer (c). © 2009, IEEE. Reprinted with permission from Ref. [39] (b).



Fig. 3. (Color online) (a) Dark-field scanning TEM image of a Si substrate with 4° off-cut. (b) High resolution high-angle annular dark-field scanning TEM image of the 6 nm-thick AIAs nucleation layer between GaAs buffer layer and a Si substrate. (c) Bright-field scanning TEM image of DFLs. (d) Bright-field scanning TEM image of the QD active layers. (e) Photoluminescence (PL) spectrum of a QD active region grown on GaAs and Si. (f) Cross-sectional scanning emission microscopy (SEM) image of the fabricated RWG laser with as-cleaved facets. Copyright © 2016, Springer Nature. With permission of Springer (a–c). Reprinted with permission from Ref. [49], The Optical Society (OSA), CC BY 4.0 (d–f).

quality<sup>[43–46]</sup>.

A few epitaxial growth techniques were used to minimise the impact of various defects and the general Si off cut substrate developed by the UCL group can be found in Fig. 3. The design of the 4° off-cut angle could eliminate the formation of APBs. At the initial stage, a thin AlAs nucleation layer was employed to suppress 3-D growth and provided a flat interface for the following depositions<sup>[43]</sup>. After that, a threestep growth method was introduced: a GaAs buffer layer with a thickness of 30, 170 and 800 nm grown at 350, 450 and 590 °C, respectively<sup>[18, 42]</sup>. With these techniques, most of the defects could be well confined in the first 200 nm, although the density of TDs propagating towards the active region was still as high as  $1 \times 10^9$  cm<sup>-2[47]</sup>. To further eliminate the TDs, the periodically strained-layer superlattices (SLSs) composed of In<sub>0.18</sub>Ga<sub>0.82</sub>As/GaAs were developed as the dislocation filter layers (DFLs)<sup>[48]</sup>. Moreover, an in situ thermal annealing cycle was used immediately after the growth of the SLSs which, could improve the structural and optical properties<sup>[45]</sup>. Consequently, a QD density of ~  $3.0 \times 10^{10}$  cm<sup>-2</sup> with relatively good quality was obtained together with a dislocation density at the order of  $10^5$  cm<sup>-2</sup>.

In this section, a few representative off cut Si-based QD lasers fabricated using the above growth techniques will be reviewed, including the Fabry-Perot (FP) laser, the narrow-ridge-waveguide (RWG) FP laser, the distributed feedback (DFB) laser and the micro-disk laser, all on off cut Si substrate.

## 4.1. FP lasers on off cut Si substrate

A pioneering work was done by Chen *et al.* in 2016<sup>[11]</sup>. In this work, Si-based InAs/GaAs QD lasers grown by a solidsource molecular beam epitaxy (MBE) lasing at 1.3  $\mu$ m were demonstrated. Thanks to the rigorous design and growth, a high-performance electrically pumped continuous-wave (CW) 1310 nm III–V/Si laser was achieved with remarkable proper-

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Fig. 4. (Color online) (a) Measured RT CW *L*–*I*–*V* curves for a 50  $\mu$ m × 3200  $\mu$ m InAs/GaAs QD laser grown on a Si substrate. (b) Ageing data for the InAs/GaAs QD laser under CW (drive current = 210 mA) at a constant heat sink temperature of 26 °C. (c) Measured CW *L*–*I* curve from a 2.2  $\mu$ m × 4 mm narrow-ridge-waveguide laser as a function of temperature. (d) Lateral near-field intensity profiles with different injection currents. Inset: infrared (IR) camera image of lasing near-field at the threshold of 20 mA (well above threshold). (e) RIN spectra up to 16 GHz at gain currents of 40, 60 and 80 mA. (f) Experimental results. Left: 25.6 Gb/s eye-diagrams at back-to-back and after transmission over 13 km SMF28. Right: measured bit-error rate (BER) results at different received power. Copyright © 2016, Springer Nature. With permission of Springer (a and b). Reprinted with permission from Ref. [49], The Optical Society (OSA), CC BY 4.0 (c–f).

ties: operating up to 75 °C (120 °C under pulsed operation), with an ultralow threshold current density of 62.5 A/cm<sup>2</sup> and output power of over 100 mW at RT (shown in Fig. 4(a)). It has to be noted that the lifetime of this device is predicted to be over 100, 158 h<sup>[11]</sup>. These parameters are powerful evidences of the highly reliable and cost competitive Si-based photonic-electronic integration.

Later, the broad-area lasers described in Ref. [11] were used to generate the first gain-switched optical pulses. Their dynamic properties for optical communications have been studied in both experimental and theoretical ways, and good agreement could be found between the two. The typical time duration for the shortest pulses observed is between 175 and 200 ps, with a maximum peak output power of up to 66 mW. A trade-off between maximising the optical power and narrowing the pulse duration has been noticed: a longer high-amplitude drive pulse results in a higher peak optical power, while a shorter one can provide us with a narrower pulse width. Simulations and analysis suggest that there are two main reasons for the broadened pulse width: a limited gain from InAs QDs and a high gain compression factor. Therefore, it is believed that the optical applications of Si-based QD lasers could be significantly improved by increasing the number of active layers, introducing the p-doping in the active region as well as modifying the devices' geometry<sup>[50]</sup>.

Because the multi-transverse-mode operation might limit the laser's dynamic response, the RWG FP lasers with 2.2  $\mu$ m ridge width were then fabricated to ensure the single-transverse mode lasing at 1.3  $\mu$ m. In additions, these RWG lasers could help us to take a closer step towards practical applications in tele-/data communications. Depicted in Figs. 4(c)– 4(f), the designed RWG QD lasers have a CW threshold current as low as 12.5 mA at RT, and it could operate at up to 90 °C under CW. In addition, an ultra-low relative intensity noise (RIN) (< -150 dB/Hz) was measured in the range of 4–16 GHz. Based on these excellent noise characteristics, 25.6 Gb/s data transmission over 13.5 km SMF-28 has been demonstrated<sup>[49]</sup>. This low-noise QD laser shows promise for future inter/hyper data-centre interconnection.

Soon after, a comprehensive study was carried out on a 2.5 mm long RWG QD lasers. The small-signal modulation unveils the maximum 3 dB bandwidths of 1.6 GHz with the value of *D*-factors value equals to 0.3 GHz/mA<sup>1/2</sup>, *K*-factors in the range of 2.4 to 3.7 ns and the modulation current efficiencies of 0.4 GHz/mA<sup>1/2</sup>. A simulation of small-signal response was also applied to reveal the optimised device geometry: a 0.75 mm long cavity with 99% and 60% high-reflection coatings and ten QD layers, which forecast a maximum 3 dB-modulation bandwidth of 5 GHz to 7 GHz<sup>[51]</sup>. More importantly, this work indicates that neither the increased non-radiative recombination in the wetting layer nor the dislocation-induced optical seem to limit the devices' modulation characteristics once the QDs active region provides sufficient gain.

# 4.2. Distributed-feedback (DFB) lasers on off cut Si substrate

The excellent Si-based III–V FP laser sources with specially designed cavities could implement single-mode lasers, and this will enable wavelength division multiplexing (WDM), which is necessary in high-performance applications<sup>[52]</sup>. With the development of a selective-area growth technique, highquality InP-based material directly grown on buffer-free Si substrate was presented in 2014<sup>[53]</sup>. Based on this technique, the first Si-based InP distributed feedback (DFB) laser array was demonstrated by Wang *et al.* in 2015<sup>[54]</sup>, and the first Sibased O-band DFB array was achieved by the same research group one year later<sup>[55]</sup>. However, both of them are limited by optical pumping. An electrically pumped DFB laser array on Si substrate was not realised until 2018 when a breakthrough was made by researchers from UCL in the UK and



Fig. 5. (Color online) (a) Cutaway schematic of the DFB laser array on Si (not to scale). (b) High-resolution SEM image of the gratings with a  $\lambda/4$  phase shift in the middle of the etched gratings. (c) The *L*–*I*–*V* curve of a single 1 mm long Si-based DFB laser. (d) Optical spectra of a DFB laser array with different grating periods around their maximum output power levels before saturation at RT. Reprinted with permission from Ref. [56], The Optical Society (OSA).



Fig. 6. (Color online) (a) Schematic of layer structure of QD MD laser on Si. The insets are an SEM image of a cross-section of the structure (left) and a TEM image of the QD active region (right). (b) The RT CW *L*–*I*–*V* curve for a 25  $\mu$ m × 3 mm Si-based edge-emitting QD laser. The inset shows an atomic force microscopy (AFM) image of an uncapped Si-based QD sample. (c) RT CW emission spectrum measured at a current of ~ 1.4 × threshold for MD-on-Si lasers with diameters of 14 and 30  $\mu$ m. Dashed curve: PL spectrum of QD material (not in scale). The inset: an SEM image of the MD laser (*D* = 30  $\mu$ m). Reprinted with permission from Ref. [61], The Optical Society (OSA).

the Sun Yat-sen University in China<sup>[56]</sup>. An InAs/GaAs QD laser grown on the n-doped Si (001) misoriented 4° towards the [011] plane was used in this work. In addition, the optimised growth conditions yield an almost defect-free active region. Additionally, the conventional complicated regrowth process was replaced by a simpler method, in which lateral surface gratings were fabricated during the waveguide etching process by inductively coupled plasma (ICP). At the same time, the cost-effectively dry-etched anti-reflection (AR) output couplers were utilised to suppress the facet back reflections<sup>[57]</sup>. The device structure is displayed in Fig. 5 and a  $\lambda/4$  phase shift in the middle of the etched gratings can be noted, forcing single-longitudinal-mode lasing in the defect mode<sup>[56]</sup>.

As a result, the first Si-based electrically pumped singlemode DFB laser array using InAs/GaAs QD gain materials was demonstrated, which could lase at RT under CW. The threshold current of this device was as low as 12 mA, and its single-mode side mode suppression ratio (SMSR) was as high as 50 dB<sup>[56]</sup>. Also, it exhibited a record wavelength coverage of 100 nm, mainly due to the inhomogeneous nature of QDs.



Fig. 7. Bright-field scanning TEM image of on-axis Si (001) substrate.

#### 4.3. Micro-lasers on off cut Si substrate

Not only macro-lasers but also micro-lasers show great potential to work as a long-wavelength light source for Si photonics owing to their short cavity length, small size and low power consumption<sup>[58]</sup>. Unlike macro-lasers, micro-ring and microdisk (MD) lasers can lock a specific laser wavelength by whispering-gallery-mode (WGM) resonance, which is less sens-



Fig. 8. (Color online) (a) Single-facet output power versus current density for the same Si-based InAs/GaAs QD laser as a function of temperature under CW operation. The inset shows the *L*–*I* curve for this Si-based InAs/GaAs QD laser at a heat sink temperature of 36 °C. (b) Single-facet light power verses current density for an InAs/GaAs QD laser grown on GaAs/Si (001) substrate at various heat sink temperatures under pulsed condition. The inset shows the natural logarithm of current density against temperature in the ranges of 16–102 °C. (c) Measured *L*–*I*-*V* curve of 50  $\mu$ m × 3 mm broad-area InAs/GaAs QD laser directly grown on GaAs/Si (001) substrate. (d) Measured *L*–*I* curve of InAs/GaAs QD laser directly grown on GaAs/Si (001) as a function of temperature. Inset: lasing spectrum at 180 A/cm<sup>2</sup> injection current density. Reprinted with permission from Ref. [68], CC BY 4.0 (a and b). Reprinted with permission from Ref. [69], CC BY 4.0 (c and d).

itive to temperature variation and thus offers higher stability of emission wavelength<sup>[59]</sup>. Compared with the micro-ring lasers, the micro-disk lasers attract more attention owing to their better heat dissipation characteristics, as well as their ability to be scaled down to ultra-small diameters (~1  $\mu$ m)<sup>[60]</sup>.

In 2017, Kryzhanovskaya *et al.* succeeded in demonstrating the first Si-based micro-disk lasers with diameters from 14 to 30  $\mu$ m and enabled emission in the range of 1320–1350 nm with minima  $J_{\rm th}$  of 600 A/cm<sup>2[61]</sup>. The layer structure of the fabricated MD laser can be found in Fig. 6(a), and its RT CW light–current–voltage (*L–I–V*) curve and emission spectrum are shown in Figs. 6(b) and 6(c) respectively. Notably, these devices with a diameter of 30  $\mu$ m can operate in nearly single-mode up to 60 °C in CW and 110 °C in pulsed mode with a side mode suppression ratio of about 18 dB<sup>[62]</sup>.

# 5. QD lasers on on-axis Si (001) substrate

Even though, the off cut Si substrate could inhibit the emergence of APBs, adopting the 'exact' (001) Si substrates (shown in Fig. 7) is an essential step to fully exploit the potential of Si photonics because of its compatibility with conventional CMOS processing<sup>[63]</sup>. There are a few approaches to obtain the III–V lasers on on-axis Si (001) substrates, including the usage of the GaP/Si template<sup>[64, 65]</sup> and the GaAs-on-V-groove (GoVS) Si template<sup>[66, 67]</sup>. However, for these technologies, either an intermediate buffer layer or a patterned substrate is needed. To overcome these barriers, Leti developed an APB-free GaAs/Si (001) virtual substrate by using a metal-organic chemical vapour deposition (MOCVD) method, in

which a two-step process was used: an initial 40 nm GaAs layer was grown as the nucleation layer that was deposited at low-temperature (400–500 °C), then, a 360 nm GaAs layer was grown in a relative high temperature (600–700 °C)<sup>[17]</sup>. Subsequently, this grown wafer was transferred to an MBE growth chamber at UCL for the deposition of the rest of the laser structure, which is similar to that described in Section 4, consisting of buffer layers, DFLs, p–i–n structures and the contacting layer.

In this section, our recent achievements in FP and MD QD lasers with on-axis Si (001) substrate will be covered.

#### 5.1. FP lasers on on-axis Si substrate

The first CW electrically pumped InAs/GaAs QD lasers monolithically grown on 'exact' (001) Si substrate were reported by Chen *et al.* from UCL in 2017, in which no intermediate buffer layers or patterned Si substrates were used<sup>[68]</sup>. In this work, a 600 nm GaAs buffer was first deposited by MBE, followed by five repeats of DFLs and the InAs/GaAs QD laser structure. Finally, this growth process ended with a 300 nm p-type GaAs contact layer. As a result, an RT CW lasing at ~ 1.3  $\mu$ m with a threshold current density of 425 A/cm<sup>2</sup> and a single-facet output of around 43 mW was achieved (as shown in Fig. 8(a)). Its characteristics under pulsed mode can be seen in Fig. 8(b): it could achieve lasing up to 102 °C with a threshold current density equals to 250 A/cm<sup>2</sup> and a single-facet output power exceeding 130 mW at RT<sup>[68]</sup>.

By optimising the growth conditions, Li *et al.* achieved a high-quality electrically pumped on-axis Si-based QD laser with low threshold current density (160 A/cm<sup>2</sup> @ RT CW).



Fig. 9. (Color online) (a) Schematic diagram of a microdisk laser grown on planar on-axis Si (001) substrate. (b) SEM image of a fabricated microdisk laser. (c) Collected PL spectra above and below the lasing threshold of a MD with  $D \sim 1.9 \mu$ m. Reprinted with Permission from Ref. [70], The Optical Society (OSA).

Moreover, the lasing was observed at up to 52 °C under CW<sup>[69]</sup>.

# 5.2. Micro-lasers on on-axis Si substrate

Recently, Zhou *et al.* have developed a CW optically pumped InAs/GaAs QD MD directly grown on on-axis Si (001) substrate<sup>[70]</sup>. Above the buffer layer, three stacked InAs/In<sub>0.15</sub>Ga<sub>0.85</sub>As/GaAs dot–in-well (DWELL) active layers were sandwiched by two Al<sub>0.4</sub>Ga<sub>0.6</sub>As cladding layers. A 10 nm GaAs layer was used to cap the whole epitaxy structure. Detailed descriptions of the epitaxy structure can be found in Refs. [17, 68].

Fig. 9 displays the schematic diagram of the MD laser together with its SEM image. The lasing characteristics of the fabricated device with small diameter (*D*) in the range of 1.1– 2  $\mu$ m are demonstrated. According to the measurements, lasing emission from both ground states and excited states were obtained with an ultra-low lasing threshold of ~ 3  $\mu$ W (for *D* ~ 1.1  $\mu$ m)<sup>[70]</sup>. The realisation of on-axis (001) Si-based MD lasers, and their promising lasing characteristics is an essential step towards the high density, large-scale integration of Si photonics.

# 6. Conclusion

This paper has predominantly reviewed the recent achievements made by UCL in the epitaxial growth of various III–V QD lasers monolithically grown on Si substrates, including the FP laser, the DFB laser and the MD laser. We also discuss material dissimilarity issues between the III–V compounds and Si substrates, which will generate different kinds of defects and seriously hinder the development of Si photonics. The 3-D confined island-structure with discrete energy levels has captured plenty of attention since first realised in 1994 due to its irreplaceable properties in temperature stability and defects immunisation. To obtain a high-performance device, numerous growth techniques have been investigated worldwide, such as the nucleation layers, the dislocation filter layers and the in situ thermal annealing process.

Owing to these intense research efforts, 1.3  $\mu$ m InAs QD lasers monolithically grown on Si (001) substrate with a 4° off-cut towards the [011] plane have achieved impressive results: the FP laser with ultra-low threshold current density (62.5 A/cm<sup>2</sup>) and incredibly long life-time (over 100,158 h), the narrow-ridge-waveguide FP laser with excellent noise characteristics (RIN < -150 dB/Hz), and the first electrically pumped DFB laser together with the first Si-based micro-disk laser. However, lasing from the on-axis Si (001) substrate is needed to take advantages of the well-developed CMOS industry. Our group succeeded in realising the first CW electrically pumped InAs/GaAs QD lasers monolithically grown on the on-axis Si (001) substrate lasing up to 102 °C (pulsed) in 2017. Soon after, an improved version was exhibited with a threshold current density of 160 A/cm<sup>2</sup> (@ RT CW) and lasing up to 52 °C under CW mode. Moreover, a MD laser directly grown on 'exact' Si (001) substrate was obtained recently with a lasing threshold as low as 3  $\mu$ W.

These remarkable accomplishments confirm that the monolithic Si-based QD laser is a promising candidate for integrating the optoelectronics devices with Si platforms and prove the feasibility to fully explore the applications of Si photonics.

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