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## Low temperature performance of transimpedance amplifier and its application in amplification of terahertz photoelectric signal

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**Abstract** With the development of terahertz technology, low temperature electronics and radio astronomy, the demand for integrated transimpedance amplifier chips working in low temperature environment increases. The electrical performance of a Ge Si based transimpedance amplifier in deep low temperature environment is studied. The current voltage curves of the typical ports and gain curve of the amplifier chip at 8 K, and a relatively flat gain effect in the 0.1 GHz 3 GHz band are obtained. In order to verify its amplification function of terahertz photoelectric signal, GN1068 is integrated with terahertz Quantum Well Photodetector(QWP), and a terahertz pulse laser detection system is built. A terahertz photoelectric signal, with a pulse width of 2 s, is successfully amplified at 8 K. The transimpedance gain is about 560 . The current amplification gain is 1.78 mA/V. The above results verify the feasibility of commercial transimpedance amplifier in deep low temperature environment for the first time, and provide an effective technical means for integrated transimpedance amplifier in the field of terahertz high-speed detection and high frequency communication.

Keywords terahertz detectingUž high speed packagingUž transimpedance amplifierUž cryogenic amplification technique

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		THz QWP	Liu <sup>[7]</sup> 20 <sup>[8-14]</sup>	004 T	1~10 THz Hz QWP		[15]	
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(Bipol (Complementary <sup>[18]</sup>	ar JunctionTr ⁄ Metal Oxide	(Junction ansistor BJT) Semiconduct	FieldEffect Tra	ansisto	r JFET)		100 K	
					TH-7			40 K
		100 MHz <sup>[2</sup> Ge-	<sup>ອງ</sup> Si		1112	(GN1068)	ті	Hz QWP
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THz QWP

18			VB—	¥.ý ' :*a	→ ' Ñ				21
	THz			[11]	THz QV	VP	РСВ	THz OWP	GN1068
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		2 (P	rinted Circuit E	Board PCB	PCB	68	Fig. 2 Pac	heat sink (Cu)	ram of THz QWP and
PCB	(Heat s	sink) 24350B					2 TH	IZ QWP GN	11068
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	sar	nple holder (Cu)	de la comparación de la comp de la comparación de la comparación		RF−in <b>───┨</b> ┣-	U-set		OUT-N UUT-P	
	Fig. 3 P G 3 GN	hoto of the integr N1068installed o I1068	rated chip of THz ( n a copper sample THz Q	QWP and holder WP	Fig. 4 Sche meas 4	matic of th surement of G I GN1068	r <b>−</b> le electri 1068	cal charact	eristic
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		////	0 N 0 1 V	TH-		l OA	P G	N1068	
QCI	0.1 A	V V	[2:3]	1112	Fig. 5 Option	schematic of	vorificatio	n evetor for	amplifying detection
4	.3 TH7		100 m×2	2 mm	signal o	f THz QWP u	nder low t	emperature	
	56	К	2.0 /	4	5	THz QWP			





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