

Ultra-power-efficient heterogeneous III–V/Si MOSCAP (de-)interleavers for DWDM optical links

STANLEY CHEUNG,*  GEZA KURCZVEIL, YINGTAO HU, MINGYE FU, YUAN YUAN,  DI LIANG,  AND RAYMOND G. BEAUSOLEIL

Hewlett Packard Labs, Hewlett Packard Enterprise, Milpitas, California 95035, USA

*Corresponding author: stanley.cheung@hpe.com

Received 11 October 2021; revised 7 December 2021; accepted 13 December 2021; posted 13 December 2021 (Doc. ID 444991); published 24 January 2022

We discuss the design and demonstration of various III–V/Si asymmetric Mach–Zehnder interferometer (AMZI) and ring-assisted AMZI (de-)interleavers operating at O-band wavelengths with 65 GHz channel spacing. The wafer-bonded III–V/Si metal-oxide-semiconductor capacitor (MOSCAP) structure facilitates ultra-low-power phase tuning on a heterogeneous platform that allows for complete monolithic transceiver photonic integration. The second- and third-order MOSCAP AMZI (de-)interleavers exhibit cross-talk (XT) levels down to -22 dB and -32 dB with tuning powers of 83.0 nW and 53.0 nW, respectively. The one-, two-, and three-ring-assisted MOSCAP AMZI (de-)interleavers have XT levels down to -27 dB, -22 dB, and -20 dB for tuning powers of 10.0 nW, 7220.0 nW, and 33.6 nW, respectively. The leakage current density is measured to be in the range of 1.6–27 $\mu\text{A}/\text{cm}^2$. To the best of our knowledge, we have demonstrated for the first time, athermal III–V/Si MOSCAP (de-)interleavers with the lowest XT and reconfiguration power consumption on a silicon platform. © 2022 Chinese Laser Press

<https://doi.org/10.1364/PRJ.444991>

1. INTRODUCTION

Over the past decade, there continues to be an increased demand for high bandwidth density optical interconnects for growing mega data centers, long-haul telecommunications, and peta/exa-scale high-performance computing. Some studies suggest annual global data center IP traffic will reach 20.6 zettabytes by the end of 2021; 94% of those workloads will be processed by cloud data centers and 6% by traditional data centers [1]. The majority of this IP traffic comes from video streaming services, and machine-learning applications [2]. With the advent of the Internet-of-Things, machine-to-machine connections via home applications and connected cars are expected to have a 30% compound annual growth rate over a forecast period from 2018 to 2023, connecting half of global devices [3]. To accommodate the exponential growth in data traffic, the trend has been to scale mega-data centers with hundreds of thousands of servers, which underscores the concern for data center power consumption given increased ecological concerns and future environmental impact [2,4]. In fact, by 2030, it is estimated that 8%–20.9% of global power consumption will be from data centers [5]. Therefore, future data center interconnects will need to support higher bandwidths at reduced energy dissipation (energy/bit) at a cost that economically scales.

Silicon photonics is a promising technology that aims to reduce system-level power consumption to a few or sub-picojoule/bit, increase aggregate bandwidth to multiple terabytes/second, and lower manufacturing costs by leveraging well-established complementary metal oxide semiconductor technologies. A major advantage of silicon photonics lies in dense integration capacity to conveniently enable advanced technology, such as dense wavelength-division multiplexing (DWDM) where many parallel channels of wavelengths can be multiplexed onto a single fiber for massive aggregate bandwidth. At Hewlett Packard Labs, a novel DWDM architecture to drastically reduce chip power consumption (<1.5 pJ/bit), while simultaneously increasing transmission bandwidth (>1 Tbit/s), has been proposed [6]. A heterogeneous III–V/Si platform is being developed to enable such a DWDM transceiver. Within this platform, optical (de-)interleaving is an important functionality for realizing high-bandwidth DWDM systems with our heterogeneous optical frequency comb (OFC) sources [6–10], semiconductor optical amplifiers (SOAs) [11], and micro-ring modulators [12,13]. Typically, micro-ring resonators (MRRs) are cascaded onto a single common bus waveguide; however, there is a practical limit to the number of MRRs that can be used in this architecture. This is due to cross

talk (XT), off-resonance insertion loss (IL), and the free spectral range (FSR) limitation of MRRs at the expense of smaller ring radius size, as well as control complexity [14–16]. On-chip (de-)interleavers address this issue by splitting every other OFC wavelength onto a separate spatial bus waveguide such that each spatial bus links a half number of the MRRs, but with the channel spacing doubled. The channel spacing is chosen to be 65 GHz because it was found in DWDM applications to suffer from moderate power penalty for modulation at 10 Gbit/s [17]. Multiple stages of (de-)interleaving can further extend the channel spacing if necessary. Current state-of-the-art silicon photonic (de-)interleavers use either power inefficient thermal [18,19] or current injection phase shifters to compensate for either waveguide phase errors, power splitting errors, or temperature drift. In this paper, we demonstrate the use of a heterogeneously integrated III–V/Si metal-oxide-semiconductor capacitor (MOSCAP) structure to efficiently tune the phase errors in various (de-)interleavers such that channel XT is reduced. In this case, we aim to (de-)interleave an OFC source (comb laser A) with 65 GHz channel spacing, as illustrated in Fig. 1 [6]. We have designed and fabricated various (de-)interleaver architectures such as second/third asymmetric Mach–Zehnder interferometers (AMZIs); one-, two-, three-ring-assisted AMZIs (RAMZIs); and cascaded second-order ring filters on the same heterogeneous III–V/Si chip. Each design has advantages/disadvantages in terms of flatness of passband, passband roll-off, and IL. These designs should address three main desired requirements of (de-)interleavers: (1) low IL such that the optical power budget is minimized, (2) passband widths are wide enough to accommodate OFC drift and temperature fluctuation such that minimal wavelength tuning is required, and (3) there is a way to efficiently correct for fabrication imperfections which are an issue for high-index platforms such as silicon photonics. The use of heterogeneously integrated MOSCAP (de-)interleavers not only addresses these three issues, but also allows for convenient integration of low-power OFC sources [7], SOAs [11], modulators [12,13], and photodetectors [6,20–23], all with identical III–V/Si material

stack and coherent fabrication flow. In addition, the ultra-energy-efficient MOSCAP (de-)interleavers presented here offer a building block that will benefit a wide range of applications such as DWDM/CWDM (de-)multiplexers [24–30], programmable photonics [31–36], RF photonics [37,38], and optical neural networks [39,40].

In this paper, we demonstrate various second/third-order MOSCAP AMZI and one-, two-, three-ring-assisted MOSCAP AMZI (de-)interleavers operating at O-band wavelengths with 65 GHz channel spacing. The second/third-order AMZI (de-)interleavers show channel XT of -22 to -15 dB and -32 to -22 dB, respectively, with an associated power consumption of 83.0 and 53.0 nW. The power consumption for the second-order MOSCAP AMZI is 2.77×10^5 smaller than that of a similar AMZI-based interleaver using thermal re-configuration [18]. The one-, two-, three-ring-assisted AMZI (de-)interleavers exhibit channel XT of -27 to -16 dB, -22 to -21 dB, and -20 to -18 dB, respectively, with associated power consumption of 10.0, 7220.0, and 33.6 nW. The power consumption for the one-ring-assisted MOSCAP AMZI is 2.55×10^6 smaller than that of other similar reported (de-)interleavers with thermal tuning [19]. To the best of our knowledge, we have demonstrated for the first time, III–V/Si MOSCAP (de-)interleavers with the lowest XT and reconfiguration power consumption. Such an athermal phase tuning process results in zero thermal XT to adjacent devices, e.g., micro-ring modulators, and facilitates dense integration. Table 1 shows a list of current state-of-the-art silicon-based (de-)interleavers, their performance, and the work presented in this paper. A comparison of different material systems (silica, silicon nitride) was not made due to several reasons: (1) these devices are significantly larger in footprint; (2) difficulty with active device integration; and (3) significantly larger power consumption when aligning to OFC spectra. The designs in this work can be adapted for C-band wavelengths ($\lambda_0 = 1550$ nm) with $\sim 1.4 \times$ phase tuning efficiency, but with the cost of an equally incurred factor of optical loss. This will be discussed in Section 2.

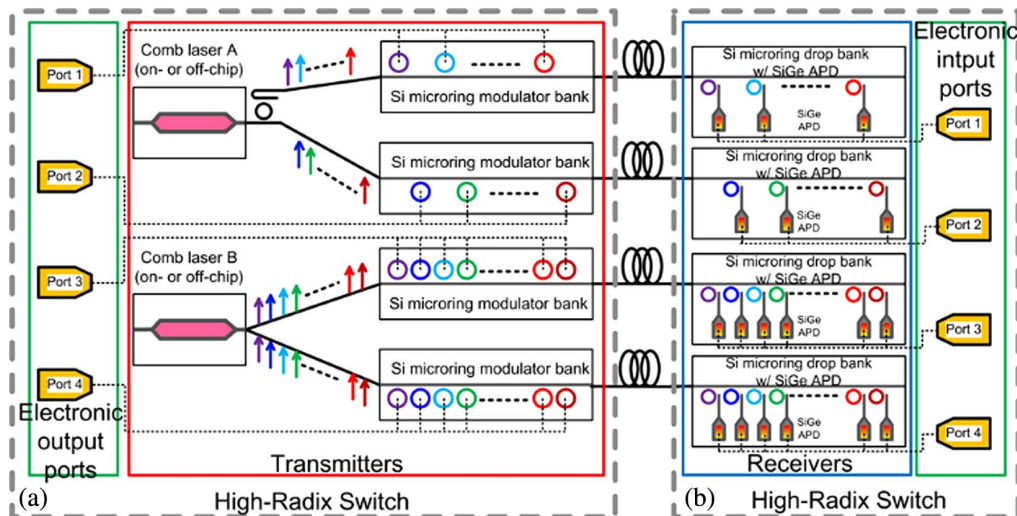


Fig. 1. (a) Schematic of envisioned DWDM architecture with integrated OFC, MOSCAP (de-)interleaver, MRRs, and photodetectors; (b) (de-)interleaver after comb-source [6].

Table 1. Complete Survey of State-of-the-Art DWDM Si (De-)interleavers

Authors	Device Type	Material	Wave. (μm)	Sep. (GHz)	XT (dB)	IL (dB)	Tuning Pow. (mW)
Q. Deng	2nd-order AMZI	Si	1.55	1838	-15	0.4	0
A. Rizzo [16]	1-ring RAMZI	Si	1.55	400	-15	<1	N/A
S. Lai [41]	SCOW	Si	1.55	100	-20	8	0
J. F. Song [19]	1-ring RAMZI	Si	1.55	178	-22	8	25.5
N. Zhou [18]	MZI-SLM	Si	1.55	56	N/A	<1	23
J. F. Song [42]	1-ring RAMZI	Si	1.55	1250	-7 to -10	10	0
J. F. Song [43]	1-ring RAMZI	Si	1.55	250	< -10	8	0
L. W. Luo [14]	3-ring RAMZI	Si	1.55	120	-20	8	5
M. Cherchi [44]	2nd-order AMZI	Si	1.55	1875	-22	3	0
M. Cherchi [45]	1-ring RAMZI	Si	1.55	125	-9 to -18	3	0
X. Jiang [46]	MZI-SLM	Si	1.55	123	-20	<1	N/A
This work	2nd-order AMZI	III-V/Al ₂ O ₃ /Si	1.31	65	-22 to -15	2	0.000083
This work	3rd-order AMZI	III-V/Al ₂ O ₃ /Si	1.31	65	-32 to -22	1.4	0.000053
This work	1-ring RAMZI	III-V/Al ₂ O ₃ /Si	1.31	65	-27 to -16	1.8	0.000010
This work	2-ring RAMZI	III-V/Al ₂ O ₃ /Si	1.31	65	-22 to -21	2.0	0.00722
This work	3-ring RAMZI	III-V/Al ₂ O ₃ /Si	1.31	65	-20 to -18	4.4	0.000034

2. III-V/Si HETEROGENEOUS DWDM PLATFORM DESIGN

For silicon photonic filters, it is critical to understand the process variability on device performance which eventually affects electrical power consumption from the choice of phase tuning. Phase-sensitive devices such as arrayed waveguide gratings (AWGs), lattice filters, and (de-)interleavers can be sensitive to phase errors and are dependent on waveguide width, thickness, and refractive index non-homogeneity. The change in resonant wavelength can be determined by the following equation: $\Delta\lambda_0 = (\lambda_0/n_g) \sqrt{[(dn_{\text{eff}}/dw) \cdot \Delta w]^2 + [(dn_{\text{eff}}/dt) \cdot \Delta t]^2}$, where λ_0 , n_{eff} , n_g , Δw , and Δt are the free-space wavelength,

effective index, group index, width variation, and thickness variation, respectively. By taking into account the group index defined as $n_g = n_{\text{eff}} - \lambda_0 \cdot dn_{\text{eff}}/d\lambda$, the resonant wavelength shift for each dimensional variation can be calculated by the following: $\Delta\lambda_0/\Delta w = (\lambda_0/n_g)(dn_{\text{eff}}/dw)$ and $\Delta\lambda_0/\Delta t = (\lambda_0/n_g)(dn_{\text{eff}}/dt)$. In Figs. 2(a) and 2(b), the effective index and group index are plotted, and it can be observed these two parameters monotonically increase as waveguide dimensions increase because modal confinement becomes larger within the silicon material. Figures 2(c) and 2(d) show that width sensitivity (dn_{eff}/dw) and thickness sensitivity (dn_{eff}/dt) of the effective index are decoupled when the width

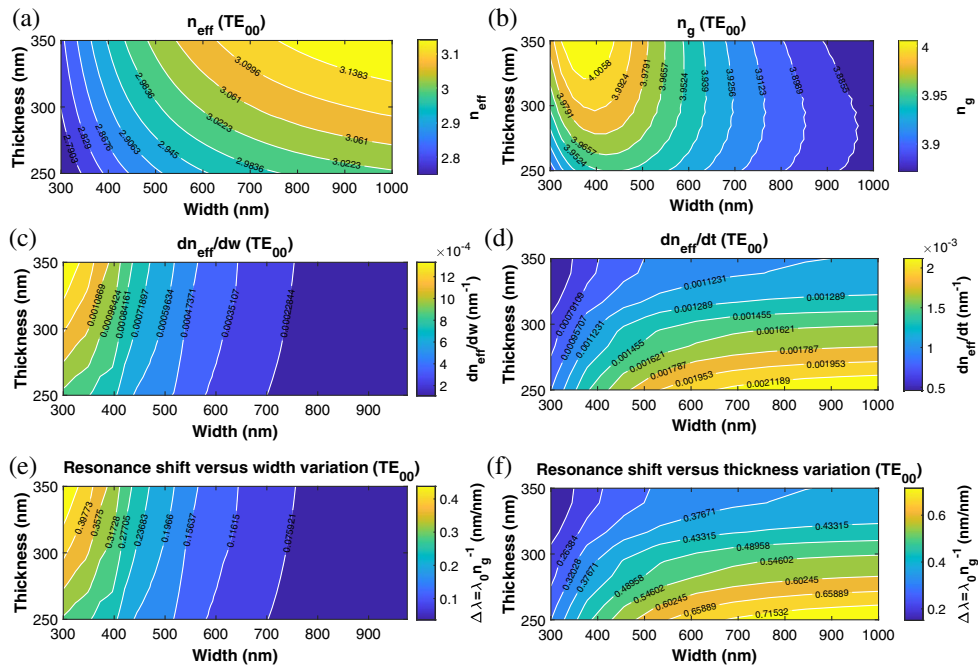


Fig. 2. Finite difference eigen-mode (FDE) calculations for (a) effective index (n_{eff}), (b) group index (n_g), (c) effective index change versus width (dn_{eff}/dw), (d) effective index change versus thickness (dn_{eff}/dt), (e) wavelength shift versus width ($\Delta\lambda_0/\Delta w$), (f) wavelength shift versus thickness ($\Delta\lambda_0/\Delta t$).

>500 nm and thickness >300 nm for $\lambda_0 = 1310$ nm. Throughout this paper, single-mode silicon rib waveguides are used and have design dimensions of height = 300 nm, width = 500 nm, and etch depth (ED) = 170 nm, thus resulting in effective index variations of $dn_{\text{eff}}/dw = 5.974 \times 10^{-4} \text{ nm}^{-1}$ and $dn_{\text{eff}}/dt = 1.338 \times 10^{-3} \text{ nm}^{-1}$. As a result, the most critical parameter in controlling phase errors is the starting wafer thickness uniformity. The wavelength shift variation also follows the same trend with $\Delta\lambda_0/dw = 0.1968 \text{ nm/nm}$ and $\Delta\lambda_0/dt = 0.4418 \text{ nm/nm}$, as shown in Figs. 2(e) and 2(f). Use of thicker waveguides will significantly reduce the effective index variation due to silicon thickness variations (dn_{eff}/dt); however, to maintain single-mode operation, the width will have to be narrower, which works against lowering overall dimensional sensitivity and becomes more sensitive to waveguide sidewall roughness. In fact, the silicon waveguide dimensions used throughout this paper offer the best design trade-off in terms of low dn_{eff}/dw and dn_{eff}/dt while maintaining single-mode operation. In the design section, we will show how local width variations from dn_{eff}/dt can significantly affect passband XT.

In order to correct for phase errors due to silicon waveguide non-uniformity (dn_{eff}/dw , dn_{eff}/dt), we explored the use of ultra-power-efficient MOSCAP phase tuners based on a GaAs/dielectric/Si heterogeneous structure, as shown in Figs. 3(a)–3(c). There exists a number of high- k dielectrics [47] such as Ta_2O_5 ($k \sim 22$), TiO_2 ($k \sim 80$), SrTiO_3 ($k \sim 2000$), and ZrO_2 ($k \sim 25$), to name a few; however, we focused on Al_2O_3 ($k \sim 9$) and HfO_2 ($k \sim 25$) here due to well-studied property and availability of the in-house fabrication. Between these two, HfO_2 should exhibit 2–3 times the capacitance of Al_2O_3 for the same given area and dielectric thickness. In order to determine the effective optical phase shift due to electron and hole induced effects, we simulated the heterogeneous MOSCAP structure which takes into account the free-carrier absorption (FCA), plasma dispersion, and bandgap shrinkage. The change in material index Δn and FCA $\Delta\alpha$ due to voltage-dependent plasma dispersion effect can be described by the classic Drude model [10,48]. The associated index and FCA values are directly proportional to the square of the operating wavelength and thus at C-band wavelengths ($\lambda_0 = 1550$ nm), Δn and $\Delta\alpha$ should increase by $\sim 1.4\times$ compared to O-band wavelengths ($\lambda_0 = 1310$ nm). Due to $\sim 4\times$ smaller electron effective mass and $\sim 6\times$ larger electron mobility of GaAs ($m_e^* = 0.063m_0$, $\mu_e = 8500 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$) than that of crystalline Si ($m_e^* = 0.28m_0$, $\mu_e = 1400 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$), the plasma dispersion effect on index change in n-type GaAs is more efficient with lower

FCA losses. The similarity in hole parameters for these materials is quite similar in terms of index change; the fact that GaAs hole-induced inter-valence band absorption is larger than that of Si also precludes the use of p-GaAs [10]. In addition, the effects of optical phase shifts from Si free-carrier holes are larger than from electrons which make the choice of using p-Si a convenient one [48]. InP ($m_e^* = 0.07m_0$, $\mu_e = 5400 \text{ cm}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$) is another III–V choice; however, GaAs performs better in terms of FCA losses and slightly better for Δn change. These design choices align with conventional heterogeneous III–V/Si active device (laser, amplifier, and photodetector) structures where the III–V p-i-n layer stack is positioned from top to bottom after wafer bonding, such that the n-layer serves as the diode cathode terminal and MOSCAP gate layer simultaneously. Our recent strategic change from previous InP/InAlGaAs-based quantum well lasers/amplifiers to GaAs/InAs-based quantum-dot (QD) is another pleasant coincidence for improved phase tuning and lower FCA losses. As a result, there is no need to alter the conventional III–V epitaxial structure and fabrication process to integrate highly efficient MOSCAP in our heterogeneous GaAs/Si QD-powered platform.

Our single-mode waveguide structure is defined by a width, height, and etch depth of 500, 300, and 170 nm, respectively, as indicated in Fig. 3(a). The wafer-bonded III–V region is primarily 190 nm thick and n-GaAs doped at $3 \times 10^{18} \text{ cm}^{-3}$. Figures 4(a) and 4(b) show the simulated transverse electric (TE) effective index change (Δn_{TE00}) and the associated FCA optical losses for both n-GaAs/ Al_2O_3 /p-Si and n-GaAs/ HfO_2 /p-Si structures for various values of dielectric thicknesses at a forward bias. Assuming a 5 nm thick Al_2O_3 with a refractive index of $n_{\text{Al}_2\text{O}_3} = 1.75$, the calculated optical confinement factors are $\Gamma_{\text{Al}_2\text{O}_3} = 1.15\%$ and $\Gamma_{\text{III-V}} = 28.2\%$ with an overall effective index of $n_{\text{eff}} = 3.1144$. A 5 nm thick HfO_2 with a refractive index of $n_{\text{HfO}_2} = 1.88$ has optical confinements of $\Gamma_{\text{HfO}_2} = 1.15\%$ and $\Gamma_{\text{III-V}} = 28.3\%$ with an overall effective index of $n_{\text{eff}} = 3.1154$. In forward bias, the MOSCAP structure operates in accumulation mode to achieve maximal plasma dispersion effect over other modes. If we assume a length of $L = 500 \mu\text{m}$, wavelength of $\lambda = 1.31 \mu\text{m}$, and an Al_2O_3 thickness of 5, 10, 15 nm, the corresponding static $V_{\pi}L = 0.13, 0.21, 0.29 \text{ V}\cdot\text{cm}$, respectively, based on $\Delta\varphi = 2\pi\Delta n_{\text{TE00}}L/\lambda$, where $\Delta\varphi$ is the phase change from MOSCAP carrier-dependent index Δn_{TE00} . For a high- k -dielectric such as HfO_2 , a thickness of 5, 10, 15 nm corresponds to a static $V_{\pi}L = 0.09, 0.12, 0.16 \text{ V}\cdot\text{cm}$, considerably more efficient than Al_2O_3 . In both structures, a thinner dielectric thickness yields increased phase tuning efficiency at the expense of increased FCA and fabrication challenge to maintain a negligible leakage current.

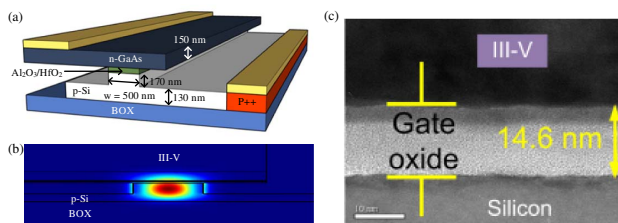


Fig. 3. (a) 3D schematic of the heterogeneous III–V/Si MOSCAP tuner, (b) simulated TE optical mode for a HfO_2 dielectric interface, and (c) TEM image of a GaAs/dielectric/Si interface as an example.

3. III–V/Si MOSCAP (DE-)INTERLEAVER DESIGN

A. N th-Order AMZI (De-)interleavers

It is well known that N th-order AMZI (de-)interleavers have been shown to have low loss and a flat-top filtering response [49]. There have been a few variations on the SOI (silicon-on-insulator) platform, particularly at the 1550 nm wavelength bands [44,45], with channel spacing range of 56–1875 GHz and channel XT down to -22 dB with thermal tuning [18].

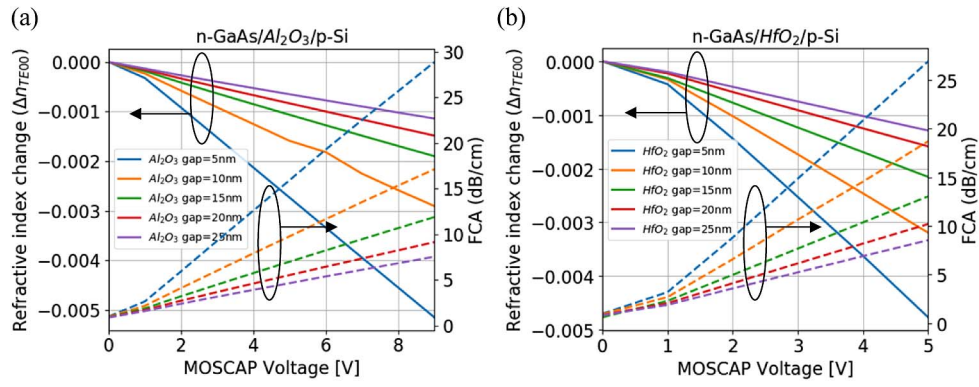


Fig. 4. Simulated refractive index change and FCA losses for (a) n-GaAs/Al₂O₃/p-Si, and (b) n-GaAs/HfO₂/p-Si for gap thicknesses of 5, 10, 15, 20, and 25 nm. Layer doping: n-GaAs ($3 \times 10^{18} \text{ cm}^{-2}$), n-Al_{0.20}Ga_{0.80}As ($3 \times 10^{18} \text{ cm}^{-2}$), Si ($5 \times 10^{16} \text{ cm}^{-2}$).

An N th-order (de-)interleaver consists of N differential path lengths and $N + 1$ power splitters. The N th-order AMZI (de-)interleavers are modeled with a transfer matrix model where a single MZI is defined as $\Phi_{\text{MZI}} = \Phi_{\text{cplr}}(\kappa_2)\Phi_{\text{delay}}\Phi_{\text{cplr}}(\kappa_1)$, where [49]

$$\Phi_{\text{MZI}} = \begin{bmatrix} c_1(\lambda) & -js_1(\lambda) \\ -js_1(\lambda) & c_1(\lambda) \end{bmatrix} \begin{bmatrix} e^{-j2\pi n_g(\lambda)\Delta L_1/\lambda} & 0 \\ 0 & 1 \end{bmatrix} \times \begin{bmatrix} c_0(\lambda) & -js_0(\lambda) \\ -js_0(\lambda) & c_0(\lambda) \end{bmatrix}. \quad (1a)$$

The through and cross port transmissions are respectively defined as $c_{0,1} = \sqrt{1 - \kappa_{0,1}(\lambda)}$ and $-js_{0,1} = -j\sqrt{\kappa_{0,1}(\lambda)}$, where $\kappa_{1,2}$ is the power coupling coefficient for each coupler and ΔL_1 is the path length difference that determines the FSR. The FSR of a (de-)interleaver is twice the intended channel spacing where $\text{FSR} = \lambda_{\text{target}}/n_g\Delta L$. ΔL is the base optical path length difference between the upper and lower arms. Therefore, a channel spacing of 65 GHz for the second-order AMZI requires $\Delta L = 610.5 \mu\text{m}$ for a calculated group index of $n_g = 3.78$. The transfer function of a higher N th-order AMZI can be derived by simply cascading another set of matrices such that the transfer function can be succinctly defined as

$$\Phi_N = \begin{bmatrix} e^{-j2\pi n_g(\lambda)\Delta L_N/\lambda} & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} c_N(\lambda) & -js_N(\lambda) \\ -js_N(\lambda) & c_N(\lambda) \end{bmatrix} = \begin{bmatrix} c_N(\lambda)e^{-j2\pi n_g(\lambda)\Delta L_N/\lambda} & -js_N(\lambda)e^{-j2\pi n_g(\lambda)\Delta L_N/\lambda} \\ -js_N(\lambda) & c_N(\lambda) \end{bmatrix}, \quad (1b)$$

$$\Phi_{N\text{order AMZI}} = \begin{bmatrix} c_{N+1}(\lambda) & -js_{N+1}(\lambda) \\ -js_{N+1}(\lambda) & c_{N+1}(\lambda) \end{bmatrix} \Phi_N \dots \Phi_0. \quad (1c)$$

For a second-order AMZI, the second arm delay is defined as $\Delta L_2 = 2\Delta L_1$ with coupling coefficients c_1, c_2, c_3 being 0.50, 0.29, 0.08, respectively. Table 2 outlines these design parameters. The power splitters implemented throughout this paper are standard directional couplers, and dimensional tolerances were examined for performance robustness in terms of $\Delta\kappa^2/\Delta\Omega$ where κ is the field coupling coefficient and Ω is either width (w), gap (g), or ED. For a 50% power coupler, the percent difference from nominal was $\pm 9\%/40 \text{ nm}$,

Table 2. Design Summary of III-V/Si MOSCAP N th-Order AMZI (De-)interleavers

Design	ΔL (μm)	c_1	c_2	c_3	c_4
2nd-order AMZI	610.5	0.50	0.29	0.08	–
3rd-order AMZI	610.5	0.50	0.19	0.19	0.025

$\pm 11\%/40 \text{ nm}$, and $\pm 20\%/40 \text{ nm}$ for $\Delta\kappa^2/\Delta w$, $\Delta\kappa^2/\Delta g$, and $\Delta\kappa^2/\Delta\text{ED}$, respectively. Etch control is expected to be $\pm 10 \text{ nm}$, which made the choice of using directional couplers a reasonable one due to its lower backreflection, albeit more spectrally narrow band than a multi-mode interferometer (MMI) coupler. On the other hand, broadband directional couplers have been demonstrated [50] and offer an alternative to traditional MMIs with limited arbitrary splitting ratios. Arbitrary power splitting ratios for MMIs have been demonstrated [44,45], but were not used to de-risk design complexity.

Figures 5(a) and 5(b) show the calculated optical response of a second-order and third-order AMZI for the cases of with and without phase tuning. The solid lines indicate the case for perfect phase tuning whereas the dashed lines indicate the case for a π phase offset. Phase errors are an unavoidable issue in high-contrast-index systems such as the SOI platform and usually require power-hungry thermal phase tuning to achieve the expected response in phase-sensitive devices such as (de-)interleavers, AWGs, and lattice filters. For the second-order AMZI, we assume an effective width variation of $\Delta w = 2 \text{ nm}$ between the two delay paths due to sidewall roughness and variability. This results in a variation of $\Delta n_{\text{eff}} = 0.0011$ ($dn_{\text{eff}}/dw = 5.974 \times 10^{-4} \text{ nm}^{-1}$), which is large enough to increase theoretical XT by 13 dB. It will be shown in the experimental section that the III-V/Si MOSCAP tuning can significantly improve channel XT with ultra-low power consumption. Third-order AMZIs exhibit wider 1 dB channel bandwidth due to the increased passband roll-off compared to a second-order AMZI, however, at the cost of increased waveguide loss due to a third delay arm. An extra coupler also makes it more challenging to realize than a second-order AMZI. As can be seen in the insets of Figs. 5(a) and 5(b), the long delay paths for each (de-)interleaver design were made in a spiral geometry to minimize footprint size.

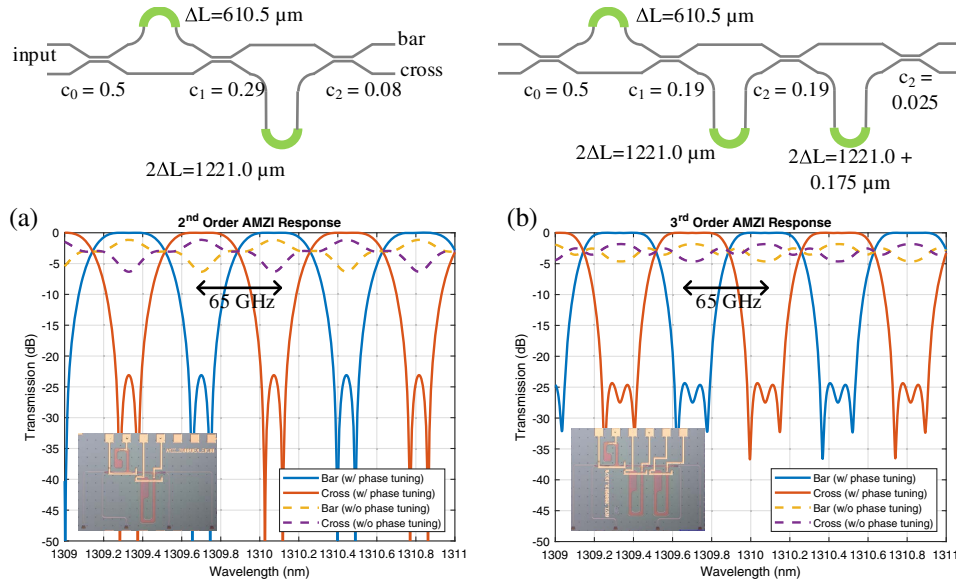


Fig. 5. 65 GHz (de-)interleaver transmission response with and without MOSCAP phase tuning for (a) second-order AMZI and (b) third-order AMZI.

B. *N*-Ring-Assisted AMZI (De-)interleavers

Ring-assisted AMZIs with N rings have been shown to exhibit wider flat-top response with improved channel XT [51]. There have been a number of demonstrations on silicon with channel spacing ranging from 120 to 1250 GHz and channel XT ~ -22 dB [16,19,42,43,45,51]. The one-ring-assisted AMZI (one-ring RAMZI) is modeled by [49]

$$\Phi_{1\text{-ring RAMZI}} = \begin{bmatrix} c_1(\lambda) & -js_1(\lambda) \\ -js_1(\lambda) & c_1(\lambda) \end{bmatrix} \begin{bmatrix} A^R(z)/A(z) & 0 \\ 0 & e^{j2\pi n_g(\lambda)L_{\text{ring}}/\lambda} \end{bmatrix} \times \begin{bmatrix} c_0(\lambda) & -js_0(\lambda) \\ -js_0(\lambda) & c_0(\lambda) \end{bmatrix}, \quad (2a)$$

$$A^R(z) = \sqrt{1 - \kappa_r} + (e^{j2\pi n_g(\lambda)L_{\text{ring}}/\lambda})^{-2}, \quad (2b)$$

$$A(z) = 1 + \sqrt{1 - \kappa_r} (e^{j2\pi n_g(\lambda)L_{\text{ring}}/\lambda})^{-2}. \quad (2c)$$

The AMZI bar and cross port transmissions are respectively defined similarly for a first-order AMZI filter with the addition that κ_r is the ring coupling coefficient. The FSR is defined by the ring circumference such that the $\text{FSR} = c/n_g L$. Therefore, a channel spacing of 65 GHz for the one-ring RAMZI requires $L_{\text{ring}} = 1200 \mu\text{m}$ for a calculated group index of $n_g = 3.78$. The transfer function of an N -ring RAMZI can be derived by simply cascading another set of matrices such that the transfer function can be succinctly defined as

$$\Phi_{N\text{-ring RAMZI}} = \begin{bmatrix} c_1(\lambda) & -js_1(\lambda) \\ -js_1(\lambda) & c_1(\lambda) \end{bmatrix} \Phi_N \times \dots \times \begin{bmatrix} A_1^R(z)/A_1(z) & 0 \\ 0 & e^{j2\pi n_g(\lambda)L_{\text{ring}}^N/\lambda} \end{bmatrix} \times \begin{bmatrix} c_0(\lambda) & -js_0(\lambda) \\ -js_0(\lambda) & c_0(\lambda) \end{bmatrix}, \quad (2d)$$

$$\Phi_N = \begin{bmatrix} 1 & 0 \\ 0 & A_N^R(z)/A_N(z) \end{bmatrix}, \quad (2e)$$

$$A_N^R(z) = \sqrt{1 - \kappa_r^N} + (e^{j2\pi n_g(\lambda)L_{\text{ring}}^N/\lambda})^{-2},$$

$$A_N(z) = 1 + \sqrt{1 - \kappa_r^N} (e^{j2\pi n_g(\lambda)L_{\text{ring}}^N/\lambda})^{-2}. \quad (2f)$$

The ideal ring resonator coupling for one-ring RAMZI, two-ring RAMZI, and three-ring RAMZI occurs at $\kappa_r = 0.89$; $\kappa_r^{1,2} = 0.97, 0.62$; and $\kappa_r^{1,2,3} = 0.96, 0.68, 0.25$, respectively. Table 3 outlines these design parameters.

Figures 6(a)–6(c) show the calculated optical responses of the one-, two-, and three-ring RAMZIs for the case of with and without phase tuning. Again, the solid lines indicate the case for perfect phase tuning whereas the dashed lines indicate the case for a π phase offset for each of the rings. The result of successive ring resonator loading on one or both AMZI arms allows for broadband and flat passbands at the expense of complicated coupler and phase control, as well as increased optical losses on fabricated devices. Optical insertion loss has the effect of reducing the flatness of the passbands; therefore, waveguide scattering should be kept to a minimum in fabricated devices. In fact, passband flatness is much more sensitive to loss than to alternative N th-order AMZI designs. Simulations show that for every additional 10 dB/cm loss incurred, there will be a 14% reduction of the 0.5 dB bandwidth.

For the one-ring RAMZI, we assume an effective width variation of $\Delta w = 2$ nm between the ring circumference and delay

Table 3. Design Summary of III–V/Si MOSCAP One-, Two-, Three-ring RAMZI (De-)interleavers

Design	L_{ring} (μm)	κ_r^1	κ_r^2	κ_r^3	c_0	c_1
1-ring RAMZI	1200	0.89	–	–	0.50	0.50
2-ring RAMZI	1200	0.97	0.62	–	0.50	0.50
3-ring RAMZI	1200	0.96	0.68	0.25	0.50	0.50

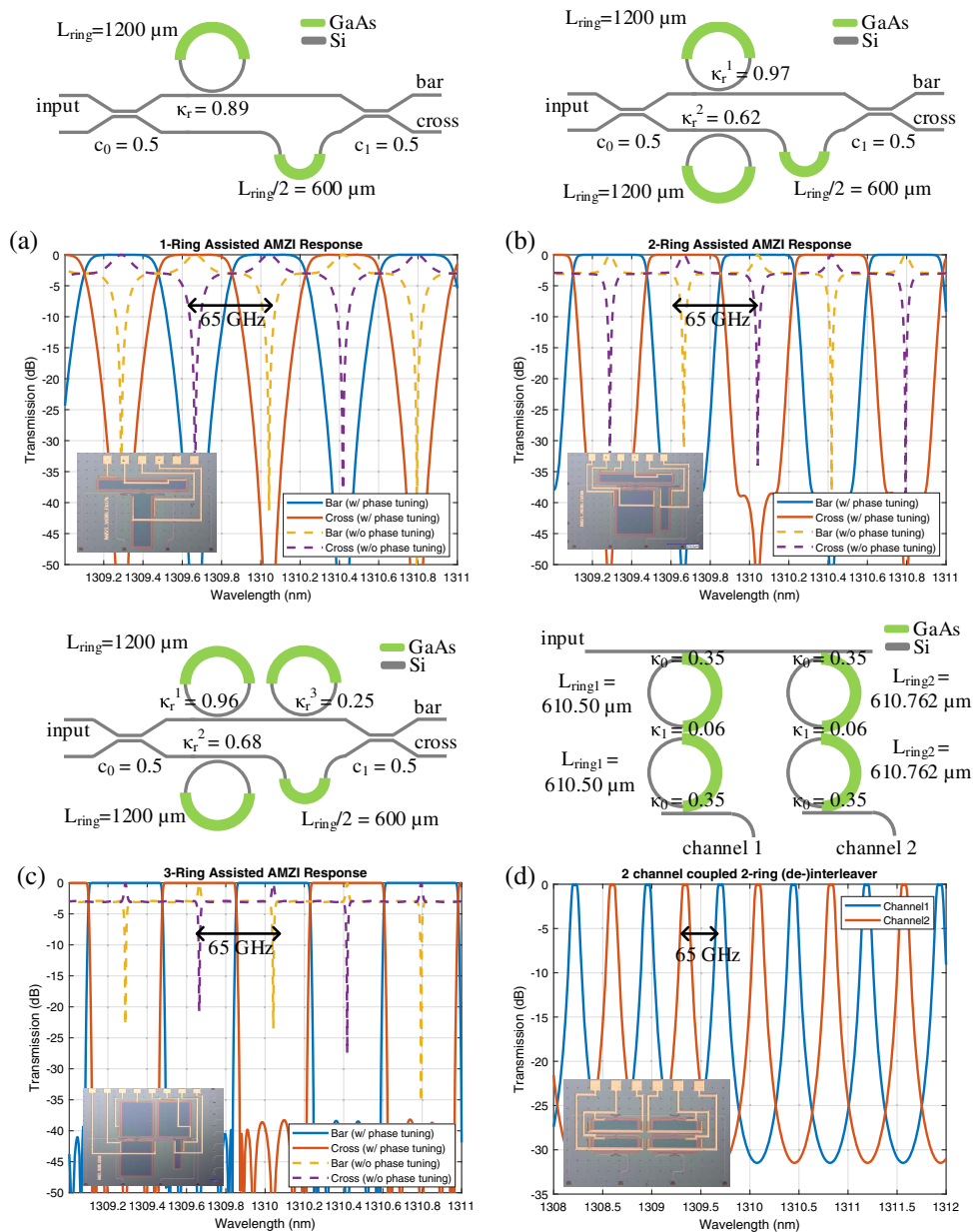


Fig. 6. 65 GHz (de-)interleaver transmission response with and without MOSCAP phase tuning for (a) one-ring RAMZI, (b) two-ring RAMZI, (c) three-ring RAMZI, and (d) two-channel coupled two-ring resonator.

path due to sidewall roughness and variability. A local effective index variation of $\Delta n_{eff} = 0.0011$ ($dn_{eff}/dw = 5.974 \times 10^{-4} \text{ nm}^{-1}$) is found to be large enough to increase theoretical XT by >40 dB. Also, waveguide and power coupler dispersion will have to be taken into account such that perturbations in the delay arm of the ring-assisted AMZI will be minimum for ultra-broadband operation (>100 nm). In Fig. 6(d), for completeness, we included the design of a 65 GHz (de-)interleaver based on the use of two second-order ring resonators for each drop channel. The details of the design can be found in Refs. [52–55], but the purpose is to illustrate that AMZI-based designs are the more appropriate design choice based on wider channel passbands.

4. FABRICATION

Experimentally, we explored three different MOSCAP gate oxide designs with varying degrees of silicon doping and a dielectric selection of Al_2O_3 and/or HfO_2 . These designs are shown in Table 4. The HfO_2 dielectric has a higher dielectric constant ($k \sim 25$) compared to Al_2O_3 ($k \sim 9$); therefore, a HfO_2 -based

Table 4. Fabricated Platform Variations

Design Name	Si Doping (cm^{-3})	Gate Type
Design 1	4×10^{16}	Al_2O_3 (6 nm)
Design 2	5×10^{17}	$\text{HfO}_2/\text{Al}_2\text{O}_3$ (10/3 nm)
Design 3	u.i.d.	$\text{HfO}_2/\text{Al}_2\text{O}_3$ (5.4/3 nm)

capacitor should have $\sim 3\times$ the capacitance for the same unit area. However, the measured thickness of HfO_2 in Design 2 is 10 nm with an additional 3 nm of Al_2O_3 , which indicates the capacitance is only $\sim 1.7\times$ or less compared to Design 1. The majority of measured (de-)interleavers in this paper are based on Designs 1 and 2.

In-house device fabrication begins with a 100 mm SOI wafer which consists of a 350 nm thick top silicon layer and a 2 μm buried oxide layer, as shown in Fig. 3(a). The top silicon is thinned down to 300 nm by thermal oxidation and buffered hydrofluoric acid etching, thus leaving a clean silicon surface. Doped silicon regions are defined by a deep-UV (248 nm) lithography stepper, and boron is implanted to create p-Si regions. Grating couplers, silicon rib waveguides, and vertical out-gassing channels [56] are respectively patterned using the same deep-UV stepper and then subsequently etched 170 nm with Cl_2 -based gas chemistry. Next, a dielectric of Al_2O_3 and/or HfO_2 is deposited on both the patterned silicon and GaAs/AlGaAs epi-wafer via atomic layer deposition. The two wafers are then wafer bonded. An $\text{Al}_{0.20}\text{Ga}_{0.80}\text{As}$ etch stop layer allows selective substrate removal via wet etching, thus leaving a 150 nm thick n-GaAs thin film on top of the SOI substrate. Next, the III–V film is defined and dry etched for device regions that require the use of MOSCAP phase tuning. The III–V film covers all of the delay lengths or ring resonators up until the ends of the directional couplers. Next, a plasma-enhanced chemical vapor deposition SiO_2 cladding is deposited and vias are defined and etched. Finally, the n-contact and p-contact are defined on the n-GaAs and p-Si layers, respectively. Optical microscopic images of the various fabricated (de-)interleaver devices are shown in Figs. 7(a)–7(f).

5. (DE-)INTERLEAVER CHARACTERIZATION AND MEASUREMENTS

A. Measurement Preliminaries

The spectral responses of the MOSCAP (de-)interleavers were characterized with a Thorlabs superluminescent diode capable

of 40 nm bandwidth (1290–1330 nm) and a launch power of 12 dBm. The 100 mm wafer is vacuum mounted onto a stainless-steel chuck on a semi-automatic probe station. Light is vertically coupled in/out of devices via grating couplers and a 7° polished fiber array with 250 μm pitch. Polarization control is performed with the use of a polarization controller and maximized for peak transmission on a straight test waveguide. All measured (de-)interleaver responses are normalized to a 0.5 μm wide straight waveguide with a length of $\sim 850 \mu\text{m}$. However, this is not the same length as the various (de-)interleaver designs measured throughout this paper. The pre-bonded 0.5 μm wide straight waveguide TE losses were determined to be about $\sim 9.2 \text{ dB/cm}$ for a wavelength of 1310 nm from a series of cut-back test structures. 0.8 μm wide straight waveguide TE losses were about 9.8 dB/cm. After III–V removal, TE waveguide losses were measured to be ~ 21.1 and 42.2 dB/cm for the 0.8 and 0.5 μm wide waveguides, respectively. TE waveguide losses with wafer-bonded III–V regions are undetermined due to the absence of these types of test structures. Circular bends of radius 2, 5, 7, 14 μm had bend losses 1.22, 0.83, 0.3, 0.08 dB/90° bend, respectively. Spline bends with a radius of 4 μm and angles of 45°, 10°, 5° have measured bend losses of 0.23, 0.86, 1.09 dB/bend, respectively. The angular coverage of a spline bend is discussed at length in Ref. [57]. A spline bend with radius of 2 μm exhibited a bend loss of 0.74 dB/bend. Grating coupler losses before and after bonding were calculated to be 7.7 and 7.8 dB/coupler (using single cleaved fibers angled at 13°) indicating negligible effect after III–V removal. The use of a 7° polished fiber array for the (de-)interleaver experiments is expected to significantly increase coupling losses to $\sim 14 \text{ dB/coupler}$. Initial phase tuning measurements were performed on a 350 μm long p-Si/ Al_2O_3 (6 nm)/n-GaAs MOSCAP MZI structure and spectral responses indicated $\sim 2 \text{ nm}$ of tuning at a 2 V bias while maintaining an extinction ratio of $\sim 24 \text{ dB}$, as shown in Fig. 8(b). The measured FSR = 18.58 nm and the calculated $V_\pi L = 0.325 \text{ V}\cdot\text{cm}$ which is $4\times$ smaller than typical values seen in PN junction-based phase tuners. The observed

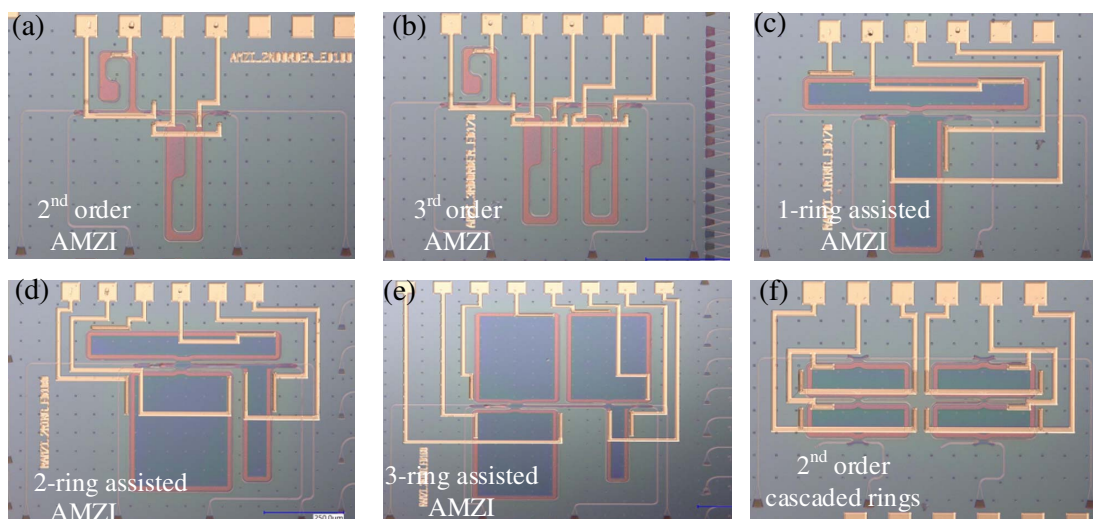


Fig. 7. Microscope images of various (de-)interleavers: (a) second-order AMZI; (b) third-order AMZI; (c)–(e) one-, two-, three-ring-assisted AMZIs; and (f) second-order cascaded rings.

leakage current appears to be smaller than the current meter limit of sub-nA, indicating negligible power consumption. The MOSCAP MZI is capable of achieving 4 Gbit/s eye diagrams and an $f_{3\text{dB}} \sim 1.5$ GHz. We also measured an MZI with HfO_2 (10 nm)/ Al_2O_3 (3 nm) dielectric; however, the $V_\pi L$ was slightly larger ($V_\pi L = 0.487 \text{ V} \cdot \text{cm}$) than the Al_2O_3 counterpart. We believe this is due to the thicker HfO_2 and Al_2O_3 capacitor or/and smaller dielectric constant in actual gate oxide thin film than their literature values. III-V/Si transitions are also important to consider since MOSCAP structures are only placed in regions where phase tuning is desired for a large-scale photonic integrated circuit. We numerically analyze the reflection and insertion loss of the III-V/Si interface as a function of angle between the III-V and Si, as shown in Fig. 8(a). Three-dimensional finite-difference time-domain simulations show minimal reflection (< -60 dB) back into the fundamental TE_{00} mode at an angle of 45° with an insertion loss of 0.36 dB/facet. Minimal insertion loss was found to occur at 72° , but with increased

reflection into the main TE_{00} mode. Throughout this paper, an angle of $\theta = 45^\circ$ is used for all fabricated and measured (de-)interleavers. Angled GaAs/Si interface losses were evaluated by cutback loss structures shown in Fig. 8(a). The interface losses for angles of 0° , 45° , and 72° were measured to be 1.08, 0.69, and 0.29 dB/facet, respectively, and match up with simulated values quite well.

B. Nth-Order AMZI (De-)interleavers

Figures 9(a) and 9(b) show the measured optical response for a second-order MOSCAP AMZI (de-)interleaver before and after phase correction, respectively. In the second-order AMZI, errors in the three coupling ratios and the path length difference ΔL can contribute to non-ideal passband response with reduced channel XT. As shown in Fig. 9(a), the cross and bar channels exhibit XT ~ -10.1 and -18.8 dB, respectively. With appropriate modeling using the transfer matrix model, it was determined the biggest error came from the first coupler c_0 being 0.35 instead of the ideal 0.50 as well as a 0.0035%

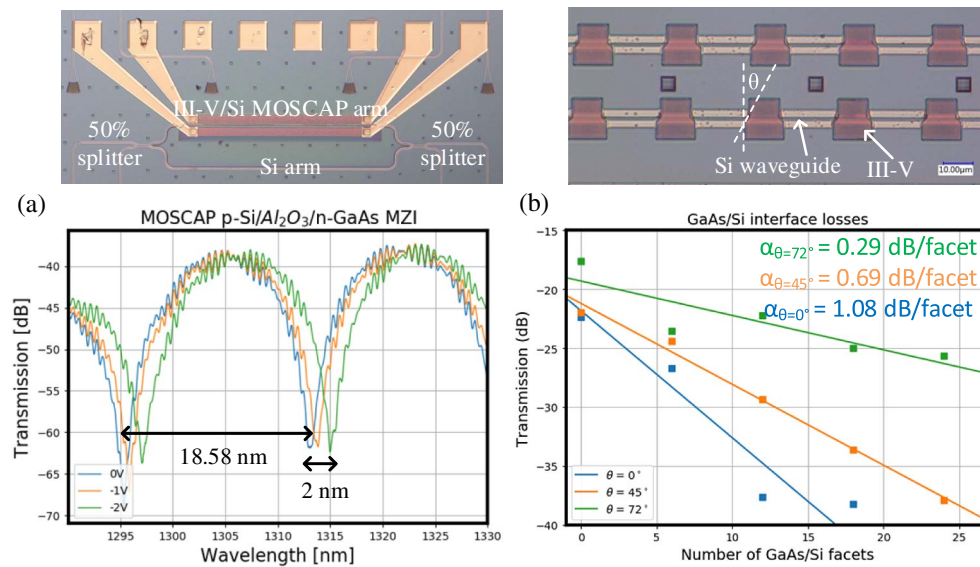


Fig. 8. (a) Microscope image of angled III-V/Si test structures and cutback loss measurements for evaluating III-V/Si transition losses, (b) image of MOSCAP MZI structure for evaluating phase tuning efficiency and optical response as a function of bias voltage.

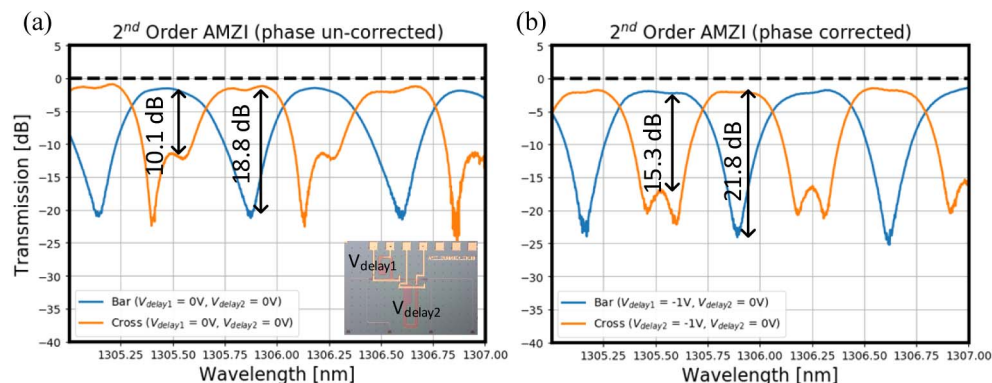


Fig. 9. Measured response of second-order MOSCAP AMZI (de-)interleaver with (a) un-corrected phase and (b) corrected phase with $V_{\text{delay1}} = -1 \text{ V}$.

error in the lengths between the first and second delay paths. The assumption of $c_0 \sim 0.35$ is further experimentally substantiated by confirming a 190 nm etch depth will yield this cross-coupling value. By applying -1 V on the first delay length ($V_{\text{delay}1}$) to correct for this error, the XT of the bar channel was improved from -10.1 dB to -15.3 dB while the cross-channel XT improved from -18.8 dB to 21.8 dB. At $V_{\text{delay}1} = -1$ V, approximately 83.0 nA was drawn resulting in a tuning power consumption of 83.0 nW. Regarding IL, the measured ILs before and after MOSCAP tuning were ~ 1.8 and 2.0 dB. Keep in mind that all (de-)interleaver structures have two III-V/Si interfaces each angled at 45° . Therefore, 1.4 dB of the total IL can be attributed to III-V/Si interface losses. The remaining losses will be due to a combination of FCA, doping absorption, and waveguide interface scattering. In the future, an angle ($\theta = 72^\circ$) with lower interface loss (0.3 dB/facet) or III-V/Si taper structure will be used. Figures 10(a) and 10(b) show the measured optical response for a third-order MOSCAP AMZI (de-)interleaver before and after phase correction, respectively. Modeling shows that the non-ideal passband transmission in Fig. 10(b) came from $c_0 = 0.35$, c_1 , $c_2 = 0.18$ along with a 0.004% error in ΔL . With the appropriate bias conditions ($V_{\text{delay}1} = 0.3$ V, $V_{\text{delay}2} = 1.0$ V, $V_{\text{delay}3} = 0.1$ V), the XT of the bar channel was improved from -7.9 dB to -27.8 dB or -32.0 dB while the cross-channel XT improved from -4.6 dB to 22.0 dB. In this case, the XT levels approach the theoretical limit, as shown in Fig. 5(b). The total tuning power consumption was calculated to be 53.0 nW.

C. N-Ring-Assisted AMZI (De-)interleavers

Figures 11(a)–11(e) show the measured optical responses for one-, two-, and three-ring-assisted MOSCAP AMZI (de-)interleavers before and after phase correction, respectively. The main source of errors for non-ideal passbands and reduced XT comes from errors in the AMZI coupling ratios (c_0 , c_1), ring coupler ratios (κ_r , κ_r^1 , κ_r^2 , κ_r^3), and the path length difference $L_{\text{ring}}/2$. Fortunately, $c_0 = c_1 = 0.50$; thus the case of the one-ring-assisted AMZI requires only two parameters to target compared to the N th-order AMZIs. As shown in Fig. 11(a), the measured bar and cross channels for the one-ring-assisted AMZI exhibit XT ~ -7.1 dB and -10.7 dB, respectively, before phase correction. By applying -2 V on the delay length (V_{delay}), the XT of

the bar channel was improved from -7.1 dB to -16.4 dB while the cross-channel XT improved from -10.7 dB to -26.6 dB. At $V_{\text{delay}} = -2$ V, approximately 5.0 nA was drawn resulting in a tuning power consumption of 10.0 nW. The passbands are far from the theoretical flat-top response shown in Fig. 6(a), which indicates a combination of waveguide loss and errors in power coupling coefficients. No flat-top response is mainly attributed to waveguide loss, whereas increase channel XT indicates non-ideal power coupling. An improved design is underway with the use of robust 50% MMI power couplers for c_0 and c_1 as well as tunable MOSCAP directional couplers for the ring resonator. The two- and three-ring-assisted AMZIs are considerably more complicated owing to the fact that there are multiple ring resonator coupling ratios that have to be targeted as well as the presence of increased optical losses. As shown in Figs. 11(c) and 11(d), the bar and cross channel XT for the two-ring-assisted AMZI is -4.3 dB and -4.7 dB, respectively, without tuning. After phase correction of the delay length, the bar and cross-channel XT improved to -20.6 dB and -21.5 dB. At a $V_{\text{delay}} = -1.9$ V, approximately 3.8 μ A was drawn resulting in a tuning power consumption of 7.2 μ W. Again, the passbands are far from the intended flat-top response shown in Fig. 6(b) and attempts were made to tune a combination of the two rings and delay; however, none resulted in the theoretical passbands. This most likely indicates errors in the coupling coefficients κ_{r1} , κ_{r2} , c_0 , and c_1 and the presence of increased optical loss beyond the 9.2 dB/cm measured before wafer-bonding. With improved reduction of losses in the future, we believe flat-top response will be achievable. The bar and cross channel XT for the three-ring-assisted AMZI in Fig. 11(e) is -6.1 dB and -4.3 dB without tuning. With $V_{\text{delay}} = -3$ V, the channel XT improved to -20.0 dB and -18.1 dB for the bar and cross channels, respectively. The reasons for non-ideal passband shapes are the same as discussed above. At a $V_{\text{delay}} = -3.0$ V, approximately 11.2 nA was drawn resulting in a tuning power consumption of 33.6 nW. The leakage current density for the two-ring-assisted AMZI is quite high and represents an outlier possibly due to design/fabrication imperfections which results in electrical conduction between tuners in some areas. Overall, the leakage current density is measured to be in the range of 1.6 – 27 μ A/cm², if the two-ring-assisted AMZI is ignored.

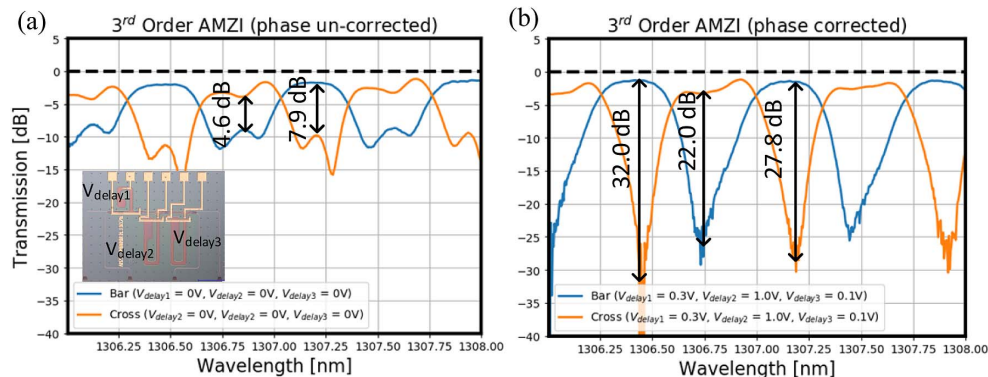


Fig. 10. Measured response of third-order MOSCAP AMZI (de-)interleaver with (a) un-corrected phase and (b) corrected phase with $V_{\text{delay}1} = 0.3$ V, $V_{\text{delay}2} = 1$ V, $V_{\text{delay}3} = 0.1$ V.

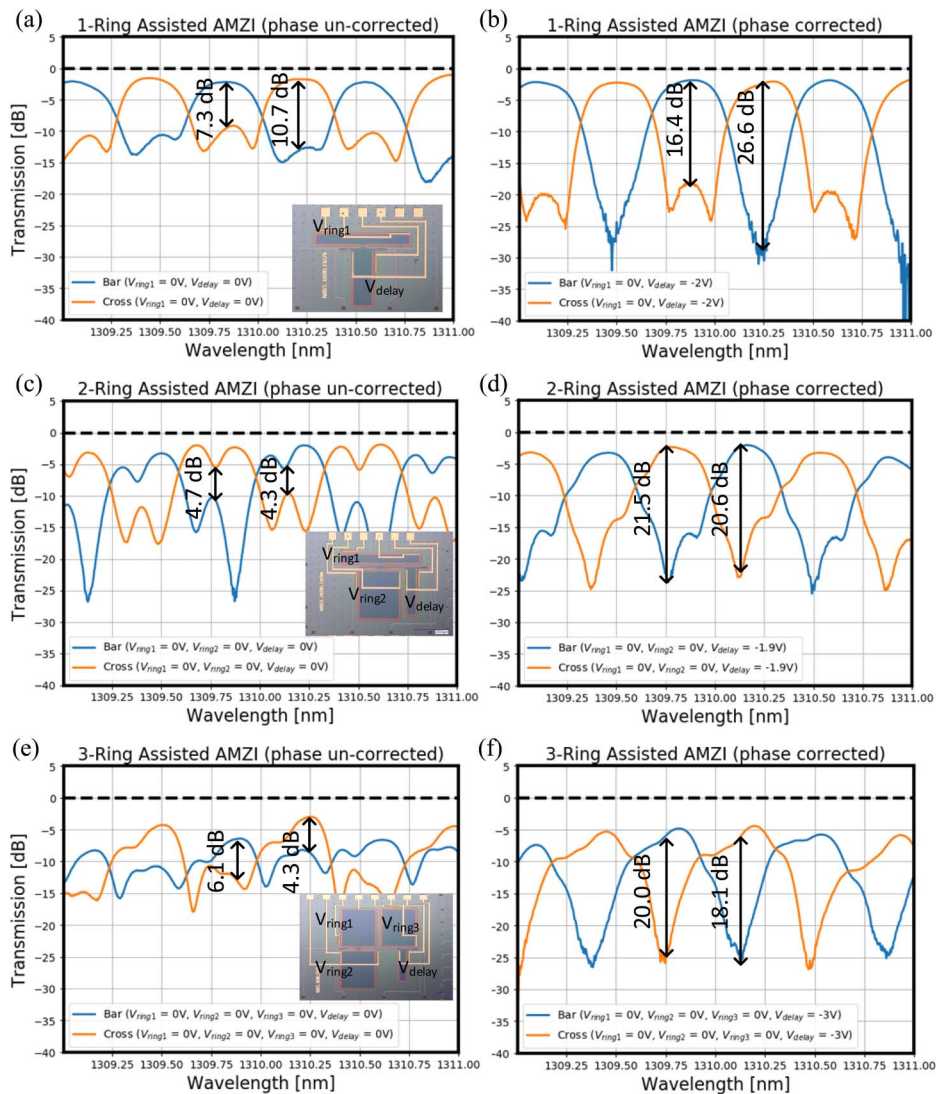


Fig. 11. Measured response of one-ring-assisted MOSCAP AMZI (de-)interleaver with (a) un-corrected phase and (b) corrected phase at $V_{\text{ring1}} = 0 \text{ V}$, $V_{\text{delay}} = -2 \text{ V}$. Measured response of two-ring-assisted MOSCAP AMZI (de-)interleaver with (c) un-corrected phase and (d) corrected phase at $V_{\text{ring1}} = 0 \text{ V}$, $V_{\text{ring2}} = 0 \text{ V}$, $V_{\text{delay}} = -2 \text{ V}$. Measured response of three-ring-assisted MOSCAP AMZI (de-)interleaver with (e) un-corrected phase and (f) corrected phase at $V_{\text{ring1}} = 0 \text{ V}$, $V_{\text{ring2}} = 0 \text{ V}$, $V_{\text{ring3}} = 0 \text{ V}$, $V_{\text{delay}} = -3 \text{ V}$.

Regarding device repeatability, the wafer-bonded III–V film was only large enough to cover four dies (each $9.4 \times 9.0 \text{ mm}$) on a 4 in. SOI wafer with a unique device in each die. We measured the one-ring-assisted AMZI from each die without MOSCAP tuning and (de-)interleaver response can vary greatly. This can be due to wafer variability, global fabrication inaccuracies such as etch depth and waveguide width which can affect either the directional couplers or path length differences. In addition, the possibility of the wafer-bonding process altering the aforementioned reasons is unknown at this time. However, we believe if the directional couplers can be substituted with fabrication robust MMIs or tunable directional couplers, device performance yield should increase significantly with minimal power consumption cost. The use of MMIs and the evaluation of tunable MOSCAP directional couplers will be underway in the near future.

6. CONCLUSION

This work demonstrates for the first time various III–V/Si MOSCAP (de-)interleavers consisting of second/third-order AMZIs and one-, two-, three-ring-assisted AMZIs with 65 GHz channel spacing at O-band wavelengths. The wafer-bonded MOSCAP structure allows for ultra-low-power phase tuning compared to thermal counterparts. Such athermal phase tuning allows more compact integration without thermal XT/impact on other components. The tuning response is also in sub-nanosecond scale as shown by high-speed MOSCAP modulators we developed recently [13]. The second- and third-order MOSCAP AMZI (de-)interleavers have XT levels down to -22 dB and -32 dB with tuning powers of 83.0 and 53.0 nW, respectively. The one-, two-, and three-ring-assisted MOSCAP AMZI (de-)interleavers have XT levels down to -27 , -22 , and -20 dB for tuning powers of 10.0, 7220.0, and

33.6 nW. Overall, the leakage current density is measured to be in the range of 1.6–27 $\mu\text{A}/\text{cm}^2$, assuming an outlier from the two-ring-assisted AMZI. Transfer matrix calculations indicate non-ideal coupling ratios and path length differences affect bandpass shape, whereas waveguide loss will affect flat-top response and overall insertion loss. The demonstrated (de-)interleavers represent the lowest tuning power consumption and XT levels for (de-)interleavers on a silicon platform. Future improvements will include the use of fabrication robust MMI-based arbitrary power couplers, tunable MOSCAP directional couplers/MZIs, advanced dispersion engineered components, and better fabrication process for lower waveguide loss. These should all further improve XT levels down to theoretical values for each respective design and increase overall bandwidth > 100 nm. Current investigation into thinner and higher- k dielectric such as HfO_2 is underway for improved phase tuning efficiency. We believe the III-V/Si MOSCAP (de-)interleaver presented here is a fundamental building block that offers significant improvement in energy efficiency for future large-scale DWDM transceivers, optical neural networks, RF-photonics, and programmable photonic architectures.

Funding. Advanced Research Projects Agency-Energy (DE-AR0001039).

Acknowledgment. We thank the UCSB nanofabrication facilities.

Disclosures. The authors declare no conflicts of interest.

Data Availability. The data that support the findings of this study are not publicly available at this time but may be obtained from the authors upon reasonable request.

REFERENCES

- <https://www.cisco.com/c/en/us/solutions/collateral/service-provider/global-cloud-index-gci/white-paper-c11-738085.html>.
- Q. Cheng, M. Bahadori, M. Glick, S. Rumley, and K. Bergman, "Recent advances in optical technologies for data centers: a review," *Optica* **5**, 1354–1370 (2018).
- <https://www.cisco.com/c/en/us/solutions/collateral/executive-perspectives/annual-internet-report/white-paper-c11-741490.pdf>.
- E. Masanet, A. Shehabi, N. Lei, S. Smith, and J. Koomey, "Recalibrating global data center energy-use estimates," *Science* **367**, 984–986 (2020).
- N. Jones, "How to stop data centres from gobbling up the world's electricity," *Nature*, **561**, 163–166 (2018).
- D. Liang, G. Kurczveil, Z. Huang, B. Wang, A. Descos, S. Srinivasan, Y. Hu, X. Zeng, W. V. Sorin, S. Cheung, S. Liu, P. Sun, T. Van Vaerenbergh, M. Fiorentino, J. E. Bowers, and R. G. Beausoleil, "Integrated green DWDM photonics for next-gen high-performance computing," in *Optical Fiber Communications Conference and Exhibition (OFC)* (2020), pp. 1–3.
- G. Kurczveil, A. Descos, D. Liang, M. Fiorentino, and R. Beausoleil, "Hybrid silicon quantum dot comb laser with record wide comb width," in *Frontiers in Optics* (2020), paper FTu6E.6.
- G. Kurczveil, C. Zhang, A. Descos, D. Liang, M. Fiorentino, and R. Beausoleil, "On-chip hybrid silicon quantum dot comb laser with 14 error-free channels," in *IEEE International Semiconductor Laser Conference (ISLC)* (2018), pp. 1–2.
- G. Kurczveil, M. A. Seyed, D. Liang, M. Fiorentino, and R. G. Beausoleil, "Error-free operation in a hybrid-silicon quantum dot comb laser," *IEEE Photonics Technol. Lett.* **30**, 71–74 (2018).
- D. Liang, G. Kurczveil, X. Huang, C. Zhang, S. Srinivasan, Z. Huang, M. A. Seyed, K. Norris, M. Fiorentino, J. E. Bowers, and R. G. Beausoleil, "Heterogeneous silicon light sources for datacom applications," *Opt. Fiber Technol.* **44**, 43–52 (2018).
- A. Descos, G. Kurczveil, D. Liang, and R. Beausoleil, "Heterogeneous O-band InAs/GaAs quantum-dot optical amplifier on silicon," in *Asia Communications and Photonics Conference* (2021).
- S. Srinivasan, D. Liang, and R. G. Beausoleil, "High temperature performance of heterogeneous MOSCAP microring modulators," in *Optical Fiber Communication Conference (OFV)* (2021), paper Th5A.1.
- S. Srinivasan, D. Liang, and R. G. Beausoleil, "Heterogeneous SISCAP microring modulator for high-speed optical communication," in *European Conference on Optical Communications (ECOC)* (2020), pp. 1–3.
- Y. London, T. Van Vaerenbergh, A. J. Rizzo, P. Sun, J. Hulme, G. Kurczveil, A. Seyed, B. Wang, X. Zeng, Z. Huang, J. Rhim, M. Fiorentino, and K. Bergman, "Energy efficiency analysis of comb source carrier-injection ring-based silicon photonic link," *IEEE J. Sel. Top. Quantum Electron.* **26**, 3300113 (2020).
- Y. London, G. Kurczveil, A. Seyed, J. Rhim, M. Fiorentino, and K. Bergman, "Performance requirements for terabit-class silicon photonic links based on cascaded microring resonators," *J. Lightwave Technol.* **38**, 3469–3477 (2020).
- A. Rizzo, Q. Cheng, S. Daudlin, and K. Bergman, "Ultra-broadband interleaver for extreme wavelength scaling in silicon photonic links," *IEEE Photonics Technol. Lett.* **33**, 55–58 (2021).
- M. A. Seyed, C.-H. Chen, and M. Fiorentino, "Error-free DWDM transmission and crosstalk analysis for a silicon photonics transmitter," *Opt. Express* **23**, 32968–32976 (2015).
- N. Zhou, S. Zheng, Y. Long, Z. Ruan, L. Shen, and J. Wang, "Reconfigurable and tunable compact comb filter and (de)interleaver on silicon platform," *Opt. Express* **26**, 4358–4369 (2018).
- J. F. Song, S. H. Tao, Q. Fang, T. Y. Liow, M. B. Yu, G. Q. Lo, and D. L. Kwong, "Thermo-optical enhanced silicon wire interleavers," *IEEE Photonics Technol. Lett.* **20**, 2165–2167 (2008).
- S. Srinivasan, B. Tossoun, G. Kurczveil, Z. Huang, D. Liang, and R. Beausoleil, "160 Gb/s optical link using quantum-dot comb laser source and SiGe APD," in *IEEE Photonics Conference (IPC)* (2020), pp. 1–2.
- B. Tossoun, G. Kurczveil, S. Srinivasan, A. Descos, D. Liang, and R. G. Beausoleil, "32 Gbps heterogeneously integrated quantum dot waveguide avalanche photodiodes on silicon," *Opt. Lett.* **46**, 3821–3824 (2021).
- Y. Yuan, Z. Huang, X. Zeng, D. Liang, W. V. Sorin, M. Fiorentino, and R. G. Beausoleil, "High responsivity Si-Ge waveguide avalanche photodiodes enhanced by loop reflector," *IEEE J. Sel. Top. Quantum Electron.* **28**, 3800508 (2022).
- Y. Yuan, Z. Huang, B. Wang, W. V. Sorin, X. Zeng, D. Liang, M. Fiorentino, J. C. Campbell, and R. G. Beausoleil, "64 Gbps PAM4 Si-Ge waveguide avalanche photodiodes with excellent temperature stability," *J. Lightwave Technol.* **38**, 4857–4866 (2020).
- Q. Deng, L. Liu, R. Zhang, X. Li, J. Michel, and Z. Zhou, "Athermal and flat-topped silicon Mach-Zehnder filters," *Opt. Express* **24**, 29577–29582 (2016).
- F. Horst, W. M. J. Green, S. Assefa, S. M. Shank, Y. A. Vlasov, and B. J. Offrein, "Cascaded Mach-Zehnder wavelength filters in silicon photonics for low loss and flat pass-band WDM (de-)multiplexing," *Opt. Express* **21**, 11652–11658 (2013).
- S. Dwivedi, P. De Heyn, P. Absil, J. Van Campenhout, and W. Bogaerts, "Coarse wavelength division multiplexer on silicon-on-insulator for 100 GbE," in *IEEE 12th International Conference on Group IV Photonics (GFP)* (2015), pp. 9–10.
- H. Xu and Y. Shi, "Flat-top CWDM (de)multiplexer based on MZI with bent directional couplers," *IEEE Photonics Technol. Lett.* **30**, 169–172 (2018).
- X. Fu, H. Jia, S. Shao, X. Fang, L. Zhang, and L. Yang, "Silicon photonic wavelength (de-)multiplexer for low-loss flat-passband DWDM applications," in *Asia Communications and Photonics Conference (ACP)* (2018), pp. 1–2.
- K. Hassan, C. Sciancalepore, J. Harduin, T. Ferrotti, S. Menezo, and B. B. Bakir, "Toward athermal silicon-on-insulator (de)multiplexers in the O-band," *Opt. Lett.* **40**, 2641–2644 (2015).

30. H. Xu, D. Dai, and Y. Shi, "Low-crosstalk and fabrication-tolerant four-channel CWDM filter based on dispersion-engineered Mach-Zehnder interferometers," *Opt. Express* **29**, 20617–20631 (2021).
31. X. Cao, S. Zheng, Y. Long, Z. Ruan, Y. Luo, and J. Wang, "Mesh-structure-enabled programmable multitask photonic signal processor on a silicon chip," *ACS Photonics* **7**, 2658–2675 (2020).
32. D. Pérez, I. Gasulla, L. Crudgington, D. J. Thomson, A. Z. Khokhar, K. Li, W. Cao, G. Z. Mashanovich, and J. Capmany, "Multipurpose silicon photonics signal processor core," *Nat. Commun.* **8**, 636 (2017).
33. A. Macho-Ortiz, D. Pérez-López, and J. Capmany, "Optical implementation of 2×2 universal unitary matrix transformations," *Laser Photonics Rev.* **15**, 2000473 (2021).
34. D. Pérez, I. Gasulla, and J. Capmany, "Programmable multifunctional integrated nanophotonics," *Nanophotonics* **7**, 1351–1371 (2018).
35. W. Bogaerts, D. Pérez, J. Capmany, D. A. B. Miller, J. Poon, D. Englund, F. Morichetti, and A. Melloni, "Programmable photonic circuits," *Nature* **586**, 207–216 (2020).
36. D. Pérez, I. Gasulla, J. Capmany, and R. A. Soref, "Reconfigurable lattice mesh designs for programmable photonic processors," *Opt. Express* **24**, 12093–12106 (2016).
37. L. Zhuang, C. G. H. Roeloffzen, M. Hoekman, K.-J. Boller, and A. J. Lowery, "Programmable photonic signal processor chip for radiofrequency applications," *Optica* **2**, 854–859 (2015).
38. Q. Sun, L. Zhou, L. Lu, G. Zhou, and J. Chen, "Reconfigurable high-resolution microwave photonic filter based on dual-ring-assisted MZIs on the Si_3N_4 platform," *IEEE Photonics J.* **10**, 1–12 (2018).
39. X. Xiao and S. J. B. Yoo, "Scalable and compact 3D tensorized photonic neural networks," in *Optical Fiber Communications Conference and Exhibition (OFC)* (2021), pp. 1–3.
40. Y.-J. Lee, M. B. On, X. Xiao, and S. J. B. Yoo, "Energy-efficient photonic spiking neural network on a monolithic silicon CMOS photonic platform," in *Optical Fiber Communications Conference and Exhibition (OFC)* (2021), pp. 1–3.
41. S. Lai, Z. Xu, B. Liu, and J. Wu, "Compact silicon photonic interleaver based on a self-coupled optical waveguide," *Appl. Opt.* **55**, 7550–7555 (2016).
42. J. Song, Q. Fang, S. H. Tao, M. B. Yu, G. Q. Lo, and D. L. Kwong, "Proposed silicon wire interleaver structure," *Opt. Express* **16**, 7849–7859 (2008).
43. J. Song, Q. Fang, S. H. Tao, M. B. Yu, G. Q. Lo, and D. L. Kwong, "Passive ring-assisted Mach-Zehnder interleaver on silicon-on-insulator," *Opt. Express* **16**, 8359–8365 (2008).
44. M. Cherchi, M. Harjanne, S. Ylinen, M. Kapulainen, T. Vehmas, and T. Aalto, "Flat-top MZI filters: a novel robust design based on MMI splitters," *Proc. SPIE* **9752**, 975210 (2016).
45. M. Cherchi, F. Sun, M. Kapulainen, T. Vehmas, M. Harjanne, and T. Aalto, "Fabrication tolerant flat-top interleavers," *Proc. SPIE* **10108**, 101080V (2017).
46. X. Jiang, Y. Yang, H. Zhang, J. Peng, Y. Zhang, C. Qiu, and Y. Su, "Design and experimental demonstration of a compact silicon photonic interleaver based on an interfering loop with wide spectral range," *J. Lightwave Technol.* **35**, 3765–3771 (2017).
47. J. Robertson, "High dielectric constant oxides," *Eur. Phys. J. Appl. Phys.* **28**, 265–291 (2004).
48. R. Soref and B. Bennett, "Electrooptical effects in silicon," *IEEE J. Quantum Electron.* **23**, 123–129 (1987).
49. C. K. Madsen and J. H. Zhao, *Optical Filter Design and Analysis: A Signal Processing Approach* (Wiley, 1999).
50. Z. Lu, H. Yun, Y. Wang, Z. Chen, F. Zhang, N. A. F. Jaeger, and L. Chrostowski, "Broadband silicon photonic directional coupler using asymmetric-waveguide based phase control," *Opt. Express* **23**, 3795–3808 (2015).
51. L.-W. Luo, S. Ibrahim, A. Nitkowski, Z. Ding, C. B. Poitras, S. J. B. Yoo, and M. Lipson, "High bandwidth on-chip silicon photonic interleaver," *Opt. Express* **18**, 23079–23087 (2010).
52. P. D. Heyn, "Receivers based on silicon ring resonators for multi-wavelength optical interconnects," Ph.D. dissertation, Ghent University (2014).
53. P. De Heyn, J. De Coster, P. Verheyen, G. Lepage, M. Pantouvaki, P. Absil, W. Bogaerts, D. Van Thourhout, and J. Van Campenhout, "Polarization-insensitive 5×20 Gb/s WDM Ge receiver using compact Si ring filters with collective thermal tuning," in *Optical Fiber Communication Conference (OFC)* (2014), pp. 1–3.
54. P. De Heyn, J. De Coster, P. Verheyen, G. Lepage, M. Pantouvaki, P. Absil, W. Bogaerts, J. Van Campenhout, and D. Van Thourhout, "Fabrication-tolerant four-channel wavelength-division-multiplexing filter based on collectively tuned Si microrings," *J. Lightwave Technol.* **31**, 2785–2792 (2013).
55. C. L. Manganelli, P. Velha, P. Pintus, F. Gambini, O. Lemonnier, L. Adelmini, C. Kopp, S. Faralli, F. Di Pasquale, C. Wenger, and C. J. Oton, "Low-power-consumption integrated tunable filters for WDM switching applications in silicon photonics," *IEEE Photonics Technol. Lett.* **30**, 1601–1604 (2018).
56. D. Liang and J. E. Bowers, "Highly efficient vertical outgassing channels for low-temperature InP-to-silicon direct wafer bonding on the silicon-on-insulator substrate," *J. Vac. Sci. Technol. B* **26**, 1560–1568 (2008).
57. W. Bogaerts and S. K. Selvaraja, "Compact single-mode silicon hybrid rib/strip waveguide with adiabatic bends," *IEEE Photonics J.* **3**, 422–432 (2011).