PHOTONICS Research

Ultracompact, polarization-independent, and highly scalable optical power splitting model employing fan-out bending metamaterials

ZHENZHAO GUO,^{1,2} DINBIAO XIAO,^{2,3} AND SHENGBAO WU^{1,4}

¹Photonics Information Innovation Center, Hebei Provincial Center for Optical Sensing Innovations, College of Physics Science and Technology, Hebei University, Baoding 071002, China

²National Research Center for Optical Sensing/Communications Integrated Networking, School of Electronic Science and Engineering, Southeast University, Nanjing 210096, China

³e-mail: jbxiao@seu.edu.cn

⁴e-mail: swu@hbu.edu.cn

Received 18 July 2022; revised 25 August 2022; accepted 29 August 2022; posted 29 August 2022 (Doc. ID 470827); published 10 October 2022

Optical power splitters (OPSs) are essential components in the photonic integrated circuits. Considerable power splitting schemes have been reported on the silicon-on-insulator platform. However, the corresponding device lengths are enlarged, and polarization-sensitive operations are usually encountered when the splitting channels are increased from two to five. In this paper, a novel power splitting model is proposed to overcome these limitations. Here, fan-out bending subwavelength grating (FBSWG) metamaterials instead of classical straight SWGs are leveraged to expand the input TE/TM mode in an ultracompact region and further bend its wavefronts. By using N-angled tapers to match bending wavefronts, the light expanded by FBSWGs can be collected and evenly distributed into N output channels. Based on such a model, three OPSs are designed and experimentally demonstrated, which are the shortest polarization-independent 1×3 , 1×4 , and 1×5 OPSs reported until now to our knowledge. The characterizations show low insertion losses (<1.2 dB, <1.35 dB, and <1.65 dB) and uniformities (<0.9 dB, <1 dB, and <1 dB) over bandwidths of 54 nm, 49 nm, and 38 nm for the 1×3 , 1×4 , and 1×5 OPSs, respectively. For the first time, an ultracompact device length of <4.3 μ m and a polarization-independent operation can be maintained simultaneously as the output splitting channels are increased. ©2022 Chinese Laser Press

https://doi.org/10.1364/PRJ.470827

1. INTRODUCTION

Silicon photonics based on the silicon-on-insulator (SOI) platform, offering a high index contrast between the silicon core and its surroundings for strong mode field confinement and light-matter interaction, is a promising technology for realizing ultracompact devices in the high-density photonic integrated circuits (PICs) [1]. As fundamental and indispensable building elements serving the purpose of power distribution (1-to-N channels), optical power splitters (OPSs) have been broadly utilized in light manipulation and/or forming complex on-chip devices, e.g., optical modulators [2,3], sensors [4], optical phased arrays [5], switches [6], and logic circuits [7]. Over recent years, Y-branches [8], directional couplers (DCs) [9], and multimode interference (MMI) couplers [10] are three main methods towards the realization of on-chip power splitters. Among them, Y-branches, composed of two output branches with an extremely small branching angle, require stringent fabrication processes and rather long device lengths (\sim 50 µm) due to a typical loss figure of 1 dB with a branching angle of 1° [11].

2327-9125/22/112448-12 Journal © 2022 Chinese Laser Press

As an alternative, DCs in which two or three silicon nanowires are assembled in parallel are popular options, owing to their structural simplicity. However, the wavelength sensitivity brought by the strict phase-matching condition [12], limits their applications for broadband PICs. MMI couplers based on the self-imaging effect as another approach, seem to be superior candidates in terms of the large fabrication tolerance and broadband operation, but they normally suffer from the polarization dependence as the output ports are expanded. Typically, as the number N of output splitting channels/ports increases, the following issues are highlighted. (1) Only one operating polarization is supported, indicating that the power splitter is sensitive to the polarization state. For example, the 1×2 , 1×2 , 2×2 , 1×3 , and 1×4 power splitters, respectively, in Refs. [13-17], are all working for the TE polarization only. Under this scenario, the cascade of polarization controlling devices is necessary to ensure good performance of the power splitting [18,19]. (2) Relatively larger splitting lengths are required. A 1×4 power splitter can be realized by leveraging

a tapers-assisted MMI coupler [20], which has a maximum insertion loss of 0.89 dB within a bandwidth ranging from 1.520 to 1.624 μ m. However, the device length is as long as 36 μ m, and only the TE mode is operated. (3) The power splitting scheme is not universal and, thus, cannot be expanded to apply in other cases of different numbers for N, i.e., rather low scalability and flexibility, which means a redesign or a cascaded strategy is required as the number N increases. For power splitters using three-guided DCs, a 1×2 power splitter [21] with a splitting length of ~120 μ m and a 1 × 3 power splitter [22] with a splitting length of 7.3 µm are both polarization insensitive. However, such a scheme cannot be applied for designing a 1×4 or a 1×5 power splitter unless cascaded schemes are implemented at the cost of rather large footprints, which hinders the realization of high-density on-chip PICs. As to conventional Y-branches, more branches are required to attain a $1 \times N$ (N > 2) power splitter. In this way, more than one branching angle is formed and, thus, a high insertion loss as well as a long splitting length will be obtained, subsequently [23]. For this reason, power splitters using traditional Y-branches, have rarely been reported for cases of N = 3-5 to our knowledge. By contrast, a $1 \times N$ power splitter can be readily realized by exploiting the MMI coupler, via the corresponding N-fold image. However, it is difficult to simultaneously operate for both TE and TM polarizations as the output port number N increases since MMI couplers are polarization-dependent devices [24,25]. Furthermore, the N-fold image requires a wider multimode waveguide to excite higher-order eigenmodes with larger *N*, thereby increasing the coupling length of the *N*-fold image for either TE or TM mode, i.e., larger device footprints with increased N of output channels/ports. For instance, the device length of the 1×4 MMI power splitter is ~75% of that of the 1 × 8 splitter in Ref. [20]. Therefore, a universal and ultracompact scheme, addressing these issues mentioned above, is much more attractive and highly desired. To the best of our knowledge, there is a lack of such a power splitting model.

Subwavelength gratings (SWGs) metamaterials, formed by an arrangement of nanostrips with the grating pitch below the subwavelength regime, have properties of diffraction suppression, homogenous medium behavior for light guiding and controlling, and efficient manipulation of the refractive index, which paves a new path towards highly efficient nanophotonic devices [26-28]. Recently, the SWG metamaterials, usually arranged in straight periodic nanowires, have been introduced to realize high-performance optical power splitters [29-33]. However, these reported power splitting schemes are focused on the specific case of N = 2 (i.e., the 3-dB coupler) and, thus, their design principles are inapplicable when it comes to cases of N = 3-5, showing low scalability and flexibility in the design of multioutput optical power splitters, and, meanwhile, most of them are limited by the polarization-sensitive operations. In fact, bending SWG metamaterials are promising for building an efficient on-chip device. For examples, a waveguide crossing and a multimode bend can be realized by using the bending SWG configuration [34,35]. In this paper, another type of SWG metamaterial termed as fan-out bending SWG (FBSWG) is exploited to design an ultracompact, polarization-independent, and universal on-chip power splitting model

where the fully etched SWG wires are bent and fanned-out gradually in the FBSWGs. Different from conventional straight SWG-based devices, the FBSWGs connected to the input channel via a mode transition taper, can fully expand the TE/TM mode in an ultracompact region and further shape its wavefronts. By using N-angled output tapers with optimal parameters to meet the wavefront match, the expanded light can be effectively collected and evenly distributed into N-output channels in a low-loss mode transition, or say adiabatic evolution. In this way, an ultracompact $1 \times N$ power splitter can be realized for both TE and TM polarizations, which can address the above-mentioned issues brought by the increased output channels. As proof-of-concept experiments, we demonstrate a 1×3 , a 1×4 , and a 1×5 optical power splitters all within an ultracompact length of less than 4.3 μ m [here the 1×2 optical power splitters (OPSs) are theoretically proposed only], which are the shortest polarization-independent 1×3 , 1×4 , and 1×5 power splitters reported as far as we know. Experimental results show that such a power splitting model using FBSWG metamaterials can offer insertion losses <1.2 dB, <1.35 dB, and <1.65 dB (power uniformities <0.9 dB, <1 dB, and <1 dB) over bandwidths of 54 nm, 49 nm, and 38 nm for the 1×3 , 1×4 , and 1×5 power splitters, respectively, for both TE and TM modes. To the best of our knowledge, this is the first power splitting model that can maintain an ultracompact device length of <4.3 µm and a polarization-independent operating state simultaneously as the output channel number N is increased from 2 to 5.

2. CONFIGURATION, DESIGN, AND ANALYSIS

A. Configuration and Design

Figure 1 shows the schematic layouts of the proposed power splitting model for the TE/TM dual-polarization with enlarged views of simulated light propagations of the 1 × 4 power splitting in Fig. 1(a). The 1×2 , 1×3 , 1×4 , and 1×5 power splitters are designed on the SOI platform with a 220-nm silicon core ($n_{Si} = 3.476$), a 2-µm buffer oxide layer ($n_{SiO_2} = 1.444$), and a 2.2- μ m silicon oxide up claddings. Here, a 1 × N power splitter is composed of three sections along the z direction as well as the light wave propagating direction: an input taper connected to the input waveguide, the FBSWGs forming a circular sector of θ_{SWG} degrees, and N-output-angled/inverse tapers connected to output channels (namely, O_i for the *i*th output taper) as shown in Fig. 1(b). Specifically, the power splitter model is symmetric related to the central axis of x = 0 and, here, we separate the FBSWGs from input and output tapers in Fig. 1(b) for ease of viewing. For input and output taper sections embedded in the FBSWG region, respectively, they are tapered down from w_1 to w_r and tapered up from w_r to $w_{\rm O}$ in lengths of $L_{\rm I}$ and $L_{{\rm O}i}$ with *i* in the range of 1 to N in which θ_{O_i} , w_i , and L_i denote the angle between the taper O_i and the axis of x = 0, the crosswise position of taper O_i in the x direction, the lengthwise position of taper O_i in the *z* direction, respectively. As to the FBSWG region with bending angle θ_{SWG} , it is placed at the central axis of x = 0 and symmetric related to it, and, meanwhile, the lengthwise position of FBSWGs in the z direction is denoted by L_s . The inner bending radius for the bending SWGs is defined as



Fig. 1. A 3D schematic of the proposed power splitting scheme: (a) the 1×4 power splitter with enlarged views of light propagation profiles for TE and TM modes, respectively; (b) the top view of the input taper, angled output tapers, and FBSWGs, which are shown separately to facilitate the understanding; the designed (c) 1×2 power splitter, (d) 1×3 power splitter, and (e) 1×5 power splitter. All devices are covered by up-SiO₂ claddings, which are not shown here for clarity. (f) Working principle of power splitters using FBSWGs, the optical path from the center point to the arbitrary point of the *m*th bending grating line is $n_0 R_m$, resulting in curved wavefronts, and such curved wavefronts are matched with that of angled output tapers. Comparisons of (g) conventional straight SWGs and (h) a bending silicon waveguide with the proposed FBSWG scheme. Distributions of electric-fields E_x and E_y for TE and TM modes are also simulated by the 3D-FDTD method and displayed in (f)–(h).

$$R_m = R_0 + (m-1) \cdot \Lambda, \tag{1}$$

where R_m is the inner bending radius for the *m*th bending silicon grating wire and Λ is the pitch for the SWG metamaterial and, thus, dimensions for the FBSWGs can be formally determined by combining θ_{SWG} , L_s , M_{1i} (the period number of the FBSWGs), and R_m . The SWG metamaterial can be considered as an equivalent homogenous medium and the effective refractive indices for SWG metamaterial waveguides ($\Lambda \ll \lambda$) can be approximately given by Rytov's formulas [26–28],

$$n_{\rm o}^2 \approx f \cdot n_{\rm Si}^2 + (1 - f) \cdot n_{\rm SiO_2}^2,$$
 (2)

$$1/n_{\rm e}^2 \approx f/n_{\rm Si}^2 + (1-f)/n_{\rm SiO_2}^2$$
, (3)

where $f = a/\Lambda$ is the duty cycle of gratings (one has f = 120/220 in this paper) and n_o/n_e is the ordinary/extraordinary refractive index for polarization parallel/perpendicular to the segmenting silicon wires. In the present case, the effective refractive index of the FBSWGs should be n_o for both TE and TM polarizations since wave vectors of expanded modes are perpendicular to the tangent direction, and, meanwhile, both TE and TM polarizations are parallel to the tangent plane of bending silicon segments of the FBSWGs. In a classical focusing grating coupler (FGC) producing curved cylindrically wavefronts, curved grating lines follow the equation (in a polar system) [36–38],

$$n_0 \cdot k_0 \cdot R = n_c \cdot k_0 \cdot R \cdot \sin(\theta_f) \cos(\alpha) + 2\pi m,$$
 (4)

where $k_0 = 2\pi/\lambda$ is the wavenumber in free space, n_c is the upper-cladding refractive index, θ_f is the angle between the fiber and the chip surface normal, α denotes the angle subtended by an arbitrary point on the curved grating line and to the *z* direction, and *m* is an integer for the *m*th curved grating line. This equation essentially indicates that the light launched from the fiber and the light within the grating go through an equal light path or a difference of $2\pi m$, and thereby wavefronts (or say phase fronts) are equal along the curved grating line to focus the light [39]. For the FBSWGs in this paper, the light path from the center point to the arbitrary point of the *m*th bending grating line is $n_0 R_m$ and, thus, wavefronts are equal along the curved grating line for both TE and TM modes in the FBSWGs. In this way, wavefronts of light expanded/unfolded in the FBSWGs are curved correspondingly.

The power splitting mechanism in the present model is essentially expanding (or say unfolding) the light in an effective medium and then collecting/equally distributing it through output silicon waveguides. In this way, the key to realize an ultracompact and universal $1 \times N$ power splitting model is then twofold: (1) efficient light expanding, an area in an ultracompact length for fully and widely expanding the light with a low transition loss; (2) power collecting/distributing, output silicon waveguides for collecting the unfolded light and further distributing it equally. Unlike conventional power splitters, whose power splitting region is larger with increased N, an ultracompact area for power splitting can be maintained for TE and TM modes in the proposed model. Figure 1(f) shows the operating principle and design rule of our power splitting model. In such a model, the input TE/TM mode is fully expanded in the FBSWG region with curved wavefronts. From Fig. 1(f), input TE and TM modes can be fully and widely expanded in a sector-shaped area, in an ultracompact length of $<4.5 \ \mu m$, such as an FGC which shows a multifold length reduction compared to a standard straight grating coupler for expanding or focusing the light [36]. Subsequently, N inverse and wideangled tapers $(O_i - O_N)$ with dimension parameters chosen carefully are embedded in the FBSWGs and able to collect and distribute the expanded light power into N channels equally and adiabatically since the wavefronts of expanded modes and corresponding output modes are matched (marked by blue dotted curves) as shown in Fig. 1(f). Moreover, those angled tapers fit the ultracompact area of the FBSWGs mathematically. By taking advantages of the sector-shaped structure of the FBSWGs for the power expanding and angled tapers for the power collecting/distributing, total device lengths are all controlled within the range of [4.02, 4.25] µm only in this paper so that the 1×2 , 1×3 , 1×4 , and 1×5 power splitters are all realized within an ultracompact size. Therefore, increasing the output ports number N from 2 to 5 will not lead to a much larger footprint compared with conventional power splitting schemes mentioned above, together with the polarization operation maintained, which breaks the limitations that power splitting lengths are significantly longer and polarization-sensitive operations are encountered as the output port number N increases.

One may wonder if classical straight SWGs or a bending silicon waveguide structure can do the same thing. To answer

this issue, we compare these two scenarios with FBSWGs in Figs. 1(g) and 1(h). For the straight SWGs, the input TE/ TM mode is expanded in the strip SWGs region with straight wavefronts shaped by straight/segmenting silicon wires [see the straight dotted lines in Fig. 1(g)], and the input light is unfolded in the straight SWG region. However, the wavefronts of unfolded modes in the straight SWG region do not match those of angled output tapers and, thus, large transition losses will be generated. By contrast, if output tapers ($\theta_{Oi} = 0$) parallel to the input tapers are used to satisfy the wavefront match, the length of the strip SWG region should be relatively long for a larger N since a wide area for the full light power unfolding is required for parallel light power collection. As to the bending silicon waveguide shown in Fig. 1(h), the wavefront of the expanded mode field is matched with those of output modes. Nevertheless, multiple output Y-branches are then needed to collect and distribute the expanded modes, and such a design correspondingly encounters the designing problems of conventional Y-branches mentioned above.

B. Optimizations and Simulation Results

According to the above-mentioned operating principle and design rule, the taper lengths, angles, and positions of output tapers (O_1-O_i) , which dramatically affect the power collecting as well as distributing, are key parts to the realization of $1 \times N$ power splitters. In this way, key parameters of θ_{Oi} , w_i , L_i , and L_{Oi} need to be optimized for determination. Here, the three-dimensional finite-difference time-domain (3D-FDTD) method is carried out for simulation and quantifying the device performance [40] where three figures of merit of insertion loss (IL), output uniformity (OU), and power splitting difference (PSD) for $1 \times N$ power splitter are defined as

IL (dB) = -10 log
$$\left(\sum_{i}^{N} P_{Oi} / P_{I}\right)$$
, (5)

OU(dB)

$$= -10 \log(\min\{P_{O1}, P_{O2}, \dots, P_{ON}\} / \max\{P_{O1}, P_{O2}, \dots, P_{ON}\}),$$
(6)

$$PSD = \max\{NT_{O1}, NT_{O2}, ..., NT_{ON}\} - \min\{NT_{O1}, NT_{O2}, ..., NT_{ON}\},$$
(7)

where P_{Oi} is the optical power obtained at the output port of taper O_i , P_I is the optical power at the input port, and NT_{O_i} is the normalized transmission attained at the output port of taper O_i. Essentially, both OU and PSD refer to output power uniformity, or say, power imbalance in some reports, and, here, we use PSD for easy calculation in the device optimization. Simulations are performed via 3D models where the grid sizes in the simulation process are chosen as $\Delta x = 10$ nm, $\Delta y = 10$ nm, and $\Delta z = 15$ nm in the *x*, *y*, and *z* coordinates, respectively, to mesh the structure. The computational window is set to be 10 μ m × 2.5 μ m on the *x*-*y* plane, and *z* is decided by the output tapers, including a 0.5-µm thickness of a perfectly matched layer boundary at each edge. For the sake of readability and simplification, only the 1 × 4 power splitter is chosen to describe in detail, here, as an example where the input and output core widths are chosen as $w_{\rm I} = 500$ nm,

 $w_{\rm O} = 500$ nm to support the single mode and connect with the grating couplers, and the input taper, transferring input light to the FBSWGs, is tapered down from $w_{\rm I} = 500$ nm to $w_{\rm t} = 100$ nm in a length of $L_{\rm I} = 1.82$ µm. The initial guessing parameters of the output tapers and other fixed parameters are summarized as follows: $\theta_{O1}/\theta_{O4} = 25^\circ$, $\theta_{O2}/\theta_{O3} = 8^\circ$, $w_1/w_4 = 0.3 \ \mu\text{m}, \quad w_2/w_3 = 0.3 \ \mu\text{m}, \quad L_1/L_4 = 0.8 \ \mu\text{m},$ $L_2/L_3 = 2.2 \ \mu\text{m}, \ L_{O1}/L_{O4} = 1.5 \ \mu\text{m}, \ L_{O2}/L_{O3} = 1.5 \ \mu\text{m},$ $M_{14} = 18$, $L_{\rm s} = 0.6 \ \mu {\rm m}$, $\theta_{\rm SWG} = 56^{\circ}$, $a = 100 \ {\rm nm}$, $\Lambda =$ 220 nm, and $R_0 = 0.76 \,\mu\text{m}$. The following is the related optimization flow per run.

(I) Angles of θ_{O1}/θ_{O4} and θ_{O2}/θ_{O3} are swept simultaneously, and then the results (PSDs) are recorded as contour maps. To achieve low PSDs for both TE and TM modes, a trade-off is required to perform since the minimum values of PSDs for $\theta_{\rm O1}/\theta_{\rm O4}$ and $\theta_{\rm O2}/\theta_{\rm O3}$ are normally different. If the $max_{I}{PSD_{TE}, PSD_{TM}}$ is lower than that of the last run, the $\theta_{\rm O1}/\theta_{\rm O4}$ and $\theta_{\rm O2}/\theta_{\rm O3}$ are updated and then fixed in this run with parameter ranges adjusted/narrowed for the next run. Otherwise, $\theta_{\rm O1}/\theta_{\rm O4}$ and $\theta_{\rm O2}/\theta_{\rm O3}$ remain unchanged. Next, go to Step II. (The initial guessing ranges are chosen to be [22°, 28°] for $\theta_{\rm O1}/\theta_{\rm O4}$ and [8°, 14°] for $\theta_{\Omega_2}/\theta_{\Omega_3}$, respectively.)

(II) Widths of w_1/w_4 and w_2/w_3 , denoting the lateral (crosswise) position of output tapers, are swept simultaneously with fixed θ_{Ω_i} , and then corresponding results are recorded as contour maps. Here, a trade-off is also performed to achieve low PSDs for two polarizations. After parameter sweeps and the further trade-off process, w_1/w_4 and w_2/w_3 are updated and then fixed in this run if the new max_{II}{PSD_{TE}, PSD_{TM}} is lower than max_I of Step I with parameter ranges adjusted/ narrowed for the next run. Otherwise, w_1/w_4 and w_2/w_3 remain unchanged. Next, go to Step III. (The initial guessing ranges are chosen to be [0.2, 0.4] µm for w_1/w_4 and [0.2,0.4] μm for w_2/w_3 , respectively.)

ously with fixed θ_{Oi} and w_i , and then corresponding results are recorded as contour maps and chosen by making a trade-off. After that, if the new $\max_{III} \{PSD_{TE}, PSD_{TM}\}$ is lower than max_{II} of Step II, L_1/L_4 and L_2/L_3 are updated and then fixed in this run, and parameter ranges are adjusted/narrowed for the next run. Otherwise, L_1/L_4 and L_2/L_3 remain unchanged. Next, go to Step IV. (The initial guessing ranges are set to be [0.7, 1.2] μ m for L_1/L_4 and [1.8, 2.4] μ m for L_2/L_3 , respectively.)

(IV) Lengths of output tapers L_{O1}/L_{O4} and L_{O2}/L_{O3} are swept simultaneously with fixed θ_{O_i} , w_i , and L_i , whose results are recorded as contour maps. Likewise, if the $\max_{IV} \{PSD_{TE}, PSD_{TM}\}$ is lower than \max_{III} of Step III, L_{O1}/L_{O4} and L_{O2}/L_{O3} are updated and then fixed in this run with parameter ranges adjusted/narrowed for the next run. Otherwise, L_{O1}/L_{O4} and L_{O2}/L_{O3} remain unchanged. Next, go to Step V. (The initial guessing ranges are set to be [1.5, 2.5] μm for L_{O1}/L_{O4} and [1.5, 2] μm for L_{O2}/L_{O3} , respectively.)

(V) If the value of $max_V{PSD_{TE}, PSD_{TM}}$ is higher than or equal to that of the last run, stop the optimization. Otherwise, go back to Step I and start the next run.

For each step, the feature size check is performed, which ensures the device has a minimum feature size of 100 nm for easy fabrication. Figures 2(a)-2(h) show the penultimate run of the device optimization, and no lower $max_V{PSD_{TE}, PSD_{TM}}$ for both TE and TM modes can be found in the next run.

From Figs. 2(a) and 2(b), it is apparent that optimal/minimum PSD_{TE} and PSD_{TM} are located at two different groups of $\theta_{\rm O1}/\theta_{\rm O4}$ and $\theta_{\rm O2}/\theta_{\rm O3}$ in Step I, that is, 0.00015 for PSD_{TE} in $\theta_{\rm O1}/\theta_{\rm O4} = 24.5^{\circ}/\theta_{\rm O2}/\theta_{\rm O3} = 11.5^{\circ}$ and 0.00092 for PSD_{TM} in $\theta_{\rm O1}/\theta_{\rm O4} = 25^{\circ}/\theta_{\rm O2}/\theta_{\rm O3} = 10^{\circ}$, respectively. In order to ensure the polarization-intensive operation of the proposed



Fig. 2. Calculated PSDs, respectively, for (a) the TE mode and (b) TM mode as θ_{O1}/θ_{O4} and θ_{O2}/θ_{O3} vary in Step I, for (c) the TE mode and (d) TM mode as w_1/w_4 and w_2/w_3 vary in Step II, for (e) the TE mode and (f) TM mode as L_1/L_4 and L_2/L_3 vary in Step III, and for (g) the TE mode and (h) TM mode as L_{O1}/L_{O4} and L_{O2}/L_{O3} vary in Step IV.

 1×4 power splitter, a trade-off is performed here by choosing $\theta_{\rm O1}/\theta_{\rm O4} = 25^{\circ}/\theta_{\rm O2}/\theta_{\rm O3} = 9.5^{\circ}$, where $\text{PSD}_{\rm TE} = 0.0057$ and $PSD_{TM} = 0.004$, respectively. One sees that PSD_{TE} and PSD_{TM}, respectively, exhibit minor variations in Figs. 2(d) and 2(f). Thus, the minimum PSD for the other polarization can be chosen as long as the max{ PSD_{TE} , PSD_{TM} } is lower than that of the previous step. In this way, $w_1/w_4 =$ $0.244 \ \mu\text{m}, \ w_2/w_3 = 0.281 \ \mu\text{m}, \ L_1/L_4 = 1.048 \ \mu\text{m}, \ \text{and}$ $L_2/L_3 = 2 \ \mu m$ are chosen. Similar to the trade-off performed in Figs. 2(a) and 2(b) of Step I, $max{PSD_{TE}, PSD_{TM}} =$ 0.00354 can be achieved by choosing $L_{O1}/L_{O4} = 2 \ \mu m$ and $L_{O2}/L_{O3} = 1.8 \ \mu\text{m}$. It is worth noting that the 1 × 4 power splitter keeps max{IL_{TE}, IL_{TM}} < 0.7 dB, for both TE and TM modes during the optimization flow, owing to the effective homogenous medium behavior of the FBSWGs metamaterials as well as efficient mode transitions between FBSWGs and tapers.

For the proposed power splitting model, the device length is determined by the whole grating length and output tapers simultaneously. According to Fig. 1(b), the power splitter begins at z = 0 where one has $R_0 + M_{1i} \cdot \Lambda - (\Lambda - a) - L_s$ for the coordinate of the FBSWG end and max{ $L_1 + L_{O1} \cdot \cos(\theta_{O1})$, ..., $L_i + L_{Oi} \cdot \cos(\theta_{Oi})$ } for the coordinate of output tapers end in the *z* direction. Therefore, the total device length *L* can be given by

$$L (\mu m) = \max\{R_0 + M_{1i} \cdot \Lambda - (\Lambda - a) - L_s, \\ \max\{L_1 + L_{O1} \cdot \cos(\theta_{O1}), ..., L_i + L_{Oi} \cdot \cos(\theta_{Oi})\}\},$$
(8)

and detailed optimal parameters of the 1×3 , 1×4 , and 1×5 power splitters are given by Table 1 for ease of reading.

To verify the power dividing function of the proposed $1 \times N$ power splitter and graphically demonstrate the transmission feature, the light propagation profiles of the 1×2 , 1×3 , 1×4 , and 1×5 power splitters, operating at the central wavelength of 1.55 µm, are calculated by performing the 3D-FDTD method as shown in Figs. 3(a)-3(h). From these simulated profiles, one can clearly see that the input TE/TM mode is expanded/unfolded via a mode transition from the input waveguide to the FBSWGs where the wavefronts of the expanded modes are bent in the fan-out-curved grating lines. Meanwhile, these unfolded modes are collected by the well-designed output tapers and then equally distributed into corresponding output channels for both TE- and TM-polarized lights. Especially, the total length of the power dividing does not become larger as output splitting channels are increased in which the device lengths of 1×2 , 1×3 , 1×4 , and 1×5 power splitters lower than 4.3 µm are realized with polarization-independent operations, showing great scalability of the present power splitting model.

The fabrication tolerances of proposed power splitters are also investigated. One can find that the designed 1×3 , 1×4 , and 1×5 power splitters still work well even when there are some deviations of $\Delta a = \pm 10$ nm as shown in Figs. 4(a)-4(c). The 3D-FDTD method is also carried out to calculate the wavelength dependence for the optimal $1 \times N$ power splitter. Figures 4(d)-4(f) show the calculated spectral responses of the 1×3 , 1×4 , and 1×5 power splitters over a wavelength band ranging from 1450 to 1650 nm. As is observed, the operating bandwidth is degraded as the output port number N increases under the same required IL and OU level. If ILs < 0.6 dB and OUs < 1 dB are required for both

Table 1. Optimized Parameters of the 1×2 , 1×3 , 1×4 , and 1×5 Power Splitters^a

OPS	M_{12}	$\theta_{\rm SWG}$	L_1	L_2	w_1	w_2	$ heta_1$	θ_2	L_{01}	L_{02}	R_0
1 × 2	17	33	1.3	1.3	0.25	0.25	8	8	2.4	2.4	0.98
1 × 3	<i>M</i> ₁₃	$\theta_{\rm SWG}$	L_1	L_2	L_3	w ₁	<i>w</i> ₂	<i>w</i> ₃	θ_1	θ_2	θ_3
	19	48	1.69	2.25	1.69	0.4	0	0.4	15.75	0	15.75
	L ₀₁	L _{O2}	L _{O3}	R ₀							
	2.2	1.95	2.2	0.76							
1 × 4	<i>M</i> ₁₄	$\theta_{\rm SWG}$	L_1	L_2	L ₃	L_4	w_1	w_2	w ₃	w_4	θ_1
	18	56	1.048	2	2	1.048	0.244	0.281	0.281	0.244	25
	θ_2	θ_3	$ heta_4$	L ₀₁	L _{O2}	L _{O3}	L _{O4}	R ₀			
	9.5	9.5	25	2	1.8	1.8	2	0.76			
1 × 5	<i>M</i> ₁₅	$\theta_{\rm SWG}$	L_1	L_2	L_3	L_4	L_5	w_1	<i>w</i> ₂	<i>w</i> ₃	w_4
	19	56	1.05	1.923	2.25	1.923	1.05	0.246	0.193	0	0.193
	w_5	θ_1	θ_2	θ_3	$ heta_4$	θ_5	L ₀₁	L _{O2}	L _{O3}	L_{04}	L ₀₅
	0.246	24.5	16.5	0	16.5	25.5	2	1.95	2	1.95	2
	R_0										
	0.76										
SPC ^b	w_{I}	w ₀	w _t	L _s	L _I	а	Λ				
	0.5	0.5	0.1	0.6	1.82	0.12	0.22				

^{*a*}L and *w*, in μ m; θ , in degrees.

^bStructural parameters in common.



Fig. 3. Calculated light propagation profiles as the fundamental TE/TM mode is injected for the (a) and (b) 1×2 power splitter, (c) and (d) 1×3 power splitter, (e) and (f) 1×4 power splitter, and (g) and (h) 1×5 power splitter, respectively.

TE- and TM-polarized modes, the 1×3 , 1×4 , and 1×5 power splitters, respectively, exhibit broad bandwidths of 180 nm (1.47–1.64 µm), 190 nm (1.45–1.64 µm), and 80 nm (1.51–1.59 µm) for the polarization-independent operations.

Furthermore, the intermodal cross talk of the proposed power splitting model is extremely low, which can be neglected. The input TE₀/TM₀ mode is adiabatically expanded in the FBSWGs and transited to output tapers, and only the wavefronts are shaped [see Fig. 1(f)] where higher-order modes are not excited. Besides, the width of output waveguides is set to be 500 nm for single-mode operation in which TE₀ and TM₀ modes are well guided, whereas, the TE₁ mode is poorly supported ($n_{\rm eff} = 1.4996$, rather close to the index of silica claddings), and the TM₁ mode is cut off ($n_{\rm eff} = 1.351 <$ 1.444). In this way, the higher-order modes are inherently filtered. Taking the 1×5 splitter, here, as an example, we use the mode expansion method [41] to calculate the normalized transmissions of the TE₁ (TM₁) modes in the output ports, which are 1.63901×10^{-5} (2.2133 × 10⁻¹⁰), 6.87412×10^{-5} $(1.09 \times 10^{-10}), 7.08812 \times 10^{-6} (1.6605 \times 10^{-11}),$ and 6.59313×10^{-5} (3.215 × 10⁻¹⁰), respectively, and, thus, there are almost no higher-order modes existing in the output waveguides of the present splitter. By contrast, the normalized transmissions of the TE_0 (TM₀) mode in the corresponding output ports are 0.2332 (0.2401), 0.2301 (0.24007), 0.22966 (0.240082), and 0.233 (0.24009), respectively. Thus, the intermodal cross talk of proposed power splitters is extremely low and can be neglected.

3. FABRICATION AND CHARACTERIZATION

To corroborate the proposed theoretical model and corresponding simulation results, the 1×3 , 1×4 , and 1×5 power splitters are fabricated on an SOI platform with a 220-nm-thick silicon device layer and a 2-µm buried oxide layer using 100-keV electron-beam lithography (EBL) and reactive ion etching (RIE) processes. The device patterns are defined into a material that is sensitive to electron beam exposure utilizing EBL, and then an anisotropic inductively coupled plasma RIE etching process is performed on the substrate for transferring the patterns into the underlying silicon device layer. A 2.2-µm SiO₂ upper-cladding layer is, subsequently, deposited using chemical vapor deposition.



Fig. 4. Calculated ILs and OUs for the (a) 1×3 power splitter, (b) 1×4 power splitter, and (c) 1×5 power splitter with a deviated duty cycle for both TE and TM modes. The calculated wavelength dependence of the ILs and OUs for the (d) 1×3 power splitter, (e) 1×4 power splitter, and (f) 1×5 power splitter for TE and TM polarizations.

Here, three-stage cascaded measure schemes are used to accurately characterize the proposed polarization-independent power splitters, together with reference waveguides composed of straight and bending nanowires. Figures 5(a)-5(h) show the microscopy images of fabricated three-stage cascaded 1×3 , 1×4 , and 1×5 power splitters and corresponding SEM images of element power splitters. Considering the polarization-independent operation, each cascaded measure scheme is divided into two measure groups, including TE- and TM-type grating couplers, respectively. Moreover, reference straight and bending waveguides for both TE and TM modes are fabricated on the same chip for the comparison and normalization. In such a cascaded measure scheme, the output port O_i is consistent with the *i*th output channel of element power splitters in each stage for evaluating the optical power uniformity more precisely. Taking the 1×5 power splitting measure scheme here as an example, the 3rd output channel (the central one) of each element power splitter in the first two stages is used for element cascading and then the 3rd output channel in the last stage (named output port O_3) is used for the characterization. For equal power distributions, the 1×2 , 1×3 , 1×4 , and 1×5 power splitters, respectively, have splitting factors of 3 dB, 4.77 dB, 6.02 dB, and 6.98 dB in each output channel. By using the three-stage cascaded scheme, the transmittance in each of N output channels for the designed $1 \times N$ power splitter can be obtained by [42]

$$T_{\text{channel }i}(\text{dB}) = T_{\text{O}i}/3,$$
(9)

where T_{O_i} is the measured and normalized transmittance in the output port O_i as shown in Figs. 5(b), 5(e), and 5(f) with *i* ranging from 1 to *N*.

Here, a tunable laser (Santec TSL-710) and an optical power meter (Santec MPM-210) are utilized for measuring the transmittance spectra of the fabricated 1×3 , 1×4 , and 1×5 optical power splitters, and the characterized normalized transmittances $T_{\text{channel }i}$ are shown in Figs. 6(a)–6(f). From the $T_{\text{channel }i}$ spectra, one can observe that the fabricated power splitters have good uniformity for polarization-independent equal light power distributions where the normalized transmittances $T_{\text{channel}i}$ for both TE and TM modes of the 1×3 , 1×4 , and 1 × 5 power splitters are around -4.77 dB, -6.02 dB, and -6.98 dB, corresponding to the 33.3%, 25%, and 20% power splittings, respectively. For the fabricated 1×3 optical power splitter, corresponding ILs (OUs) for TE and TM modes are measured to be 0.75 dB and 0.58 dB (0.64 dB and 0.32 dB), respectively, at the central wavelength of 1.55 μ m. From Fig. 6(b), the 1×3 power splitting for the TM mode has IL < 1.4 dB and OU < 1.1 dB within the wavelength range of 1500 to 1570 nm. One has a working bandwidth as large as 54 nm (1513-1567 nm) for achieving an IL < 1.2 dB/OU < 0.9 dB for both TE- and TM-polarized lights. Regarding the fabricated 1 × 4 power splitter, the



Fig. 5. (a) Microscope image of the reference TE and TM waveguides. (b) Microscope image of the three-stage cascaded measure scheme for the fabricated 1×3 power splitters and (c) and (d) corresponding scanning electron microscope (SEM) images of fabricated 1×3 power splitters. Microscope image of the three-stage cascaded measure scheme for the fabricated (e) 1×4 power splitters and (f) 1×5 power splitters. Pseudocolor SEM images of (g) the 1×4 and (h) the 1×5 power splitting elements.

measured IL and OU are 1.16 dB/0.66 dB and 0.8 dB/0.43 dB for the input TE/TM mode at 1550 nm, respectively, and ILs and OUs are lower than 1.35 dB and 1 dB over a 49-nm bandwidth (1512–1561 nm) for both TE and TM modes. As to the fabricated 1 × 5 power splitter, the performance is degraded as the number of output channels is increased. The measured

IL/OU of TE mode power splitting is 1.24 dB/0.65 dB and the IL/OU of TM mode power splitting is 0.73 dB/0.54 dB at 1550 nm, respectively. For both TE and TM modes, the measured ILs and OUs are lower than 1.65 dB and 1 dB, respectively, over an operating bandwidth of 38 nm (1521–1559 nm). One can clearly find that the performance of TE



Fig. 6. Measured and normalized transmittance $T_{\text{channel}\,i}$ spectra of the 1 × 3 power splitter for the input (a) TE and (b) TM modes, the 1 × 4 power splitter for the input (c) TE and (d) TM modes, and the 1 × 5 power splitter for the input (e) TE and (f) TM modes, respectively.

modes has larger fluctuations than that of TM modes across the operation bandwidth, which is mainly due to the higher sensitivity to sidewall roughness of the TE mode in the FBSWG metamaterials as well as input/output grating couplers. Furthermore, the large fluctuations at long wavelength (>1570 nm) are caused by the blueshifted central wavelength of the fabricated grating couplers.

Table 2 gives a brief comparison with previous reported power splitters. One can observe that the proposed 1×3 and 1×4 power splitters have the shortest device lengths and competitive ILs as well as OUs. More importantly, our power splitting model can maintain an ultracompact length of <4.3 µm and a polarization-independent operation as the number of output channels *N* increases from 2 to 5, whereas other

Table 2.	Comparison of	Several Power	Splitters	at the	Wavelength	of 1.55	iμm
----------	---------------	---------------	-----------	--------	------------	---------	-----

			IL (dB)		OU (dB)		Bandwidth (nm) ^c		
Structure	Function	Length (µm)	Sim"	Exp ^b	Sim	Exp	Sim	Exp	
Y-branch [11]	1 × 4 at TE 1 × 4 at TM	4000	0.26	<1.2 <1.8	_	<2.2	100 (IL < 1) 100 (IL < 1.3)	100 (IL < 1.2, $OU < 2.2$) 100 (IL < 1.8, $OU < 2.2$)	
Tapers [17]	1×4 at TE	12.5	0.18	<0.4		< 0.68	100 (IL < 0.22) 150 (IL < 0.50 OLL < 0.34)	40 (IL < 0.4, OU < 0.68) 104 (IL < 0.89, OU < 0.62)	
DCs [22]	1×4 at TE 1×3 at TE	56 7.3	~ 0.1 0.016	0.42	~ 0.08 0.058	<1	130 (IL < 0.39, OU < 0.34) $111 (IL < 0.031, OU < 1)$	104 (IL < 0.89, OU < 0.62) 50 (IL < 0.82, OU < 1)	
This work	1 × 3 at TM 1 × 3 at TE	4.24	0.065 0.43	0.62 0.75	0.021 0.023	<1 0.64	90 (IL < 0.17, $OU < 1$) 180 (IL < 0.6, $OU < 1$)	54 (IL < 1.5, $OU < 1$) 54 (IL < 1.2, $OU < 0.9$)	
	1×3 at TM	4.02	0.24	0.58	0.015	0.32	190 (II < 0.6 OII < 1)	49 (II < 1.35 OII < 1)	
	1×4 at TM	ч.02	0.18	0.66	0.0005	0.43	100 (IL < 0.0, 00 < 1)	(12 < 1.5), 00 < 1)	

"Simulated results.

^bExperimental results.

IL and OU shown in parentheses are in dB.

Research Article

power splitters can only realize a specific $1 \times N$ power splitter even with polarization-dependent operations [11,17,20,22].

4. CONCLUSION

In conclusion, we have proposed an ultracompact, polarizationindependent, and scalable power splitting model by using the FBSWG metamaterials instead of straight SWGs. For both input TE and TM modes, they can be effectively expanded by the FBSWGs within an ultracompact region. Due to the same optical path from the input taper to the bending grating lines, wavefronts of expanded TE and TM modes are curved. By using N-angled output tapers with optimized parameters to match these wavefronts, the TE and TM modes expanded by the FBSWGs can be collected and evenly distributed into *N*-output channels through that ultracompact region. Based on such a splitting model, a 1×2 , a 1×3 , a 1×4 , and a 1×5 OPSs are designed, and the latter three are experimentally demonstrated with device lengths all lower than $4.3 \,\mu\text{m}$, which are the shortest 1×3 , 1×4 , and 1×5 OPSs with polarizationindependent operations reported until now, to our knowledge. The measurements show low ILs (<1.2 dB, <1.35 dB, and <1.65 dB) and low OUs (<0.9 dB, <1 dB, and <1 dB) over bandwidths of 54 nm (1513-1567 nm), 49 nm (1512-1561 nm), and 38 nm (1521–1559 nm) for the 1 × 3, 1×4 , and 1×5 power splitters, respectively. Different from conventional power splitters, the proposed model is the first power splitting configuration in which an ultracompact device length ($<4.3 \mu m$) and a polarization-independent operation can be maintained simultaneously when the output channels are increased from 2 to 5 as far as we know. We believe such a power splitting model could find its applications in ultrahigh-density on-chip PICs and further provide new insight into the development of ultracompact, scalable, and polarizationindependent integrated nanophotonic devices.

Funding. Scientific Research Foundation of the Graduate School of Southeast University (YBPY2135); Natural Science Foundation of Jiangsu Province (BK20211163); National Natural Science Foundation of China (11574046, 12004092).

Disclosures. The authors declare no conflicts of interest.

Data Availability. Data underlying the results presented in this paper are not publicly available at this time but may be obtained from the authors upon reasonable request.

REFERENCES

- S. Y. Siew, B. Li, F. Gao, H. Y. Zheng, W. Zhang, P. Guo, S. W. Xie, A. Song, B. Dong, L. W. Luo, C. Li, X. Luo, and G.-Q. Lo, "Review of silicon photonics technology and platform development," J. Lightwave Technol. **39**, 4374–4389 (2021).
- S. Ummethala, J. N. Kemal, A. S. Alam, M. Lauermann, A. Kuzmin, Y. Kutuvantavida, S. H. Nandam, L. Hahn, D. L. Elder, L. R. Dalton, T. Zwick, S. Randel, W. Freude, and C. Koos, "Hybrid electro-optic modulator combining silicon photonic slot waveguides with high-k radio-frequency slotlines," Optica 8, 511–519 (2021).
- M. Xu and X. Cai, "Advances in integrated ultra-wideband electrooptic modulators [Invited]," Opt. Express 30, 7253–7274 (2022).

- S. Ghosh and B. M. A. Rahman, "Design of on-chip hybrid plasmonic Mach-Zehnder interferometer for temperature and concentration detection of chemical solution," Sens. Actuators B Chem. 279, 490– 502 (2019).
- J. Sun, E. Timurdogan, A. Yaacobi, Z. Su, E. S. Hosseini, D. B. Cole, and M. R. Watts, "Large-scale silicon photonic circuits for optical phased arrays," IEEE J. Sel. Top. Quantum Electron. 20, 264–278 (2014).
- L. Song, H. Li, and D. Dai, "Mach–Zehnder silicon-photonic switch with low random phase errors," Opt. Lett. 46, 78–81 (2021).
- Y. Tian, Z. Liu, T. Ying, H. Xiao, Y. Meng, L. Deng, Y. Zhao, A. Guo, M. Liao, G. Liu, and J. Yang, "Experimental demonstration of an optical Feynman gate for reversible logic operation using silicon micro-ring resonators," Nanophotonics 7, 333–337 (2018).
- Z. Lin and W. Shi, "Broadband, low-loss silicon photonic Y-junction with an arbitrary power splitting ratio," Opt. Express 27, 14338– 14343 (2019).
- H. Morino, T. Maruyama, and K. liyama, "Reduction of wavelength dependence of coupling characteristics using Si optical waveguide curved directional coupler," J. Lightwave Technol. 32, 2188–2192 (2014).
- L. B. Soldano and E. C. M. Pennings, "Optical multi-mode interference devices based on self-imaging: principles and applications," J. Lightwave Technol. 13, 615–627 (1995).
- K. K. Chung, H. P. Chan, and P. L. Chu, "A 1 × 4 polarization and wavelength independent optical power splitter based on a novel wide-angle low-loss Y-junction," Opt. Commun. 267, 367–372 (2006).
- L. Liu, Q. Deng, and Z. Zhou, "Manipulation of beat length and wavelength dependence of a polarization beam splitter using a subwavelength grating," Opt. Lett. 41, 5126–5129 (2016).
- Y. Zhang, S. Yang, A. E.-J. Lim, G.-Q. Lo, C. Galland, T. Baehr-Jones, and M. Hochberg, "A compact and low loss Y-junction for submicron silicon waveguide," Opt. Express 21, 1310–1316 (2013).
- Z. Sheng, Z. Wang, C. Qiu, L. Li, A. Pang, A. Wu, X. Wang, S. Zou, and F. Gann, "A compact and low-loss MMI coupler fabricated with CMOS technology," IEEE Photon. J. 4, 2272–2277 (2012).
- C. Ye and D. Dai, "Ultra-compact broadband 2 × 2 3 dB power splitter using subwavelength-grating-assisted asymmetric directional coupler," J. Lightwave Technol. 38, 2370–2375 (2020).
- M. Zhang, R. Malureanu, A. C. Krüger, and M. Kristensen, "1 x 3 beam splitter for TE polarization based on self-imaging phenomena in photonic crystal waveguides," Opt. Express 18, 14944–14949 (2010).
- X. Li, H. Xu, X. Xiao, Z. Li, J. Yu, and Y. Yu, "Compact and low-loss silicon power splitter based on inverse tapers," Opt. Lett. 38, 4220– 4223 (2013).
- D. Dai, J. Bauters, and J. E. Bowers, "Passive technologies for future large-scale photonic integrated circuits on silicon: polarization handling, light non-reciprocity and loss reduction," Light Sci. Appl. 1, e1 (2012).
- D. Dai, L. Liu, S. Gao, D. X. Xu, and S. He, "Polarization management for silicon photonic integrated circuits," Laser Photon. Rev. 7, 303–328 (2013).
- R. Yao, H. Li, B. Zhang, W. Chen, P. Wang, S. Dai, Y. Liu, J. Li, Y. Li, Q. Fu, T. Dai, H. Yu, J. Yang, and L. Pavesi, "Compact and low-insertion-loss 1 × N power splitter in silicon photonics," J. Lightwave Technol. **39**, 6253–6259 (2021).
- Y. Luo, Y. Yu, W. Wu, and X. Zhang, "Polarization-insensitive 3-dB coupler for polarization and wavelength division multiplexed systems," IEEE Photon. Technol. Lett. 29, 102–105 (2017).
- H. Li, W. Chen, P. Wang, S. Dai, Y. Liu, Q. Fu, J. Li, Y. Li, T. Dai, H. Yu, and J. Yang, "Compact and low-loss 1 × 3 polarization-insensitive optical power splitter using cascaded tapered silicon waveguides," Opt. Lett. 45, 5596–5599 (2020).
- S. H. Tao, Q. Fang, J. F. Song, M. B. Yu, G. Q. Lo, and D. L. Kwong, "Cascade wide-angle Y-junction 1 × 16 optical power splitter based on silicon wire waveguides on silicon-on-insulator," Opt. Express 16, 21456–21461 (2008).
- A. Hosseini, D. N. Kwong, Y. Zhang, H. Subbaraman, X. Xu, and R. T. Chen, "1 × N multimode interference beam splitter design techniques for on-chip optical interconnections," IEEE J. Sel. Top. Quantum Electron. **17**, 510–515 (2011).

- C. Pérez-Armenta, A. Ortega-Moñux, J. Luque-González, R. Halir, P. Reyes-Iglesias, J. Schmid, P. Cheben, Í. Molina-Fernández, and J. G. Wangüemert-Pérez, "Polarization-independent multimode interference coupler with anisotropy-engineered bricked metamaterial," Photon. Res. **10**, A57–A65 (2022).
- R. Halir, P. J. Bock, P. Cheben, A. Ortega-Moñux, C. Alonso-Ramos, J. H. Schmid, J. Lapointe, D. X. Xu, J. G. Wangüemert-Pérez, Í. Molina-Fernández, and S. Janz, "Waveguide sub-wavelength structures: a review of principles and applications," Laser Photon. Rev. 9, 25–49 (2015).
- C. Li, M. Zhang, H. Xu, Y. Tan, Y. Shi, and D. Dai, "Subwavelength silicon photonics for on-chip mode-manipulation," PhotoniX 2, 11 (2021).
- R. Halir, A. Ortega-Moñux, D. Benedikovic, G. Z. Mashanovich, J. G. Wangüemert-Pérez, J. H. Schmid, Í. MolinaFernández, and P. Cheben, "Subwavelength-grating metamaterial structures for silicon photonic devices," Proc. IEEE **106**, 2144–2157 (2018).
- R. F. de Cabo, D. González-Andrade, P. Cheben, and A. V. Velasco, "High-performance on-chip silicon beamsplitter based on subwavelength metamaterials for enhanced fabrication tolerance," Nanomaterials 11, 1304 (2021).
- J. Xiao and Z. Guo, "Ultracompact polarization-insensitive power splitter using subwavelength gratings," IEEE Photon. Technol. Lett. 30, 529–532 (2018).
- H. Yun, L. Chrostowski, and N. A. F. Jaeger, "Ultra-broadband 2 × 2 adiabatic 3 dB coupler using subwavelength-grating-assisted silicon-on-insulator strip waveguides," Opt. Lett. 43, 1935–1938 (2018).
- L. Xu, Y. Wang, A. Kumar, E. El-Fiky, D. Mao, H. Tamazin, M. Jacques, Z. Xing, M. G. Saber, and D. V. Plant, "Compact high-performance adiabatic 3-dB coupler enabled by subwavelength grating slot in the silicon-on-insulator platform," Opt. Express 26, 29873–29885 (2018).

- A. Zhang, L. Xia, T. Li, C. Chang, P. Zhou, X. Xu, and Y. Zou, "Ultracompact polarization-independent 3 dB power splitter in silicon," Opt. Lett. 46, 5000–5003 (2021).
- M. M. Gilarlue, J. Nourinia, C. Ghobadi, S. H. Badri, and H. R. Saghai, "Multilayered Maxwell's fisheye lens as waveguide crossing," Opt. Commun. 435, 385–393 (2019).
- S. H. Badri, H. R. Saghai, and H. Soofi, "Polymer multimode waveguide bend based on a multilayered Eaton lens," Appl. Opt. 58, 5219–5224 (2019).
- F. Van Laere, T. Claes, J. Schrauwen, S. Scheerlinck, W. Bogaerts, D. Taillaert, L. O'Faolain, D. Van Thourhout, and R. Baets, "Compact focusing grating couplers for silicon-on-insulator integrated circuits," IEEE Photon. Technol. Lett. 19, 1919–1921 (2007).
- S. Li, L. Cai, D. Gao, J. Dong, J. Hou, C. Yang, S. Chen, and X. Zhang, "Deterministic design of focusing apodized subwavelength grating coupler based on weak form and transformation optics," Opt. Express 28, 35395–35412 (2020).
- A. Mekis, S. Gloeckner, G. Masini, A. Narasimha, T. Pinguet, S. Sahni, and P. De Dobbelaere, "A grating-coupler-enabled CMOS photonics platform," IEEE J. Sel. Top. Quantum Electron. 17, 597–608 (2011).
- H. Becker, C. J. Krückel, D. Van Thourhout, and M. J. R. Heck, "Out-of-plane focusing grating couplers for silicon photonics integration with optical MRAM technology," IEEE J. Sel. Top. Quantum Electron. 26, 8300408 (2020).
- D. M. Sullivan, Electromagnetic Simulation Using the FDTD Method (Wiley, 2013).
- D. F. G. Gallagher and T. P. Felici, "Eigenmode expansion methods for simulation of optical propagation in photonics: pros and cons," Proc. SPIE 4987, 69–82 (2003).
- Z. Wang, Z. Fan, J. Xia, S. Chen, and J. Yu, "1 × 8 cascaded multimode interference splitter in silicon-on insulator," Jpn. J. Appl. Phys. 43, 5085–5087 (2004).