

1 550 nm long-wavelength vertical-cavity surface emitting lasers*

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(Received 14 March 2018; Revised 20 April 2018)

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A 1 550 nm long-wavelength vertical cavity surface emitting laser (VCSEL) on InP substrate is designed and fabricated. The transfer matrix is used to compute reflectivity spectrum of the designed epitaxial layers. The epitaxial layers mainly consist of 40 pairs of n-Al_xGa_yIn_(1-x-y)As/InP, and 6 strain compensated Al_xGa_yIn_(1-x-y)As/InP quantum wells on n-InP substrate, respectively. The top distributed Bragg reflection (DBR) mirror system has been formed by fabricating 4.5 pairs of SiO₂/Si. The designed cavity mode is around 1 536 nm. The dip of the fabricated cavity mode is around 1 530 nm. The threshold current is 30 mA and the maximum output power is around 270 μW under CW operation at room temperature.

Document code: A **Article ID:** 1673-1905(2018)05-0342-4

DOI <https://doi.org/10.1007/s11801-018-8037-6>

Semiconductor diode lasers can be used in a variety of applications including telecommunications, displays, solid-state lighting, sensing and printing^[1-4]. Among them, vertical cavity surface emitting lasers (VCSELs) are particularly promising^[5]. Because VCSEL has some unique properties such as wafer-scale testing, low-cost packaging, and ease of fabrication into arrays. Low loss in optical fibers, low dispersion for 1 550 nm optical fibers, higher eye safe maximum limit power and lower operation voltage are some of the advantages of long wavelength VCSELs over 850 nm and 980 nm GaAs-based VCSELs^[6,7]. Over the past decades, long-wavelength spectral regimes were reported mainly by two research teams, one of which is Chang-Hasnain team of California at Berkeley, California USA. In 2009, this group presented a high-speed VCSEL operating at data-rate up to 22 Gbit/s, and the wavelength was 1 539 nm^[8]. Next year, they presented a 1 550 nm VCSEL utilizing a high contrast grating (HCG) as a top mirror and proton implantation to form an electrical aperture^[9]. The other team is M.-C. Amann team of Technische Universität München, Garching Germany. They got a high-performance VCSEL at 1 550 nm with superior output characteristics and modulation bandwidths up to 10 Gbit/s in 2002^[10]. They utilized buried tunnel junction (BTJ) and fabricated VCSEL arrays, and the data rate was up to 12.5 Gbit/s over different fiber channels^[11,12]. Single-mode emission, high-speed operation, high-contrast grating devices and wavelength tuning are the international research group

aim. But in China, research teams mainly focus on the wavelength range from 850 nm to 980 nm^[13-16].

In this letter, we present InP-based 1 550 nm VCSELs. Al_xGa_yIn_(1-x-y)As has been chosen as the active material^[17]. A structure of VCSEL has been presented in this work, which incorporates Al_xGa_yIn_(1-x-y)As/InP bottom distributed Bragg reflection (DBR) and SiO₂/Si top DBR. By using a dielectric DBR, we can easily control the cavity resonance wavelength by changing the thickness of one dielectric layer, even if the initial cavity wavelength is mismatched from the gain peak or DBR center wavelength after crystal growth. Moreover, large index contrast, small penetration depth, reduced photon lifetime and larger bandwidth properties can be achieved.

For the design of high performance VCSELs, it is important to know the reflectivity spectra of the epitaxial layers in the resonator. A local minimum in the reflectivity spectrum occurs at the lasing wavelength of a longitudinal mode. We use transfer matrix method to calculate reflectivity spectrum. The transfer matrix is written as^[18]

$$M = \begin{pmatrix} \hat{e}B \\ \hat{e}C \end{pmatrix} \begin{pmatrix} \hat{u} \\ \hat{u} \end{pmatrix} = \begin{pmatrix} \hat{u} \\ \hat{u} \end{pmatrix} \begin{pmatrix} \hat{e} \\ \hat{e} \end{pmatrix} \quad (1)$$

$$\begin{pmatrix} \hat{u} \\ \hat{u} \end{pmatrix} = \begin{pmatrix} \cos(kn_j d_j) & \frac{i}{n_j} \sin(kn_j d_j) \\ \frac{i}{n_j} \sin(kn_j d_j) & \cos(kn_j d_j) \end{pmatrix} \begin{pmatrix} \hat{u} \\ \hat{u} \end{pmatrix} \quad (1)$$

* This work has been supported by the National High Technology and Development Program of China (No.2015AA016902).

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$$R = \frac{\alpha n_0 B - C \quad \alpha n_0 B - C \quad \alpha}{\alpha n_0 B + C \quad \alpha n_0 B + C \quad \alpha} \quad (2)$$

$$f = \arctg \frac{\alpha n_0 (CB^* - BC^*)}{\alpha n_0^2 (BB^* - CC^*)} \quad (3)$$

where M is a characteristic matrix, R is reflectivity, ϕ is reflectivity phase, n_j and d_j are refractive index and geometric thickness in the j th layer, i is imaginary unit, $k=2\pi/\lambda$, n_0 is incident medium refractive index, and n_{N+1} is emergent medium refractive index.

According to the above three formulas, the reflectivity spectrum of the designed epitaxial layers is calculated. The epitaxial layers from bottom consist of 40 pairs of $n\text{-Al}_x\text{Ga}_{1-x}\text{In}_{1-x-y}\text{As/InP}$, and 6 strain compensated $\text{Al}_x\text{Ga}_y\text{In}_{1-x-y}\text{As/InP}$ quantum wells on $n\text{-InP}$ substrate, respectively. The top DBR mirror system has been formed by fabricating 4.5 pairs of SiO_2/Si . Between multiple quantum well (MQW) and DBR, the separate confinement heterojunction (SCH) structures are all fully lattice matched with the InP material. The schematic diagram of epitaxial layers is shown in Fig.1. The device is electrically injected and the proton implantation serves to form the current confining aperture. Fig.2(a) shows the simulation reflectivity spectrum. From Fig.2(b), a dip in the stop-band clearly proves the presence of a cavity mode in the resonator. The reflectivity of simulation cavity mode is around 88%. The wavelength is around 1 536 nm.

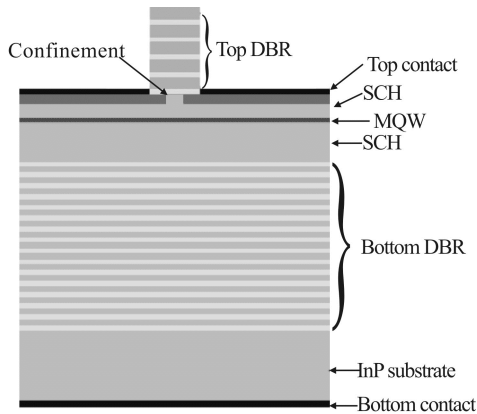


Fig.1 Schematic diagram of VCSEL

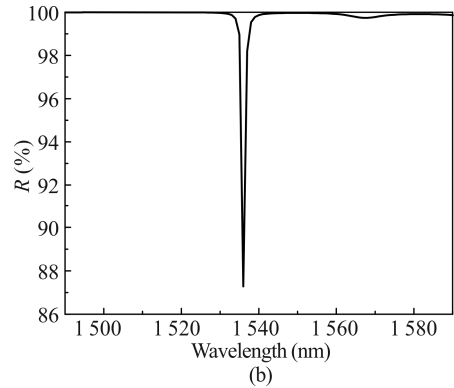
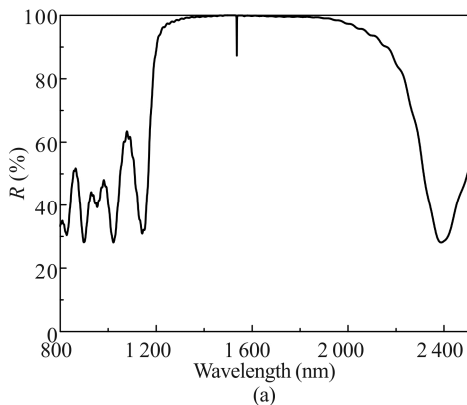


Fig.2 (a) Epitaxial layers reflectivity spectrum of simulation diagram; (b) Cavity mode reflectivity spectrum of simulation diagram

The devices were fabricated by InP processing platform. The bottom DBRs and active layers were grown on $n\text{-InP}$ substrate. After the growth of the active layers and SCH, the proton was implanted by a photo-resist patterning and wet etching process to form the current confining aperture. TiPtAu top contact metals were evaporated and the light output windows were removed for dielectric top DBR mirror. A 4.5-pairs- SiO_2/Si dielectric DBR was evaporated by E-beam evaporation. The TiPtAu top contact metals were followed by etching of the SiO_2/Si layers. Finally, the NiGeAu bottom contact metals were evaporated.

Fig.3(a) is reflectivity spectrum of the all epitaxial layers structure. There is a dip in the stop-band clearly in the resonator. From Fig.3(b), the reflectivity of experiment cavity mode is around 94%. Compared with Fig.2(b), the reflectivity 94% of experiment cavity mode is lower than the simulation one 88%. There are absorption and loss from the semiconductor-dielectric interface. The maximum transmittance in the cavity mode^[19] is

$$T_{\max} = \frac{T_1 T_2}{(1 - \bar{R})^2} \quad (4)$$

$$\bar{R} = \sqrt{R_1 R_2} \quad (4)$$

where R_1 and R_2 are the reflectivities of top and bottom DBRs, respectively. T_1 and T_2 are the transmittances of top and bottom DBRs, respectively. If there is no absorption and loss in all reflectance coatings and the reflectance coating is symmetrical, we will get

$$R_1=R_2, T_1=T_2=1-R_1 \quad (5)$$

The maximum transmittance is 1. However, the interface is not ideal in the experiment process. For example, we have three lithography processes before sputtering SiO_2/Si DBR. There may be residue in the interface after the cleaning process or the reflectance coatings quality is not good, which can bring about absorption and loss. We can improve that by adjusting the process order so that lithography process times are decreased and keeping the interface clean in the photoresist removing process.

The cavity mode wavelength is around 1 530 nm. The

cavity mode wavelength deviates by 6nm forward shorter wavelength from 1 536 nm to 1 530 nm. This is because every layer thickness in monolithic growth is thinner than the designed value. The fabricated reflectivity of 95.8% is lower than the designed value 99.8%. Every layer thickness is not easy to control, leading to the real refractive index different from the designed index. Fig.4 shows 30°-inclined top SEM image of the fabricated VCSEL. The circular current confinement diameter is 12 μm. The circular mesa diameter for light window is 56 μm. The top contact ring size is 20 μm. The pad contact area is 100 μm×100 μm.

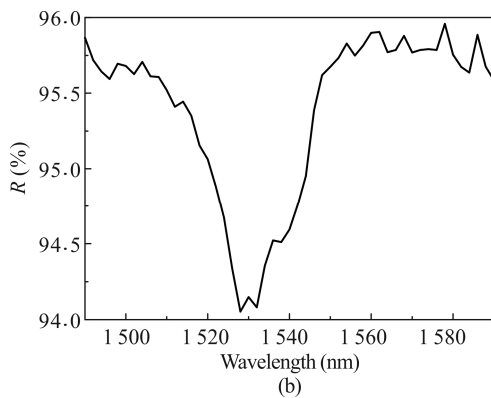
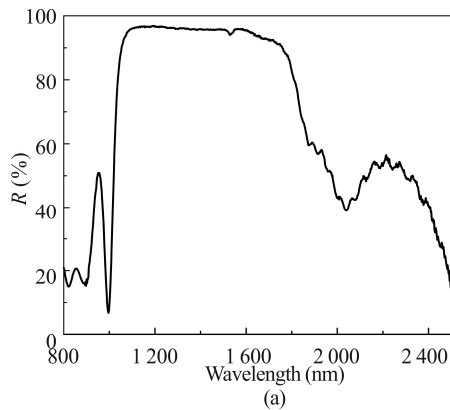


Fig.3 (a) Reflectivity spectrum of the fabricated VCSEL epitaxial structure; (b) Reflectivity spectrum of the fabricated VCSEL cavity mode

To capture the whole power, the devices were mounted in a conventional PCB test chip. Fig.5 shows *I-V* characteristics of the fabricated VCSEL under CW operation at room temperature. Fig.6 shows *I-L* characteristics of the fabricated VCSEL under CW operation at room temperature. The threshold current is 40 mA. The power was measured by an optical spectrometer. The maximum output power is around -7.17 dBm from Fig.6. This is because of the carrier leak in the current-light confinement area resulting in quantum efficiency reduction. The reflectivity 95% of epitaxial layers is lower. The solution is improving proton implant and increasing reflectivity of top DBR. Fig.7 shows the electroluminescence spectrum under 60 mA DC current injection. The

lasing occurred at 1 526 nm in Fig.7. Compared with Fig.3(b), the lasing wavelength 1 526 nm is shorter than the cavity mode wavelength 1 530 nm. The main reason may be Joule heat. The Joule heat comes from laser itself, dielectric top DBR, PCB test board. The Joule heat can result in material index variation, absorption and loss increasing. The full width at half maximum (FWHM) of lasing spectrum is about 5 nm. One reason is that carrier leak in the current-light confinement area resulted in quantum efficiency reduction. The other reason is active MQW gain increasing.

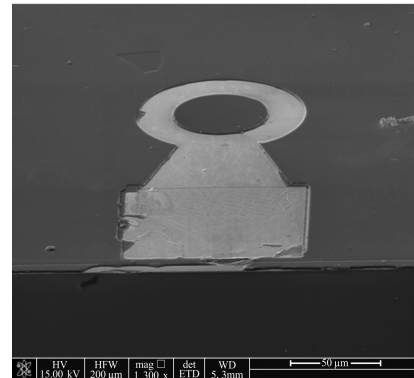


Fig.4 30° inclined top SEM image of the fabricated VCSEL sample

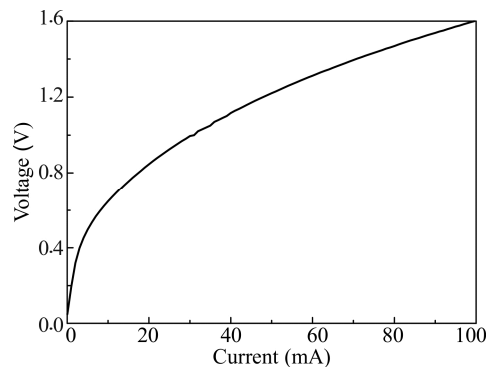


Fig.5 *I-V* characteristics of the fabricated VCSEL at room temperature

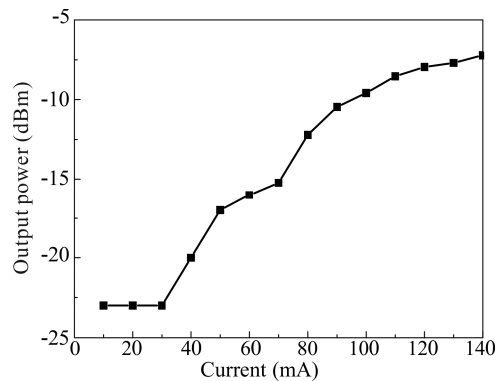


Fig.6 Plot of *I-L* characteristics of the fabricated VCSEL at room temperature

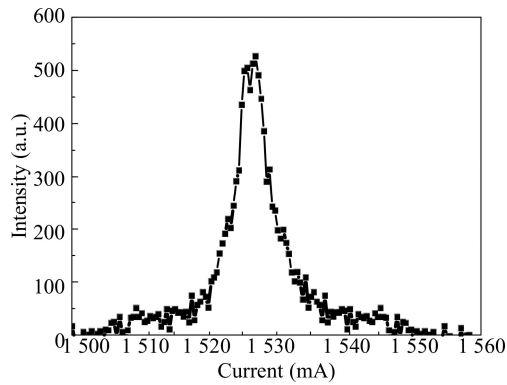


Fig.7 Lasing spectrum of the VCSEL at an injection current of 60 mA

In conclusion, a 1550 nm long-wavelength VCSEL on InP substrate has been designed and fabricated. The designed cavity mode is around 1536 nm. The dip of fabrication cavity mode is around 1530 nm. The threshold current is 40 mA under CW operation at room temperature. The lasing occurred at 1526 nm. The long wavelength VCSELs are promising to be applied in long reach data communication.

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