文章编号: 1001-9014(2021)03-0329-05

# **RF** power performance improvement of multi-finger power bipolar transistor by non-uniform emitter finger spacing design without the use of emitter ballasting resistor

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Abstract: In this paper, the RF power performance and surface temperature distributions for a multi-finger power hetero-junction bipolar transistor (HBT) with non-uniform emitter finger spacing (NUEFS) without the use of emitter-ballasting-resistor (EBR) are measured, and are compared with a multi-finger power HBT with EBR. The experiment results show that for the multi-finger power HBT with NUEFS, the highest surface temperature is lowered, the uniformity of surface temperature distributions measured by US QFI Infrared TMS is improved, the RF power gain and power-added-efficiency (PAE) are increased compared with the multi-finger power HBT with EBR respectively. These results could be attributed to the improvement in positive thermoelectric feedback and thermal coupling effects among the fingers, and the riddance of adverse impact from emitter-ballasting-resistor used in traditional power HBT.

Key words: bipolar transistor, radio frequency (RF), thermal stability, power gain, power-added-efficiency (PAE), multi-finger

# 免镇流电阻的非均匀发射极指间距设计 对多指功率双极晶体管射频功率性能的改善

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摘要:对不添加镇流电阻的非均匀发射极条间距的多发射极条异质结双极晶体管(HBT)的射频功率性能和表 面温度分布进行了测量,并与常规采用镇流电阻的多发射极条功率HBT进行了比较。实验结果表明,对具有 非均匀发射极条间距的多发射极条 HBT.采用 US OFI TMS 红外测量系统测得的最高表面温度、温度分布均匀 性以及采用射频测量系统测得的射频功率增益和功率附加效率,分别低于、好于和高于具有镇流电阻的多发 射极条功率HBT的情况。这些结果的取得,得益于采用非均匀发射极条间距改善了多发射极条HBT的热电 正反馈和不同发射极条之间的热耦合,以及摆脱了传统HBT加镇流电阻带来的对射频功率性能的负作用。 关键 词:双极晶体管;射频;热稳定性;功率增益;功率附加效率;多指

中图分类号:TN128

#### 文献标识码:A

## Introduction

Power bipolar junction transistor (BJT) and heterojunction bipolar transistor (HBT) usually employ multiple emitter fingers in parallel to improve the current handling capability, thermal dissipation capability and RF power performance<sup>[1-4]</sup>. However, the self-heating effects

(SHE) caused by the power dissipation in every emitter finger and thermal coupling effects (TCE) among emitter fingers result in the increase of temperature of HBTs and non-uniformity of temperature distributions among emitter fingers, and as a consequence, the thermal stability, reliability and RF power performance will degrade significantly<sup>[4-8]</sup>. It is obvious that the entire device will fail if

收稿日期:2020-06-20,修回日期:2020-12-29 Foundation items: Supported by National Natural Science Foundation of China (61774012, 61574010)

Received date: 2020-06-20, revised date: 2020-12-29

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any single emitter finger fails. Particularly, the temperatures at the center fingers are hotter than outer fingers. Therefore it will draw much current due to the nature of the positive temperature coefficient of emitter current. Hence additional joule heating is generated, the mechanism is thus regenerative and it ultimately destroys the device. To inhibit positive thermoelectric feedback between emitter current and temperature, and to improve thermal stability and increase current handling capability, the common method is to add a ballasting-resistor  $R_{\rm E}$ at emitter so as to compensate for the variation of emitterbase junction voltage  $V_{\rm BE}$  by using voltage drop across  $R_{\rm F}^{[7-12]}$ . For examples, Schuppen A. *et al.* <sup>[10]</sup> designed 10 fingers and 60 fingers power HBTs with uniform finger spacing, in which the  $R_{\rm E}$  of 6  $\Omega$  is added at each emitter to improve thermal effects, the output power was 1W; Potyraj, P. A. *et al.* [11] reported multi-finger power HBTs with uniform emitter finger spacing, the TiW ballasting-resistor  $R_{\rm F}$  was added at each emitter to improve current handling capability; Ma Z. et al. [12] investigated the effects of  $R_{\rm E}$ , base-ballasting-resistor  $R_{\rm B}$ , and nonballasting-resistor on the thermal stability of three kinds of 160 fingers HBTs with uniform finger spacing, the results showed that three types of HBTs were all thermally stable under  $V_{CE}$  =3 V, the two types of HBTs with  $R_{B}$  and  $R_{\rm E}$  were thermally stable under  $V_{\rm CE}$  =4 V, but only the HBTs with  $R_{\rm E}$  was thermally stable under  $V_{\rm CE}$  =5 V, and therefore the  $R_{\rm E}$  was proved to be more effective in thermal stability improvement compared with  $R_{\rm B}$  technique. However, the additional  $R_{\rm E}$  at emitter will degrade HBT RF power performance, such as power gain  $(G_p)$  and power-added-efficiency (PAE)<sup>[8-12]</sup> no matter how to optimize the  $R_{\rm E}$ . As an alternative method, a non-uniform finger spacing technique with large finger spacing in center fingers and small finger spacing in outer fingers for multi-finger HBTs can alleviate thermal coupling effects (TCE) among emitter fingers due to the improvements in thermal flow distribution. Our previous works [13-15] revealed that the non-uniform emitter finger spacing (NUEFS) technique is effective in lowering peak temperature and enhancing uniformity of the temperature profile among emitter fingers compared with uniform emitter finger spacing technique. As an extension of our previous works, in this paper, a comparative study between two types of multi-finger power HBTs with NUEFS and emitter ballasting-resistor  $(R_{\rm E})$  is performed to exhibit the superiority of NUEFS technique in improvements of RF power gain, power-added-efficiency (PAE), and temperature profile over emitter ballasting-resistor  $(R_{\rm E})$  technique. To the best of the authors' knowledge, the comparative study on RF power performances of the two types of multi-finger HBTs is not available in the literature. The paper is arranged as follows. In Section I, the mechanism and disadvantages of the introduction of  $R_{\rm E}$  to improve HBT current handling capability are briefly analyzed; the structure and layouts of two types of multi-finger power HBTs with NUEFS and  $R_{\rm F}$  are shown in Section II; the measurement results of surface temperature distributions by US QFI Infrared TMS, RF power gain  $(G_p)$ ,

collector efficiency  $(\eta_c)$  and power-added-efficiency (PAE) are demonstrated in Section III; the conclusions are given in Section IV.

## 1 Mechanism and disadvantages of the introduction of emitter ballasting resistor to improve HBT current handling capability

For an HBT, emitter-base junction voltage  $V_{\rm BE}$  varies with temperature *T*. Figure 1 shows the measured  $V_{\rm BE}$  as a function of *T* under different emitter currents of 3 mA, 1 mA, 100  $\mu$ A and 10  $\mu$ A respectively for our a SiGe HBT. The temperature coefficient ( $dV_{\rm BE}/dT$ ) of  $V_{\rm BE}$  can be obtained as -1.53 mV/°C, -1.59 mV/°C, -1.76 mV/°C and -1.92 mV/°C, respectively. The  $dV_{\rm BE}/dT$  is a negative value. Therefore, for an HBT, as the current increases, the temperature increases due to SHE and TCE. As a result, interior  $V_{\rm BE}$  decreases. Under external bias keep unchanged, the emitter current will increase, which will further increase dissipation power and lead to temperature rise. Ultimately, the device will be burned out if the measures to restrict the increase of current is not taken.



Fig. 1 The measured emitter-base junction voltage  $V_{\text{BE}}$  as a function of *T* under different emitter currents of 3 mA, 1 mA, 100  $\mu$ A and 10  $\mu$ A respectively

图 1 在发射极电流分别为 3 mA, 1 mA, 100 μA 和 10 μA 下, 发射结电压与温度关系的测量结果

In order to alleviate positive thermoelectric feedback between emitter current and temperature, and protect the HBT from thermal burn-out, it is common to add a ballasting-resistor  $R_{\rm Ei}$  at emitter as shown in Fig. 2 so as to compensate the variation of emitter-base junction voltage  $V_{\rm BE}$  by using voltage drop across  $R_{\rm Ei}$ .

To guarantee thermal stability of HBT within a certain limit of emitter current  $(I_{\rm E})$ , the engineering regulation for ballasting resistor  $R_{\rm E}$  is as follows: when emitterbase junction temperature varies by ±5 K, the added  $R_{\rm E}$ at emitter finger could able to restrict the variation of  $I_{\rm E}$ within ±5%, so the minimum emitter ballasting-resistor  $R_{\rm Emin}$  can be expressed as<sup>[8-9]</sup>

$$R_{\rm Emin} = \frac{\Delta V}{\Delta I} = \frac{5 \times (\mathrm{d}V_{\rm BE}/\mathrm{d}T)}{0.05 \times I_{\rm E}} = 100 \frac{\mathrm{d}V_{\rm BE}/\mathrm{d}T}{I_{\rm E}} , \quad (1)$$



Fig. 2 The schematic diagram of an *N*-finger HBT, where  $R_{Ei}$  is the emitter ballasting resistor of the *i*th emitter finger<sup>[8-9]</sup>

图 2 具有 N个发射极指的 HBT 的示意图, R<sub>Ei</sub> 是第 i个发射极指上的镇流电阻

where units of  $dV_{\rm\scriptscriptstyle BE}/dT$  and  $I_{\rm\scriptscriptstyle E}$  are mV/°C and mA respectively.

We can see that  $R_{\text{Emin}}$  is determined by threshold emitter current  $(I_{\text{E}})$ , it is only effective for an HBT to thermally stable operate under a certain of emitter current  $(I_{\text{E}})$ .

However, on the other hand, the additional emitter ballasting-resistor  $R_{\rm E}$  will degrade RF power performance no matter how to optimize the  $R_{\rm E}$ . The parameters of power gain  $(G_{\rm p})$  and power-added-efficiency (PAE), which are employed to characterize RF power performance, are related to ballasting-resistor  $R_{\rm E}$  as follows:

$$G_{p} = \frac{p_{\text{out}}}{p_{\text{in}}} = \frac{J_{\text{T}}}{8\pi f^{2} (r_{\text{b}} + R_{\text{E}}) C_{\text{TC}}} \qquad , \quad (2)$$

PAE = 
$$\frac{p_{\text{out}} - p_{\text{in}}}{p_{\text{DC}}} = (1 - \frac{1}{G_{\text{p}}}) \frac{p_{\text{out}}}{p_{\text{DC}}} = (1 - \frac{1}{G_{\text{p}}}) \eta_{\text{C}}$$
, (3)

where  $p_{out}$  is RF output power,  $p_{in}$  is RF input power,  $f_{T}$  is transit frequency,  $r_{b}$  is base resistance,  $C_{TC}$  is collector output capacitance,  $P_{DC}$  is DC power,  $\eta_{c}=p_{out}/P_{DC}$  is collector efficiency.

Therefore,  $G_{\rm p}$  and PAE are degraded due to the existence of  $R_{\rm E}$ . Furthermore, in order to guarantee the thermal stability of HBT within a certain of emitter current  $(I_{\rm E})$  and power, it is usually suggested that  $R_{\rm E} > R_{\rm Emin}$ , this could further degrade  $G_{\rm p}$  and PAE.

### 2 Structure and layouts of multi-finger power HBTs

The schematic cross section of a cell of multi-finger HBTs based on SiGe process and the material parameters of various layers of the device are shown in Fig. 3 and Table 1, respectively. The power HBTs is fabricated in Institute of Microelectronics, Tsinghua University.

Fig. 4 and Fig. 5 are the micrographs of the fabricated multi-finger power SiGe HBTs with polysilicon as emitter ballasting resistor and non-uniform finger spacing, respectively.

#### 3 Measurement results and discussion

In this section, the measured surface temperature distributions by US QFI Infrared TMS, and the measured power gain  $(G_p)$ , collector efficiency  $(\eta_c)$  and power-



Fig. 3 The schematic-cross section of a cell in multi-finger HBTs

图3 多指HBTs中一个单元的剖面示意图

#### Table 1 The material parameters of various layers for SiGe HBTs.

表1 SiGe HBT 各层的材料参数

|           | composition                          | Thickness/nm        | Doping concentration/cm <sup>-3</sup> |
|-----------|--------------------------------------|---------------------|---------------------------------------|
| Emitter   | Si                                   | 12                  | 1×10 <sup>18</sup>                    |
| Base      | ${\rm Si}_{0.\ 84}{\rm Ge}_{0.\ 16}$ | 30                  | 2×10 <sup>19</sup>                    |
| Collector | Si                                   | 4. $4 \times 10^3$  | 1×10 <sup>16</sup>                    |
| Substrate | Si                                   | 1.5×10 <sup>5</sup> | 1×10 <sup>19</sup>                    |



Fig. 4 Micrographs of the fabricated multi-fingers SiGe HBTs with emitter ballasting resistor. (Note: polysilicon emitter ballasting resistors locate at root terminal of each emitter finger, see the enlarged image)

图4 制作的具有发射极镇流电阻的多指 SiGe HBTs 的显微照 片(注:多晶硅发射极镇流电阻在每个发射极指的根部,见局 部放大图)

added-efficiency (PAE) for two types of multi-fingers SiGe HBTs with non-uniform finger spacing (NUEFS) and emitter ballasting resistor (EBR) respectively are shown and compared.

# **3.1** Surface temperature distributions measurements of multi-fingers power HBTs

The surface temperature distributions measured by temperature measurement microscope systems (TMS) from US quantum focus instruments (QFI) Corporation for two types of multi-fingers HBTs with emitter ballasting resistor (EBR) and with non-uniform emitter finger spacing (NUEFS) under  $I_c$ =800 mA,  $V_{cE}$ =5 V and

40卷



Fig. 5 Micrograph of the fabricated multi-fingers SiGe HBTs with non-uniform finger spacing

图 5 制作的具有非均匀指问距的多指 SiGe HBTs 的显微照片

case temperature of  $T_c$ =80°C are shown in Fig. 6. We can see that the highest temperature is lowered and the maximum temperature difference is improved for the HBTs with NUEFS compared with the HBTs with EBR. Therefore, the temperature distribution uniformity is improved as expected. These results could be attributed to the decrease of the heat flow from adjacent fingers to the center fingers by increasing the spacing between fingers in center region where the thermal-coupling effects are strongest and the alleviation of non-uniform variation of  $V_{\text{BE}}$  with temperature T.

#### 3.2 RF power performance measurements

The package and RF power measurements of two types of multi-finger power HBTs with non-uniform finger spacing (NUEFS) and emitter ballasting resistor (EBR) are performed by The 13th Research Institute of China Electronic Technology Corporation. The measurement system mainly consists of a power signal source, isolator, directional coupler, test fixture, attenuator, power meter, bias power supply, and so on, as shown in Fig 7.

The measured output power  $(p_{out})$  versus input power  $(p_{in})$ , collector efficiency  $(\eta_c)$  versus input power  $(p_{in})$ , and power-added-efficiency (PAE) versus input power  $(p_{in})$  for two types of multi-fingers SiGe HBTs with emitter ballasting resistor (EBR) and non-uniform emitter finger spacing (NUEFS) under class C operation and at the frequency of 1. 2 GHz are shown in Figs. 8~10, respectively.

As shown, for the multi-fingers SiGe HBTs with EBR,  $p_{out}$  increases with the increase of  $p_{in}$ , but  $p_{out}$  tends to saturate. At  $p_{in}=0.9$  W, the  $p_{out}=1.9$  W, power gain  $G_p=3.25$ dB,  $\eta_c=72\%$ , and PAE=40%. For the multi-fingers SiGe HBTs with NUEFS,  $p_{out}$  increases linearly with the increase of  $p_{in}$  and does not saturate. At  $p_{in}=0.9$  W, the  $p_{out}=4W$ , power gain  $G_p=6.48$ dB,  $\eta_c=63\%$ , and PAE=49\%. The comparison between the HBT with NUEFS and the HBT with EBR indicates that the  $p_{out}$  is increased by 2.1W. This is because that for the same RF input voltage signal, there is not voltage drop across  $R_E$  in the HBT with NUEFS. Therefore, the current gain is improved, and the collector current  $i_e$  is increased <sup>[16]</sup>. As a result, the  $p_{out}$  is increased according to the expression  $p_{out}$ 



Fig. 6 Measured surface temperature distribution by US QFI Infrared TMS for (a) a multi-finger power HBT with emitter ballasting resistor, and (b)for a multi-finger power HBT with non-uniform finger spacing under  $I_c$ =800 mA,  $V_{CE}$ =5 V and case temperature of  $T_c$ =80 °C

图 6 在  $I_c$ =800 mA 和  $V_{ce}$ =5 V条件下,通过美国 QFI Infrared TMS 红外测量系统得到的(a)具有发射极镇流电阻的多指 HBT,(b)具有非均匀发射极指间距的多指 HBT 器件的表面温 度分布



Fig. 7 The block diagram of RF power measurement system图 7 射频功率测量系统方块图

more,  $G_{\rm p}$  is increased by 3. 23 dB,  $\eta_{\rm c}$  is decreased by 9%, and PAE is increased by 9%. As expected the RF power performance is improved.

#### 4 Summary and conclusions

A comparative study of two types of multi-finger power HBTs with non-uniform emitter finger spacing (NUEFS) and emitter-ballasting-resistor (EBR) is performed experimentally in terms of surface temperature distributions, RF power gain and power-added-efficiency (PAE). The experiment results show that for the multi-



Fig. 8 RF Output power  $p_{out}$  versus RF input power  $p_{in}$  for two types of multi-fingers power HBTs with emitter ballasting resistor and with non-uniform emitter finger spacing respectively

图 8 具有发射极镇流电阻的多指功率HBT与具有非均匀指间距的功率HBT的射频输出功率pout与射频输入功率pin的关系



Fig. 9 Collector efficiency  $\eta_c$  versus RF input power  $p_{in}$  for two types of multi-fingers power HBTs with emitter ballasting resistor and with non-uniform emitter finger spacing respectively 图 9 具有发射极镇流电阻的多指功率 HBT 与具有非均匀指 间距的功率 HBT 的集电极效率  $\eta_c$  与射频输入功率  $p_{in}$ 的关系



Fig. 10 Power-added-efficiency (PAE) versus RF input power  $p_{in}$  for two types of multi-fingers power HBTs with emitter ballasting resistor and with non-uniform emitter finger spacing respectively

图 10 具有发射极镇流电阻的多指功率HBT与具有非均匀指 间距的功率HBT的功率附加效率PAE与射频输入功率p\_n的关系

finger power HBT with NUEFS, not only the uniformity of surface temperature distributions measured by US QFI Infrared TMS but also RF power gain and power-addedefficiency (PAE) is improved compared with a multi-finger power HBT with EBR. These results could be attributed to improvement in positive thermoelectric feedback and thermal coupling effects among the fingers, and the riddance of adverse impact from emitter-ballasting-resistor used in traditional power HBTs.

#### Acknowledgment

This work is supported in part by the National Science Foundation of China (61774012, 61574010). The chip is fabricated by The 13th Research Institute of China Electronic Technology Corporation. The authors would like to thank Liu Yingkun, Gao Xuebang, Cui Yuxing and He Dawei for the supports in chip fabrication and measurements.

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