

50 m/187.5 Mbit/s real-time underwater wireless optical communication based on optical superimposition

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In this paper, an optical pulse amplitude modulation with 4 levels (PAM-4) using a fiber combiner is proposed to enhance the data rate of a field-programmable gate-array-based long-distance real-time underwater wireless optical communication system. Two on-off keying signals with different amplitudes are used to modulate two pigtailed laser diodes, respectively, and the generated optical signals are superimposed into optical PAM-4 signals by a fiber combiner. The optical PAM-4 scheme can effectively alleviate the nonlinearity, although it reduces the peak-to-peak value of the emitting optical power by 25%. A real-time data rate of 187.5 Mbit/s is achieved by using the optical PAM-4 with a transmission distance of 50 m. The data rate is increased by about 25% compared with the conventional electrical PAM-4 in the same condition.

Keywords: underwater wireless optical communication; FPGA; PAM-4.

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1. Introduction

The earth is known as a blue planet because of the ocean. In the process of developing ocean resources and conducting ocean activities, long-distance and high data rate underwater communication has grown to be a top priority. Underwater wireless optical communication (UWOC) has the advantages of high data rate and low latency, but with the shortcomings of strict link alignment and limited transmission distance^[1-4]. The blue laser diode (LD), with high power, is gradually becoming the mainstream as the light source in the long-distance UWOC system^[5-10]. Besides, many researchers employed high-sensitivity optical detectors such as a multi-pixel photon counter (MPPC), single-photon avalanche diode (SPAD), and photomultiplier tube (PMT) as receivers to detect weak optical signals^[6-11].

In recent years, offline digital signal processing (DSP) technologies have been extensively employed in UWOC systems to increase the data rate and extend the transmission distance. Table 1 lists several impressive progresses in recent years. In 2019, a UWOC system using probabilistic constellation shaping (PCS), quadrature amplitude modulation (QAM), and discrete multi-tone (DMT) technology achieved a data rate of 12.62 Gbit/s over a 35 m underwater channel for the first time^[12], to the best of our knowledge. In the same year, a

UWOC system with a data rate of 2.5 Gbit/s was deployed in a 60 m underwater channel, which used non-return to zero (NRZ) on-off keying (OOK) modulation and a digital nonlinear equalizer^[5]. In 2020, by employing Nyquist single carrier frequency domain equalization with noise prediction, a UWOC system with a data rate of 3.31 Gbit/s over a 56 m underwater channel was demonstrated^[13]. Moreover, a UWOC system utilizing a 3 × 1 fiber combiner and two MPPCs achieved a data rate of 8.39 Mbit/s at a distance of 100 m, and the alignment requirement was relaxed by diversity reception technology^[6]. In 2021, a full-duplex UWOC system was demonstrated, reaching a data rate up to 200 Mbit/s at a distance of 100 m^[7]. In the same year, a joint DSP scheme including partial response shaping and trellis-coded modulation technology was proposed and employed in a UWOC system, which successfully achieved a data rate of 500 Mbit/s with a transmission distance up to 150 m^[8]. Besides, the sparse Volterra equalizer, which can effectively reduce the computational complexity, was developed for the UWOC system to realize a data rate of 500 Mbit/s in a 200 m underwater transmission^[9]. Recently, a wideband PMT-based UWOC system was proposed, in which the transmission distance and data rate were pushed to 100.6 m and 3 Gbit/s, respectively^[10]. The aforementioned UWOC systems have achieved high data rates and long transmission distance, whereas the

Table 1. The Progress of UWOC Systems.

Light Source/ Detector ^a	Underwater Distance (m)	Attenuation Coefficient	Attenuation Length ^b	Data Rate	Modulation Scheme ^c	Hardware ^d	Reference
LD/PIN	35 (tap water)	/	/	12.62 Gbit/s	PCS-QAM-DMT	AWG/OSC	2019 ^[12]
LD/APD	60 (tap water)	/	/	2.5 Gbit/s	OOK	AWG/OSC	2019 ^[5]
LD/APD	56 (tap water)	0.0876 m ⁻¹	4.9056	3.31 Gbit/s	QAM	AWG/OSC	2020 ^[13]
LD/MPPC	100 (pool water)	0.24 m ⁻¹	24	8.4 Mbit/s	OOK	AWG/OSC	2020 ^[6]
LD/PMT	100 (pool water)	0.0585 m ⁻¹	5.85	200 Mbit/s	OOK	AWG/OSC	2021 ^[7]
LD/PMT	150 (pool water)	0.053 m ⁻¹	8.775	500 Mbit/s	PAM	AWG/OSC	2021 ^[8]
LD/PMT	200 (pool water)	0.0325 m ⁻¹	6.5	500 Mbit/s	PAM	AWG/OSC	2021 ^[9]
LD/PMT	100.6 (tap water)	0.0658 m ⁻¹	6.6195	3 Gbit/s	OOK	AWG/OSC	2022 ^[10]
LD/PMT	5 (turbid water)	/	/	10 Mbit/s	PPM	FPGA	2016 ^[14]
LD/PIN	3 (artificial seawater)	0.481 m ⁻¹	1.443	50 Mbit/s	QAM	FPGA	2019 ^[15]
LED/PIN	1.2 (tap water)	/	/	2.34 Gbit/s	DMT	FPGA	2020 ^[16]
LED/APD	10 (tap water)	0.056 m ⁻¹	0.56	1 Mbit/s	FSK	FPGA	2020 ^[17]
LD/APD	3.6 (tap water)	/	/	2.2 Gbit/s	OFDM	FPGA	2020 ^[18]
LD/SPAD	2 (tap water)	/	/	6.21 Mbit/s	PPM	FPGA	2021 ^[11]
LD/PMT	50 (pool water)	0.1307 m ⁻¹	6.535	187.5 Mbit/s	PAM	FPGA	This work

^aDetector: PIN represents positive-intrinsic-negative; APD represents avalanche photodiode.

^bAttenuation length: attenuation coefficient \times underwater distance.

^cScheme: FSK represents frequency shift keying.

^dHardware: AWG represents arbitrary wave generator; OSC represents oscilloscope.

digital processing algorithms may cause relatively high latency and increase the cost of implementation, which may hinder the practical application of UWOC systems.

In contrast, UWOC systems based on hardware technologies such as field-programmable gate array (FPGA) have the advantages of compact size, low cost, and high practicability. Therefore, these hardware are considered as the most promising solution to commercialize UWOC systems. In 2016, a UWOC system based on FPGAs was proposed, and the real-time data rate was 10 Mbit/s at a transmission distance of 5 m^[14]. In 2019, a 50 Mbit/s UWOC system with a 16-QAM scheme based on FPGA was presented over an underwater distance of 3 m^[15]. In 2020, a real-time LED-based UWOC system with DMT modulation was demonstrated, and a gross bit rate of 2.34 Gbit/s over 1.2 m underwater transmission can be achieved^[16]. In the same year, a real-time, full-duplex, point-to-point UWOC system based on FPGA was designed, successfully achieving a 10 m underwater video transmission at a data rate of 1 Mbit/s^[17]. Moreover, a real-time 2.2 Gbit/s system was realized over a 3.6 m underwater and 8 m free-space channel^[18].

In 2021, a real-time UWOC system based on SPAD and pulse-position modulation (PPM) was conducted at a data rate of 6.21 Mbit/s when the received optical power was only -84.3 dBm^[11]. However, due to the high implementation cost of real-time communication systems and the absence of detectors with high bandwidth and sensitivity, few real-time UWOC systems support a high data rate of hundreds of megabits per second with a transmission distance of dozens of meters, hindering the practical implementation of UWOC systems. Therefore, it is imminent to investigate real-time practical UWOC systems that can achieve a transmission distance of dozens of meters with a superior data rate.

In the UWOC systems, the nonlinearity caused by certain devices like LD, PMT, and amplifier (AMP) substantially degrades the performance of the UWOC system^[9,19–21]. The deployment of nonlinear compensation algorithms of real-time systems would increase the cost of implementation and complexity significantly. Spatial summing is effective in improving the performance, which has been widely used in the visible light communication (VLC) system to mitigate the nonlinear effect.

In 2013, a VLC transmitter employing discrete power level stepping was proposed to address the nonlinearity, restricted dynamic range, and complex system designs of the conventional optical transmitters^[22]. In 2017, a highly compact array of micro-LED pixels was proposed to generate a discrete optical signal, which achieved a data rate up to 200 Mbit/s^[23]. In 2018, a UWOC system using an arrayed transmitter to generate a PAM-4 signal by superimposing two streams of OOK signals in the optical domain was experimentally investigated, and a 12.288 Mbit/s PAM-4 signal was successfully transmitted through a 2 m underwater channel^[24]. In 2019, a practical and cost-effective orthogonal frequency division multiplexing VLC system based on spatial summing modulation was proposed and experimentally demonstrated^[25]. To sum up, spatial summing with LED arrays can greatly improve the data rate by relieving the nonlinearity. However, the LED array has difficulty supporting a long-distance UWOC due to the large divergence angle.

In this paper, to realize a real-time long-distance UWOC system with a high data rate, a PAM-4 UWOC system based on FPGA is designed. Besides, we propose an optical PAM-4 scheme based on two pigtailed LDs with a fiber combiner to further increase the data rate. Optical PAM-4 is generated by superimposing OOK signals of different amplitudes in the fiber combiner. Two FPGAs are utilized to implement PAM-4 modulation and demodulation in real-time processing. A high-bandwidth PMT is used to support a high data rate with weak received optical power. In addition, offline processing is implemented to verify the accuracy of the demodulation of the real-time processing scheme. Finally, experimental results show that the proposed optical PAM-4 UWOC system achieved a data rate of 187.5 Mbit/s over an underwater transmission distance of 50 m in the real-time processing scheme, which is superior to the reported real-time UWOC systems. Compared with the conventional electrical PAM-4 scheme where two LDs transmit the same PAM-4 signal, whose data rate is 150 Mbit/s in the same condition, the data rate is increased by around 25%.

2. System Implementation

2.1. Principle of optical PAM-4 scheme

In the conventional PAM-4 UWOC system, the intervals between different levels of the received PAM-4 signals are not equally distributed due to the nonlinearity of the system, which deteriorates the performance. To alleviate the nonlinearity of the system, an optical PAM-4 UWOC system using two pigtailed LDs and a fiber combiner by optical superimposition is proposed. Assuming that the expression of the P - I curve of an ideal laser is

$$P = aI + b, \quad I \in [c, +\infty), \quad (1)$$

where P , I , and c are the optical power, drive current, and turn-on current, respectively, then a and b are the coefficients of the

linear function. The optimal load current range of the ideal signal is

$$[I_{\min}, I_{\max}], \quad I_{\min} \geq c, \quad (2)$$

where I_{\min} and I_{\max} are the minimum and maximum values of the drive current in the linear range of the LD, respectively. The corresponding maximum and the minimum values of the optical power in the linear range of the LD are

$$[P_{\min}, P_{\max}]. \quad (3)$$

For the electrical PAM-4 scheme where two LDs transmit the same PAM-4 signal, the peak-to-peak values of the current and emitting optical power after combining two PAM-4 signals are

$$d_{I, \text{electrical PAM-4}} = 2(I_{\max} - I_{\min}), \quad (4)$$

$$d_{P, \text{electrical PAM-4}} = 2(P_{\max} - P_{\min})\eta, \quad (5)$$

where η is the combining efficiency of the fiber combiner. Then, we can deduce that

$$d_a + d_b + d_c = d_{P, \text{electrical PAM-4}}, \quad (6)$$

where d_a , d_b , and d_c are the optical power difference between adjacent amplitudes of the electrical PAM-4 signal, respectively. In nonlinear systems, there would be

$$\min(d_a, d_b, d_c) < \frac{1}{3} d_{P, \text{electrical PAM-4}}, \quad (7)$$

$$\max(d_a, d_b, d_c) > \frac{1}{3} d_{P, \text{electrical PAM-4}}. \quad (8)$$

It often requires a higher signal-to-noise ratio than linear systems to achieve the same performance.

In this work, the optical PAM-4 scheme that two LDs transmit the different OOK signals is used to alleviate the nonlinearity. The drive currents of the two LDs that generate the OOK signals A and B are set to

$$\left[I_{\min}, \frac{I_{\max} - I_{\min}}{2} \right], \quad [I_{\min}, I_{\max}], \quad (9)$$

and the ratio of the drive current amplitude of signal A to signal B is set to 1:2. So, the ratio of the optical power amplitude of signal A to signal B is also 1:2, and the corresponding optical power is theoretically

$$\left[P_{\min}, \frac{P_{\max} - P_{\min}}{2} \right], \quad [P_{\min}, P_{\max}]. \quad (10)$$

Then, the optical PAM-4 signal is generated through the fiber combiner. The peak-to-peak values of the current and emitting optical power of the optical PAM-4 signal are

$$d_{I, \text{optical PAM-4}} = \frac{3}{2}(I_{\max} - I_{\min}), \quad (11)$$

$$d_{P, \text{optical PAM-4}} = \frac{3}{2}(P_{\max} - P_{\min})\eta. \quad (12)$$

By adjusting the bias value and amplitude of the driving current, the optical PAM-4 signal with three equal intervals can be achieved. Compared with the electrical PAM-4 signal, although the optical PAM-4 signal reduces the peak-to-peak value of the emitting optical power by 25%, it can effectively alleviate the nonlinearity of the system.

2.2. PAM-4 modulation and demodulation based on the FPGA

The process of the optical PAM-4 modulation and demodulation based on the FPGA is shown in Fig. 1. In the modulation unit, the read-only memory (ROM) stores the pseudo-random binary sequence (PRBS), the symbol sync header, and the frame sync header sequence. The bit stream is sent out periodically from the ROM with 2-bit width and mapped into the Gray code. The most significant bit (MSB) and least significant bit (LSB) of the 2-bit stream are separated into two links. Then, every binary symbol of each link is mapped to a sampling value and transmitted to the digital-to-analog converter (DAC), of which the resolution is 16 bits. A delay control module is designed to adjust the difference between the two links with an accuracy of 0.83 ns. Then, a JESD204b protocol is used to complete the transmission between the FPGA and the DAC. Finally, two converters of the DAC output the MSB and LSB of the 2-bit stream in the form of an OOK signal, respectively.

In the demodulation unit, the sampling rate of the analog-to-digital converter (ADC) is eight times the symbol rate, and the resolution is 14 bits. Every sampling value from the ADC is transmitted into the FPGA by another JESD204b protocol and firstly enters the threshold calculation module. In the threshold and analog-to-digital (AD) demapping module, three thresholds of the PAM-4 signal are calculated by the method of accumulation and average. Every sampling value is demapped as a PAM-4 symbol using three thresholds and then decoded. In the next step, the symbol sync header sequence is used to perform symbol synchronization for determining the best sampling value among the eight sampling values of a symbol and down-sampling. The

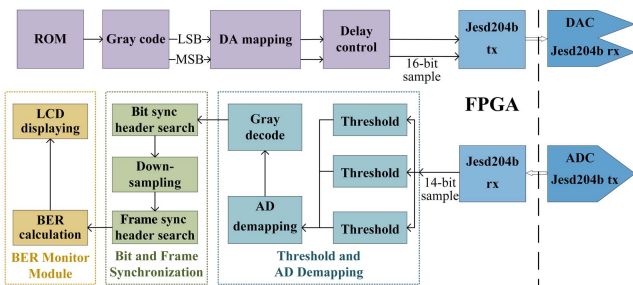


Fig. 1. Process of PAM-4 modulation and demodulation based on FPGA.

frame sync header sequence is employed to determine the start of the frame. In the bit error ratio (BER) monitor module, the demodulated signal is compared with the transmitted signal stored in the ROM, and the numbers of the error bits and received bits are counted. The latest BER is calculated according to the number of the error bits after every 1×10^8 bits are received. Then, the BER is displayed on the liquid crystal display, which can be observed during the experiment. The process of the electrical PAM-4 modulation and demodulation is similar to the above, where only the digital-to-analog (DA) mapping is different. The data transmission between the FPGA and data converters is realized by using the JESD204b protocol.

3. Experimental Setup

The experimental setup of the proposed optical PAM-4 UWOC system based on a fiber combiner is shown in Fig. 2. At the transmitter, two OOK or PAM-4 signals generated by an FPGA (KC705) were converted into an analog signal by a broadband DAC and amplified by two 37 dB AMPs (100 kHz to 75 MHz). Then, two identical variable electrical attenuators (VEA, KT3.0-30/1S-2S) were used to adjust the amplitude of the electrical signal. Two direct-current (DC) powers and signals were connected to two bias tees (Bias-T, ZHBT-4R2GW+, 10 to 4200 MHz) to drive two pigtailed blue-light LDs (NDB7875). By setting different attenuation coefficients and bias currents, two optical signals reached the optical power satisfying Eq. (10). The optical signals from LDs were injected into two input terminals of the fiber combiner (Xiri MPC 3×1), with the remaining input terminal suspended, and the combining efficiency of the fiber combiner was measured to be 73%. The output end of the fiber combiner was connected to a collimator using an armored fiber, which was assembled in a sealed cabin, as shown in Fig. 2(b). The collimator was fixed in the center of the watertight cabin by a 3D printed component.

At the receiver, a PMT (Hamamatsu H14447) was placed in another watertight cabin and converted the optical signal to the electrical signal. The electrical signal was connected to the outside through a watertight connector and was amplified by an

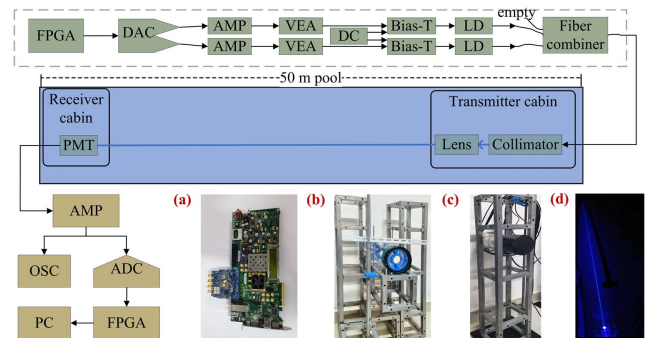


Fig. 2. Experimental setup diagram of the proposed UWOC system based on the FPGA and a fiber combiner. Inserts: (a) the FPGA, (b) the transmitter cabin, (c) the receiver cabin, and (d) the transmitter cabin in a 50 m swimming pool.

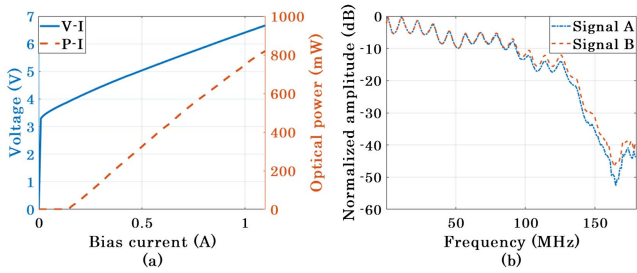


Fig. 3. (a) *P-I* and *V-I* curves of the LD; (b) normalized frequency responses of the system.

AMP (ZHL-6A-S+, 2.5 kHz to 500 MHz). The FPGA and a broadband ADC performed the real-time demodulation after receiving the electrical signal. To verify the accuracy of the demodulation of the real-time processing, the data captured by the ADC was also recorded on a computer by the logic analyzer of the FPGA developing platform for offline processing. Finally, the data captured by an oscilloscope (OSC, Agilent Technologies DSO-X 4104A, 5 GSa/s) were used for offline analysis and plotting eye diagrams. The *P-I* and *V-I* curves of the LD are shown in Fig. 3(a). The normalized frequency response of the system was measured by a network analyzer (Hewlett Packard 8753D), as depicted in Fig. 3(b).

4. Experimental Results and Discussion

In this work, the data rate is limited by the system bandwidth, the sampling rate of the ADC, the frequency division coefficient, and the reference frequency of the clock chip. Therefore, the proposed system was experimented with three data rates of 125, 150, 187.5 Mbit/s, respectively.

First, we studied the nonlinearity of the proposed system. A 25 MHz triangular wave generated by an arbitrary waveform generator (AWG) was transmitted in links A and B, respectively, and the corresponding signals A and B received by the PMT were sampled by an OSC. Figure 4 shows the transfer curve of the UWOC system.

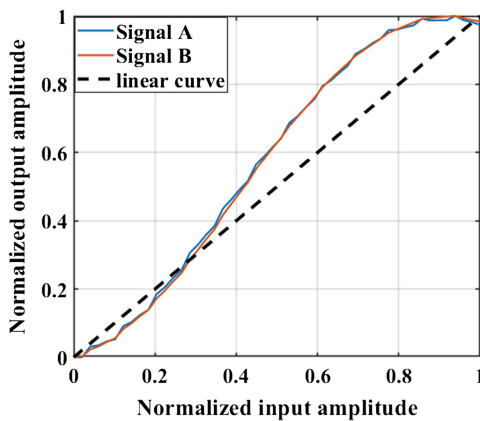


Fig. 4. Transfer curve of the UWOC system.

of the UWOC system, which indicates that the system has severe nonlinear effects.

The relationship between attenuation values of VEA and real-time BER under different bias currents with a 150 Mbit/s data rate is illustrated in Fig. 5. The devices used in the two links are the same, so the parameters of the two links are the same in the electrical PAM-4 scheme. From Fig. 5(a), the optimal bias current and attenuation value with minimum real-time BER are 0.34 A and 14 dB, respectively. In the optical PAM-4 scheme, the bias current and attenuation values of link A are set to be 0.34 A and 14 dB, respectively, and then we changed the bias current and attenuation value of link B. Figure 5(b) shows that the optimal attenuation value also decreases as the bias current decreases. To maintain the integrity of the signal and increase the emitting optical power, the optimal bias current and attenuation values for signal B are set to 0.26 A and 17 dB, respectively.

After determining the optimal parameters of the system, the performance of the proposed system was verified in a 50 m standard swimming pool. The estimated attenuation coefficient for water is 0.5671 dB/m (0.1307 m^{-1}) after removing the measured geometric loss. The BERs of the real-time processing and offline processing at different data rates are shown in Fig. 6. Compared with the electrical PAM-4 scheme, the real-time BERs of the optical PAM-4 scheme at different data rate are lower. The BER of offline processing is close to that of real-time processing, which verifies the accuracy of real-time processing. The crystal oscillators of the FPGAs used in the transmitter and the receiver are not strictly identical, so there exists a frequency offset between them. Through offline analysis, we found that the frequency offset at a data rate of 125 Mbit/s is higher than that of 150 Mbit/s due to different settings of the clocks. With the same frame length, the demodulation of the 125 Mbit/s data rate is more prone to error bits, which reduces the BER performance. The real-time BER of the optical PAM-4 UWOC system is at a data rate of 187.5 Mbit/s in continuous 40 s, which is below the hard-decision forward error correction threshold of 3.8×10^{-3} . In offline processing, the ADC can only record the data within tens of milliseconds, which leads to accidental errors due to dynamic changes in the underwater channel generated by the circulatory system.

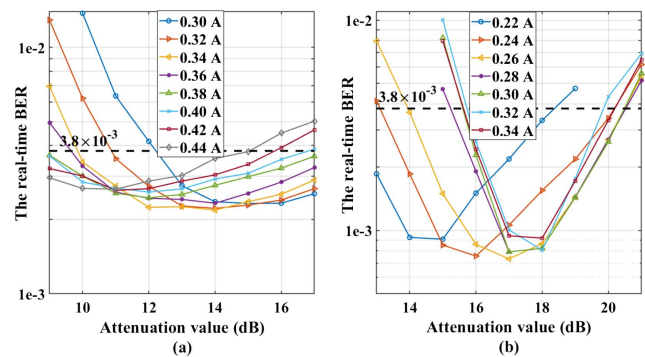


Fig. 5. Relationships between attenuation of VEA and real-time BER under different bias currents in (a) the electrical PAM-4 scheme and (b) the optical PAM-4 scheme.

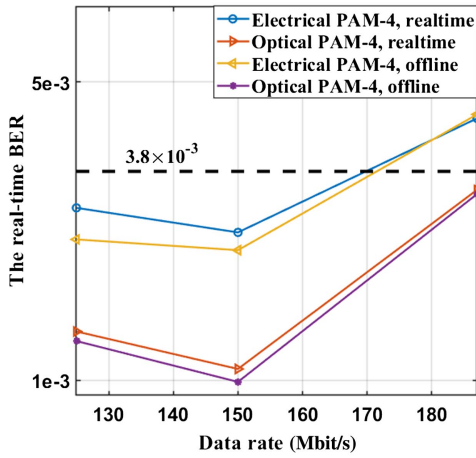


Fig. 6. BERs of the real-time processing and offline processing at different data rates.

Figure 7 illustrates the error vector magnitude (EVM) at different data rates in the optical and electrical PAM-4 UWOC system. The EVM of the electrical PAM-4 scheme at a data rate of 150 Mbit/s is 22.4048%, and the EVM of the optical PAM-4 scheme at a data rate of 187.5 Mbit/s is 22.5587%. The data rate of the optical PAM-4 scheme is increased by 25% at a close EVM. By comparing the eye diagram of the electrical and optical PAM-4 schemes at the data rate of 187.5 Mbit/s, as shown in insets of Fig. 7, it can be affirmed that the eye diagram of the optical PAM-4 scheme is more evenly distributed.

Figure 8 illustrates the ratio of the amplitude difference between adjacent levels of the PAM-4 signals at different data rates in the electrical and optical PAM-4 schemes. According to Eq. (6), we define a , b , and c as three amplitude differences between adjacent amplitudes of the received PAM-4 signal, and d_{PAM-4} is the total amplitude difference of the received PAM-4 signal. Then, we can further get

$$\alpha = \frac{a}{d_{PAM-4}} \cdot 100\%, \quad \beta = \frac{b}{d_{PAM-4}} \cdot 100\%, \quad \gamma = \frac{c}{d_{PAM-4}} \cdot 100\%, \quad (13)$$

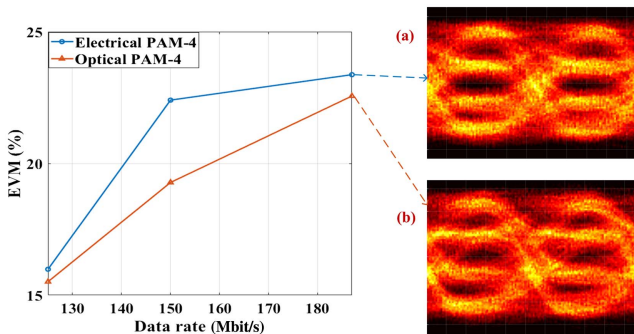


Fig. 7. EVM at different data rates in the optical and electrical PAM-4 UWOC system. Inserts: (a) electrical PAM-4 scheme at 187.5 Mbit/s, (b) optical PAM-4 scheme at 187.5 Mbit/s.

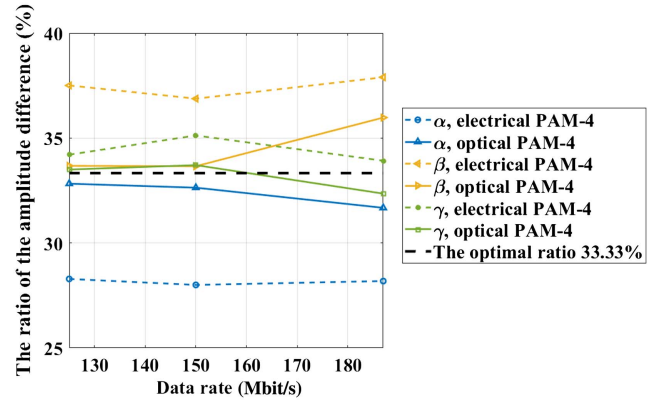


Fig. 8. Ratio of the amplitude difference at different data rates in the optical and electrical PAM-4 UWOC system.

where α , β , and γ and are the ratios of a , b , and c in d_{PAM-4} , respectively. So, it is easy to deduce that the optimal ratio is 33.33%. It can be found from Fig. 8 that the ratios of the amplitude difference at data rates of 125 and 150 Mbit/s are almost 33.33%. At 187.5 Mbit/s data rate, the data rate of the optical PAM-4 scheme is 35.97%, but that of the electrical PAM-4 scheme is 37.90%, which reveals that the optical PAM-4 scheme can efficiently alleviate the nonlinearity of the system.

5. Conclusion

In this paper, we demonstrated a real-time optical superimposition PAM-4 UWOC system based on the FPGA and fiber combiner. Compared with the conventional electrical PAM-4 UWOC system whose LDs transmit the same PAM-4 signal, the optical PAM-4 scheme using two pigtailed LDs and a fiber combiner effectively reduces the nonlinearity of the system despite some optical power loss. At the receiver, the system used a high-bandwidth PMT as the detector, which ensures a high data rate with a large attenuation coefficient. In the swimming pool experiment, the optical PAM-4 UWOC system successfully reached a transmission distance of 50 m at a data rate of 187.5 Mbit/s in the real-time processing scheme, while that of the electrical PAM-4 scheme was only 150 Mbit/s in the same condition. This work explores the potential of the optical superimposition using the fiber combiner for UWOC and provides a new solution for alleviating the nonlinearity of UWOC systems with a high data rate.

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