Uniformly high-speed semi-open loop polarization control and PMD suppression

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We propose and demonstrate a novel scheme of semi-open-loop polarization control (SOL-PC), which controls the state of polarization (SOP) with high accuracy and uniform high speed. For any desired SOP, we first adjust the initial SOP using open-loop control (OLC) based on the matrix model of a three-unit piezoelectric polarization controller, and quickly move it close to the objective one. Then closed-loop control (CLC) is performed to reduce the error and reach precisely the desired SOP. The response time is three orders faster than that of the present closed-loop polarization control, while the average deviation is on par with it. Finally, the SOL-PC system is successfully applied to realize the suppression of the polarization mode dispersion (PMD) effect and reduce the first-order PMD to near zero. Due to its perfect performance, the SOL-PC energizes the present polarization control to pursue an ideal product that can meet the future requirements in ultrafast optical transmission and quantum communication.

Keywords: polarization control; polarization mode dispersion; fiber optics components; coherent communications.

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Polarization control plays a crucial role in the fields of optical fiber-based high-speed optical transmission and quantum communication $\frac{1-7}{2}$. To guarantee the reliability of optical signals or quantum keys, the state of polarization (SOP) must be controlled effectively. Up to now, various polarization control schemes have been proposed, such as fiber bending⁸ or squeezing⁹, Faraday rotation^{10,11}, and photonic bandgap fiber^[12]. Among them, piezoelectric polarization controllers (PPCs)^[9] based on fiber squeezing are more practical and widely used because of their low power penalty, and are particularly suitable for optical fiber backbone systems. In a working PPC, squeezing will induce birefringence in the fiber, which causes a retardation between the two orthogonal modes perpendicular and parallel to the direction of pressure. Thus, the SOP varies with the squeezing pressure or the driving voltage (DV), which is the linearly amplified control voltage for the PPC. According to our previous work^[9], the output SOP is a function of the DVs; thus, it can be controlled by applying the required voltage on each PPC unit.

To quickly produce accurate and stable polarization states, we present in this Letter a novel scheme of semiopen-loop polarization control (SOL-PC). We first establish a three-unit PPC system and obtain its matrix model for open-loop control (OLC) by which we can calculate the DVs for any desired output SOPs. Applying them to the PPC, we immediately get the SOP close to the objective one. If the difference between them is beyond the tolerant value, closed-loop control (CLC) is performed to reduce the error and achieve the precision objective SOP.

Our scheme combines both advantages of fast speed of OLC and precision of CLC, so we can generate an arbitrary SOP with a uniform fast speed, which has never been achieved by CLC alone. As we all know, it usually takes a much longer time to attain SOPs far away from the current one. In addition, to further reveal its great potential for applications in the suppression of polarization mode dispersion (PMD), we demonstrate the SOL-PCbased PMD suppression system.

The core component of the PPC hardware is schematically shown in Fig. <u>1</u>, where three PPC units oriented 45° from each other and controlled by the DVs, and the DVs working range is 0–150 V. Each PPC unit is made of a jaw and a piezoelectric actuator (PZT), where the optical fiber is placed between them. When applying voltage to the PZT, it stretches to squeeze the optical fiber. Due to the elastic-optic effect, the squeezing force of the PZT induces the stress birefringence in the optical fiber, which

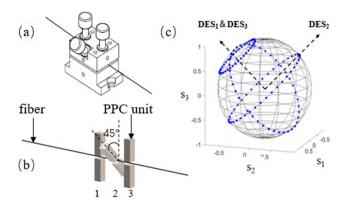


Fig. 1. Core component of the PPC hardware: (a) configuration, (b) principle, and (c) the SOP evolution on the Poincaré sphere when each PPC unit works.

leads to the retardation and then changes the SOP. Since the PPC unit has a very low insertion loss and polarization dependent loss, its Mueller matrix can be expressed by a rotation matrix^[9]

$$\mathbf{M}(u) = \begin{pmatrix} 1-p+a_1^2p & a_1a_2p+a_3q & a_1a_3p-a_2q \\ a_1a_2p-a_3q & 1-p+a_2^2p & a_2a_3p+a_1q \\ a_1a_3p+a_2q & a_2a_3p-a_1q & 1-p+a_3^2p \end{pmatrix},$$
(1)

where $p = 1 - \cos[\alpha(u)]$, $q = \sin[\alpha(u)]$, and the dynamic eigenstate of polarization (DES) of the PPC unit is $(a_1, a_2, a_3)^{T_{[13]}}$.

The input and output Stokes vectors are related by $\mathbf{S}_{out} = \mathbf{M}(u_i)\mathbf{S}_{in}$, so the output SOP rotates around the DES when the DV changes. Figure <u>1(c)</u> presents the evolution trajectories of three PPC units that were built for SOL-PC investigation.

In order to accurately obtain the DESs, we fit the SOP evolutionary traces by multiple linear regressions and calculate its normal vector, which is the DES exactly^[9]. After such processing, we have

(1) DES₁ and DES₃: $(0.1960, -0.6290, 0.753)^T$;

(2) DES₂: $(0.1800, 0.7770, 0.6040)^T$.

3

2

0

0

Experimental data

0.5

SOP rotation angle(rad)

Then we further measure the rotation angle of the SOP at different DVs, and get the results shown in Fig. 2 by linear regression:

$$\alpha(u_1) = 1.544u_1 - 0.03412 \ (R^2 = 0.9995).$$

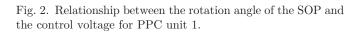
Inserting the above results into Eq. (<u>1</u>), we get $\mathbf{M}(u_1)$ of the first PPC unit. Using the same method, we obtain $\mathbf{M}(u_2)$ and $\mathbf{M}(u_3)$ of other two, and then the output SOP is determined by

$$\mathbf{S}_{\text{out}} = \prod_{n=3}^{1} \mathbf{M}(u_n) \mathbf{S}_{\text{in}}.$$
 (3)

 $\alpha(u_1)=1.544u_1-0.03412$

1.5

Generally, the input SOP \mathbf{S}_{in} is fixed, so \mathbf{S}_{out} is only a function of the DVs $(u_1, u_2, \text{ and } u_3)$. Measuring three groups of \mathbf{S}_{out} and DVs, we can work out \mathbf{S}_{in} according



Voltage(V)

1

to Eq. (3). For any desired \mathbf{S}_{out} , Eq. (3) is used to compute the required DVs. We apply them to the PPC units, and then get the approximate desired \mathbf{S}_{out} . In other words, this system does not need to measure the output SOP repeatedly and to adjust it through a complex feedback process as CLC does. Once the matrix is obtained in the initialization step, for any desired SOP, one just needs to calculate the required driving voltages by Eq. (3) and simply apply them to the PPC to get the desired SOP.

However, the output SOP deviation from the desired \mathbf{S}_{out} moves far away quickly when the number of PPC units increases, while the accuracy deteriorates due to the ferroelectric hysteresis of piezoelectric ceramics and environmental disturbances. To guarantee the accuracy of OLC, it is necessary to reduce the deviations. Thus we further apply the variable-step CLC to accurately achieve the desired SOP. The CLC process uses a polarization analyzer (PA, POD-201, General Photonics) for real-time monitoring of the output SOP from the PPC. When it deviates the required SOP, the system changes the driving voltage of the PPC step by step to decrease the deviation until the error is small enough or very close to the required SOP. Combining OLC with the CLC process, is called SOL-PC.

Figure <u>3</u> presents the flow chart of the SOL-PC scheme. The step means the output SOP goes forward one pace on the Poincaré sphere, which is a function of the DV increments and can be calculated by Eq. (<u>3</u>). Measuring the deviation between the current and the desired SOPs, if the initial step is larger than the deviation, reduce it to the half, then compare again, otherwise output the

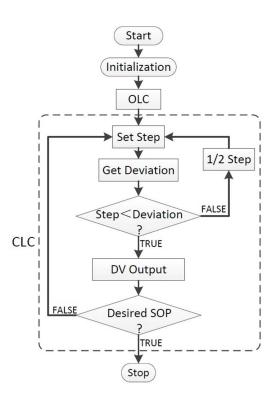


Fig. 3. Flow chart of the SOL-PC scheme.

stepped DV. After several times of variable-step closedloop feedback adjustment, the SOP meets the required accuracy and then the program stops; the entire SOL-PC process takes 3.2 ms.

To demonstrate the above SOL-PC scheme, we create the experimental system shown in Fig. <u>4</u>. The 1550 nm laser passes through a mechanical polarization controller and goes into the PPC subsystem. The DV applied on the PPC is obtained by linearly amplifying the output from the digital to analog conversion (D/A) module controlled by a personal computer. The output SOP is monitored in real time by a polarization analyzer, measuring its Stokes vector and displaying it on the Poincaré sphere.

We first observe the evolution of the SOP when the SOL-PC scheme is performed. Figure 5 illustrates the whole evolution trajectory. It is clear that there are two sections that represent the OLC (blue line) and CLC (red circles) progress, respectively. Once the DVs are calculated by Eq. (3), they will be applied on the PPC, and the SOP jumps quickly along the blue trace to approximate the objective/desired SOP, which is completed within a few tens of microseconds. Then the deviation is reduced according to the flow chart in Fig. 3, and thus the SOP goes forward step by step, as shown by the red circles in Fig. 5, and finally reaches the desired SOP.

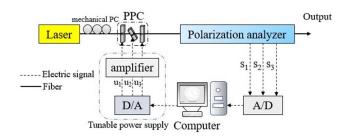


Fig. 4. Experimental setup for investigation of the SOL-PC scheme. D/A: digital to analog conversion, A/D: analog to digital conversion

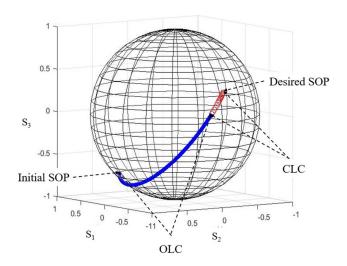


Fig. 5. Evolution of the SOP on the Poincaré sphere when performing the SOL-PC.

Second, we choose randomly some SOPs as the desired SOPs (300 samples) and compare them with the experimental results. By statistical analysis, we find that the average deviation is 0.0283 rad on the Poincaré sphere. Table $\underline{1}$ shows some of the results.

Moreover, we investigate the differences of the speed and error parameters among CLC, OLC, and SOL-PC, and the results show that CLC has the lowest response speed with the least error, while OLC is just contrary and has the highest speed with the largest error. However, our SOL-PC has a fast speed close to OLC, and less error on the bar of CLC, while they are the most uniform among these schemes. Some typical results are given in Table <u>2</u>.

So far we have demonstrated the remarkable advantages of the SOL-PC scheme. It can greatly increase the response speed up to 3 orders of magnitude compared with the widely-used CLC, while it is uniform for any desired SOP that cannot be realized by the CLC.

The PMD effect has become one of the key obstacles that limit the speed of fiber transmission systems^[14,15]. Considering the feasibility and high performance of the SOL-PC, we further reveal its great potential in the suppression of the first-order PMD effect. The experimental setup is schematically shown in Fig. <u>6</u>.

In our experiment, the differential group delay (DGD) unit is a 3 m polarization-maintained fiber (PMF). The wavelength range of the tunable laser (TSL210, Santec) is from 1530 to 1610 nm. When changing the laser wavelength continuously, the output SOP goes along nearly a round circle on the Poincaré sphere, as shown in Fig. 7(a). That is the well-known PMD. To eliminate it, we make the PPC work with the SOL-PC scheme. Setting the principal states of polarization (PSP) as the desired SOP,

Table 1. Error Analysis Results of the SOL-PC Scheme

Desired SOP	Output SOP	Error (rad)
1.000,0.000,0.000	0.994, -0.104, 0.017	0.0517
0.000,0.000,1.000	-0.040, -0.050, 0.991	0.0293
0.312, 0.156, 0.937	0.315, 0.132, 0.948	0.0253
-0.662, 0.181, 0.723	-0.669, 0.182, 0.716	0.0105
0.082, -0.879,	0.044, -0.888,	0.0146
	$\begin{array}{c} 1.000, 0.000, 0.000\\ 0.000, 0.000, 1.000\\ 0.312, 0.156, 0.937\\ -0.662, 0.181, 0.723\end{array}$	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$

Table 2. Comparison of the SOL-PC, CLC, and OLC

No.	Method	Response time (s)	Error (rad)
1	CLC	5.320,00	0.0225
2	OLC	$0.000,\!35$	0.0842
3	SOL-PC	0.003,20	0.0283

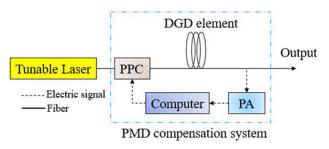


Fig. 6. Experimental PMD suppression system. PA: polarization analyzer, DGD: differential group delay.

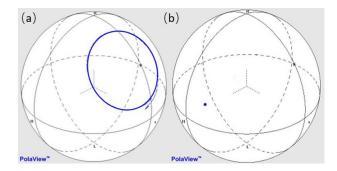


Fig. 7. Evolution of the SOP on the Poincaré sphere for 3 m PMF as the wavelength varies (a) before and (b) after PMD suppression.

the dispersed blue circle quickly focuses into a small point [Fig. 7(b)], which means the SOP no longer changes with the wavelength, namely, the PMD effect is well suppressed.

To further confirm the validity of the above result, we use the fixed-analyzer technique to measure the PMD before and after suppressing^[16]. The first-order PMD or DGD is calculated by

$$\Delta \tau = \frac{K E \lambda_1 \lambda_2}{2(\lambda_2 - \lambda_1)c},\tag{4}$$

where K is the modulus coupling coefficient, E is the number of extreme values in the wavelength window $(\lambda_1 - \lambda_2)$, and c is the speed of light.

We employ a wide-band light source (PMD440, LED light source, PerkinElmer Inc.) to replace the tunable laser in Fig. <u>6</u>, and measure the optical spectrum of the output by a spectrum analyzer (AQ6317B, Ando) with a couple of polarizers, and obtain the result as presented in Fig. <u>8(a)</u>. According to Eq. (<u>4</u>), we determine that the DGD of the 3 m PMF is 2.44 ps. After suppressing, we get the optical spectrum shown in Fig. <u>8(b)</u>, where one can find that almost all of the extrema disappear, which means the DGD is close to zero, that is to say, the first-order PMD has been suppressed extremely with uniform high speed^{[<u>17]</sub>.}</u>

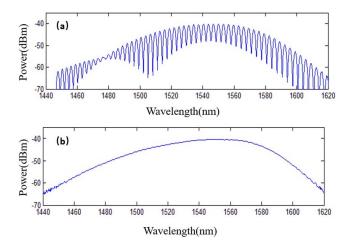


Fig. 8. Measured optical spectrum by the fixed-analyzer technique (a) before and (b) after PMD suppression.

In summary, we have proposed a new scheme of the SOL-PC for accurate polarization control with uniform high speed. Experimental results show the response time is three orders of magnitude faster compared to the present CLP control while the average deviation is on the same level. Applying the SOL-PC system, we successfully perform the suppression of the PMD effect, and reduce the first-order PMD to approximately zero. Due to its unique advantages, we are convinced that the SOL-PC will stimulate the progress of present polarization control to give birth to a perfect product that will satisfy the future advanced requirements. Compared with commonly used techniques, its advantages are very prominent, but it has higher requirements for the hardware to solve the matrix shown in Eq. (3). In case the data processing speed does not meet the requirements, it may limit the speed of polarization control. With higher-speed hardware and data processing, D/A and A/D conversion modules can be increased much more, so its performance will be further improved.

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