

光纤耦合周期极化铌酸锂薄膜波导器件的研究

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摘要 对硅基周期极化铌酸锂(PPLN)薄膜脊形波导进行了理论分析,并使用有限元软件模拟了 25℃下泵浦波长 为 1560 nm 的 PPLN 脊形波导的准相位匹配(QPM)周期与波导脊高和脊宽的关系。仿真结果表明,在相同脊宽 (10 µm)或脊高(10 µm)下,PPLN脊形波导的 QPM 周期随着脊高或脊宽的增加而增大,最后趋于常数(即块状 PPLN的 QPM 周期)。进一步模拟了在脊高和脊宽维持不变的情况下,PPLN脊形波导的 QPM 周期运渐减小,并且温度每升高1℃,QPM 周期减小约 3 nm。根据仿真结果制作了硅基片上集成 PPLN 脊形波导路件,将其封装成小体积的光纤入光纤出的波导,并测试 了性能。当温度为 24.8℃、1560 nm 基频光输入功率为 1.2 W时,最大输出 653 mW 的倍频光,光光转换效率达 54.4%,归一化转换效率为 20.2 %·W⁻¹·cm⁻²。

关键词 非线性光学;周期极化铌酸锂;薄膜;准相位匹配;脊形波导中图分类号 O437 文献标志码 A

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1 引 言

周期极化铌酸锂(PPLN)是一种用于激光波长转换的优秀非线性晶体^[1]。与通常需要高峰值脉冲功率输入的传统非线性晶体相比,PPLN由于其周期性非临界相位匹配的特性,具有很高的转换效率,适用于连续波(CW)激光波长转换,被广泛应用在连续激光器系统中^[24]。另外,还可以通过灵活设计PPLN的准相位匹配(QPM)周期,实现全彩色激光^[5-7]输出,PPLN 具有很强的实用价值。近年来,人们为了提高CW激光器的非线性频率转换效率,注意力逐渐从PPLN体块^[8-9]转移到PPLN薄膜光波导^[10]。

1998年,Levy等^[11]通过离子注入法制备了9µm 厚的铌酸锂(LN)薄膜。2003年,Nishida等^[12]利用精 密切割技术制备了直接键合PPLN脊形波导。2004 年,Rabiei等^[13]利用离子注入和键合技术制备了一层 680 nm厚的LN薄膜。2010年,Hu等^[14]使用离子切片 和层转移方法制备了厚度为0.76µm的3 inch(1 inch= 2.54 cm)晶圆级LN单晶薄膜,并受绝缘体上硅(SOI) 晶片名称的启发,将其命名为绝缘体铌酸锂(LNOI)。 目前,多家供应商可提供尺寸高达6 inch的光学质量 LNOI晶片,加速了薄膜化铌酸锂晶体相关器件的开 发。国内针对PPLN薄膜光波导器件展开了深入详实 的研究^[15-17]。基于 PPLN 波导的非线性频率转换器件 在众多领域中得到了广泛的应用,如光学通信^[18-19]、量 子光学^[20]、微波光学^[21]和光谱学^[22]等。

本文简单介绍了硅基PPLN薄膜脊形波导的基本 结构和原理,设计并制作了紧凑型光纤进光纤出 PPLN波导封装模块。

2 理论分析

对硅基 PPLN 薄膜脊形波导(图1)的倍频进行了 分析,主要是计算一定结构的波导在基频光λ₁处的二 次谐波 QPM 极化周期 Λ_{SHG},以下简称为 QPM 周期。 对于特定的 PPLN 脊形波导结构,其基模的有效折射



图 1 PPLN 脊形波导结构示意图 Fig. 1 Structural diagram of PPLN ridge waveguide

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率 n_{eff} 是确定的。利用准相位匹配原理^[23],基频光 ω_1 经过 PPLN 脊形波导后获得频率为 $\omega_{SHG} = 2\omega_1$ 的倍频 光。由于晶体材料的色散效应[23],它们的波矢量失 配为

$$\Delta k_{\rm SHG} = k_{\rm SHG} - 2k_1 - \frac{2\pi}{\Lambda_{\rm SHG}},\tag{1}$$

式中: k_{SHG} 为倍频光的波矢量大小; $k_1 = 2\pi n_1/\lambda_1$ 为对 应波长 λ_1 的波矢量大小,其中 n_1 为对应波长 λ_1 的折 射率。

当相位失配量 $\Delta k_{\rm SHG} = 0$ 时,即可满足准相位匹 配,此时QPM周期满足

$$\frac{1}{\Lambda_{\rm SHG}} = \frac{n_{\rm SHG}}{\lambda_{\rm SHG}} - \frac{2n_1}{\lambda_1},\tag{2}$$

化简得

$$\Lambda_{\rm SHG} = \frac{\lambda_1}{2(n_{\rm SHG} - n_1)},\tag{3}$$

式中: $\lambda_1 = 2\lambda_{SHG}$,其中 λ_{SHG} 为倍频光的波长。

(a)

21

18

15

12

9

6

0

5

QPM period /µm

由于铌酸锂晶体是各向异性的双折射材料,因此

在设计PPLN的QPM周期时,要考虑其折射率随波长 及温度的变化。铌酸锂晶体的色散特性由 Sellmeier 方程^[24]给出

$$n^{2} = a_{1} + b_{1}f + \frac{a_{2} + b_{2}f}{\lambda^{2} - (a_{3} + b_{3}f)^{2}} + \frac{a_{4} + b_{4}f}{\lambda^{2} - a_{5}^{2}} - a_{6}\lambda^{2}, (4)$$

$$f = (T - T_{0})(T + T_{0} + 2 \times 273.16) = (T - 24.5)(T + 570.82), \quad (5)$$

式中:n为铌酸锂晶体的折射率; $a_1 \sim a_6$ 和 $b_1 \sim b_4$ 为 Sellmeier系数;f为与温度有关的参数; λ 为铌酸锂晶体 中的光波长; T₀为室温,这里为24.5℃; T为晶体的 摄氏温度。对于掺杂摩尔分数为5%的掺镁铌酸锂 晶体,式(5)中的经验系数可在文献[24]中查得。

该Sellmeier方程仅适用于铌酸锂块状结构,如果 要计算波导结构,需要通过有限元软件建模,计算特定 波导结构下对应波长的有效折射率。本文使用有限元 软件模拟了室温下基频光波长为1560 nm的PPLN的 QPM 周期与波导尺寸之间的关系,如图2所示。从



图 2 室温下基频光波长为1560 nm 的 PPLN 的 QPM 周期与波导尺寸之间的关系。(a) QPM 周期与脊高之间的关系;(b) QPM 周期 与脊宽之间的关系

Fig. 2 Relationship between QPM period of PPLN and waveguide size under room temperature when fundamental frequency wavelength is 1560 nm. (a) QPM period versus ridge height; (b) QPM period versus ridge width

图 2(a)可以看出,在同一泵浦波长、同一波导宽度 下, PPLN 波导的 QPM 周期随着波导脊高的增加而 增大,最后趋于常数,即块状的极化周期。图2(b)展 示了与图 2(a)相似的特性,只是 PPLN 波导的 QPM 周期有微小的差异。为了充分利用PPLN的高非线 性系数 d₃₃,采用 0 类匹配,即泵浦和二次谐波产生 (SHG)信号均采用横磁(TM)偏振。此外,尽管波导 可以是多模的,这里也仅考虑基本导模之间的相位 匹配。

图 3 给出了基频光波长为 1560 nm 及脊高和脊 宽都为10 μm的PPLN的极化周期与温度之间的关 系。可以看出,无论是 PPLN 块状晶体还是脊形波 导,在同一基频波长下,QPM周期均随着温度的增 加而逐渐降低;温度每升高1℃,QPM周期减小3nm 左右。



图 3 基频光波长为1560 nm 及脊高和脊宽都为10 µm 的 PPLN的 QPM 周期与温度之间的关系

Fig. 3 QPM period of PPLN versus temperature when fundamental frequency wavelength is 1560 nm and both ridge height and width are 10 µm

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3 波导制作与封装

硅基 PPLN 薄膜脊形波导的制作过程(图4)如下:首先,对一块 Z切厚度为0.5 mm的掺杂5%(摩尔分数)MgO的铌酸锂晶片进行高压极化处理。根据上面的分析,极化周期选择为18.7 μm,以获得泵 浦波长在1560 nm附近的相位匹配。极化处理完成后,在PPLN晶片的一侧沉积一层约600 nm厚的二 氧化硅薄膜缓冲层,然后溅射一层厚度为300 nm 左 右的金。取另外一块0.5 mm 厚精密抛光的硅晶片, 镀上一层厚约300 nm 的金,并将其与 PPLN 晶片进 行键合。该过程在室温下实现,避免了两个晶圆热 膨胀系数的不同而产生的机械应力。接着进行减 薄抛光,形成 PPLN 薄膜。最后利用精密划片机制 备所需尺寸的 PPLN 脊形波导^[25],波导的走向为*X* 方向。



Fig. 4 Preparation process of PPLN ridge waveguide

这里对 PPLN 薄膜脊形波导的制作工艺进行了改 进。例如,选择二氧化硅薄膜的厚度为 600 nm,而不 是常规的 2 µm 或者 4.7 µm 等。2011年,日本 NTT 公 司在 Z 切的 500 µm 厚的 Mg:LN上溅射一层 0.5 µm 厚的二氧化硅,并贴在 0.5 mm 厚的同成分铌酸锂 (CLN)基底上,通过机械化学抛光(CMP)方法制作了 4 µm 厚的 LN 薄膜,最后测得该脊形波导的传输损耗 小于 0.5 dB·cm⁻¹@1550 nm^[26]。2016年, Chauvet 等^[27] 使用 300 nm 厚的二氧化硅缓冲层,制作了脊高和脊宽 都为 10 µm 的 PPLN 脊形波导,测得波导的传输损耗 大约为 0.8 dB·cm⁻¹。可见 600 nm 厚的二氧化硅缓冲 层不会引起较大的波导损耗。

这里使用的PPLN脊形波导的截面尺寸为10 µm× 10 µm,长度为20 mm,使用芯径为8.5 µm、数字孔径 (NA)为0.125、模场直径为10.1 µm的单模保偏光纤 进行端面直接耦合,封装制作的器件如图5所示。



图 5 硅基 PPLN 薄膜脊形波导器件 Fig. 5 Silicon-based PPLN thin film ridge waveguide device

光纤进光纤出 PPLN 波导封装模块的测试装 置图如图 6 所示,使用可调谐激光光源将波长调整 到 1560 nm,激光经过窄带掺铒光纤放大器 (EDFA)后被输入到 PPLN 脊形波导中,在输出端 加一个 1560 nm 高反、780 nm 高透的滤光片,最后激 光进入光功率计。因为 PPLN 的折射率是温度的函 数,因此需要对晶体进行控温。这里使用温控器(精 度为 0.01 ℃,控温范围是室温到 200 ℃)对 PPLN 波 导封装模块进行控温。如图 7 (a)所示,当温度为 24.8 ℃时,模块的输出光波长为 780 nm(实验中使用 的光谱仪偏差为 0.2 nm)。当光纤放大器输出的泵 浦功率 (P_p^{in}) 最大为 1.6 W时,扣除输入端光纤与波 导的耦合损耗后,输入泵浦功率 (P_p^{0}) 为 1.2 W,波导 输出端的耦合泵浦功率 $(P_p^{1}, \mathcal{T} SHG)$ 为 0.9 W,此时 输出倍频光的功率是 653 mW [图 7(b)],光光转换 效率为 54.4% (P_p^{0}) 到 SHG 功率),归一化转换效率为 20.2%·W⁻¹·cm⁻² (P_p^{1}) 到 SHG 功率)。扣除输入端光 纤与波导的耦合损耗后,基于输入泵浦功率 P_p^{0} ,计算 得到波导的光光转换效率为 72.5%,略低于文献[28] 的光光转换效率 (73%)。



图 6 实验装置图 Fig. 6 Schematic of experimental setup



图 7 硅基 PPLN 薄膜脊形波导器件的测试结果。(a)光纤进光纤出 PPLN 波导封装模块的输出光谱图;(b)PPLN 波导封装模块的输出功率与输入功率之间的关系

Fig. 7 Test results of silicon-based PPLN thin-film ridge waveguide device. (a) Output spectrum of fiber-in fiber-out PPLN waveguide package module; (b) relationship between output and input power of PPLN waveguide package module

4 结 论

模拟分析了 25 ℃下脊高或者脊宽为 10 μm 的 PPLN脊形波导的 QPM 周期与对应波导脊宽和脊高 之间的关系。结果显示,在相同泵浦波长及相同波导 脊高和脊宽下,PPLN波导的 QPM 周期随着波导脊高 或者脊宽的增加而增大,最后趋于常数(即块状晶体的 周期)。分析了在波导脊高和脊宽不变的情况下,同一 基频波长时 PPLN 脊形波导的 QPM 周期与温度之间 的关系,发现随着温度的增加,QPM 周期逐渐减小,并 且温度每升高1℃,QPM 周期减小约3 nm。对 PPLN 薄膜脊形波导的制作工艺进行了改进,制作了紧凑型 光纤进光纤出的波导封装模块,并测试了其性能。当 温度为 24.8℃,1560 nm 基频光的输入功率为 1.2 W 时,最大输出 653 mW 的倍频光,光光转换效率达 54.4%,归一化转换效率为 20.2%·W⁻¹·cm⁻²。

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Study of Fiber-Coupled Periodically Poled Lithium Niobate Thin Film Waveguide Devices

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Abstract

Objective Periodically poled lithium niobate (PPLN) is an excellent nonlinear crystal for laser wavelength conversion. Conventional nonlinear crystals typically require high peak pulse power input. However, because of its periodic non-critical phase matching characteristics, PPLN has high conversion efficiency. It is extremely suitable for continuous wave (CW) laser wavelength conversion and widely used in CW laser systems. In addition, PPLN can realize full-color laser output by flexibly designing its quasi-phase matching (QPM) period, which has strong practical value. There has recently been a gradual shift from bulk PPLN to PPLN thin-film optical waveguides to improve the nonlinear frequency conversion efficiency of CW lasers. In recent years, many domestic research institutions, such as Nanjing University, East China Normal University, Shandong University, and the Chinese Academy of Sciences, have conducted in-depth detailed research on the preparation and application of PPLN thin film optical waveguide devices. Nonlinear frequency conversion devices based on PPLN waveguides have been used in various applications, such as optical communication, quantum optics, microwave optics, and spectroscopy. As applications continue to grow, new requirements are set for the volume and portability of waveguides. This study briefly introduces the basic structure and principle of a silicon-based PPLN thin film ridge waveguide, and a commercially available compact fiber-in-fiber-out PPLN waveguide package module is designed and fabricated.

Methods The fabrication process of the silicon-based PPLN thin-film ridge waveguide is as follows. First, a Z-cut lithium niobate wafer (0.5 mm thick) doped with MgO is poled at high voltage. According to the FDTD software analysis results, the poled period is chosen to be 18.7 µm to obtain phase matching of the pump wavelength near 1560 nm. After poling, a silicon dioxide buffer layer with

a thickness of approximately 600 nm is deposited on one side of the PPLN wafer, subsequently, a gold layer of approximately 300 nm thickness is sputtered. Then, another 0.5-mm thick precision polished silicon wafer is coated with a layer of gold of approximately 300 nm thickness and bonded to the PPLN wafer. This process is realized at room temperature, avoiding mechanical stress caused by the different thermal expansion coefficients of both wafers. Next, thinning and polishing are conducted to form the PPLN film. Finally, the PPLN ridge waveguide with the desired size is prepared based on a precision cutting mechanism. The waveguide direction is X direction (Fig. 4). The PPLN ridge waveguide prepared herein has a cross section of 10 μ m × 10 μ m and a length of 20 mm. A single-mode polarization-holding fiber with a core diameter of 8.5 μ m, numerical aperture (NA) of 0.125, and mode field diameter of 10.1 μ m is used for end-face direct coupling, and the packaged device is shown in Fig. 5.

Results and Discussions A tunable laser source is used to tune the wavelength to 1560 nm. Subsequently it is incident into the PPLN ridge waveguide through a narrow-band erbium-doped fiber amplifier (EDFA). The light at the output of the waveguide passes through a 1560-nm high reflection and 780-nm high transmission filter and enters the optical power meter (Fig. 6). Because the refractive index of PPLN is a function of temperature, it is necessary to control the crystal temperature. Here, a temperature controller (the accuracy is 0.01 °C, temperature control range is from room temperature to 200 °C) is used to control the temperature of the PPLN waveguide package module. As shown in Fig. 7(a), when the temperature is 24.8 °C, the output wavelength of the module is 780 nm (the deviation of the spectrometer used in the experiment is 0.2 nm). When the pump power P_p^{in} (shown in Fig. 6) at the output of EDFA reaches 1.6 W, the input pump power P_p^{0} is calculated to be 1.2 W after deducting coupling loss between the fiber and waveguide is 0.9 W. The power of SHG is 653 mW [Fig. 7(b)], the optical-optical conversion efficiency is 54.4% (P_p^{0} to SHG power). The normalized conversion efficiency is $20.2\% \cdot W^{-1} \cdot cm^{-2} (P_p^{1}$ to SHG power). According to the input pump power P_p^{0} , after deducting the coupling loss between the input fiber and the waveguide, the optical-optical conversion efficiency of the waveguide is 72.5%.

Conclusions This study simulate and analyze the relationship between the QPM period of the PPLN ridge waveguide with a ridge height or width of 10 μ m at 25 °C and the corresponding ridge width or height. The QPM period of the PPLN waveguide increases with the increase of ridge height or width at the same pump wavelength and ridge height or width and finally tends to a constant, that is, the period of bulk PPLN crystal. The relationship between the QPM period and temperature of the PPLN ridge waveguide with constant ridge height and width at the same pump wavelength is analyzed. The QPM period decreases gradually with the increase in temperature, and the QPM period decreases by approximately 3 nm when the temperature rises by 1 °C. Here, the fabrication process of the PPLN thin-film ridge waveguide is improved. For example, the thickness of the silicon dioxide buffer layer is 600 nm. The waveguide package module with compact fiber in and out is fabricated, and its performance tested. When the temperature is 24.8 °C and the input power of 1560 nm pump light is 1.2 W, the maximum power of SHG is 653 mW, the optical-optical conversion efficiency is 54.4%, and the normalized conversion efficiency is 20.2%·W⁻¹·cm⁻².

Key words nonlinear optics; periodically poled lithium niobate; thin films; quasi-phase matching; ridge waveguide